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The PMB series of SIL DC/DC regulators (POL) are intended to be used as local distributed power sources in distributed power architecture level 4. The single in-line design makes the PMB series suitable for applications where boardspace is limited. The high efficiency and high reliability of the PMB series makes them particularly suited for the communications equipment of today and tomorrow.

DC/DC regulator Input 3.0 - 5.5 V Output 16 A



Key Features

- Wide input, 3.0-5.5 Vdc
- Programmable output, 0.75 3.6 Vdc
- Under voltage protection
- · Short circuit protection
- · Remote sense
- Remote On/Off
- Design for Environment (DfE)
- European Commission Directive 2011/65/EU (RoHs) compliant

These products are manufactured using the most advanced technologies and materials to comply with environmental requirements. Designed to meet high reliability requirements of systems manufacturers, the PMB responds to world-class specifications.

Flex is an ISO 9001/14001 certified supplier.



Product Program

VI	V _O /I _O max Output 1	P _O max	Ordering No.	Comment
3.0-5.5 V*	0.8-3.6 V/16 A	52.8 W	PMB 4518T WP	Standard version

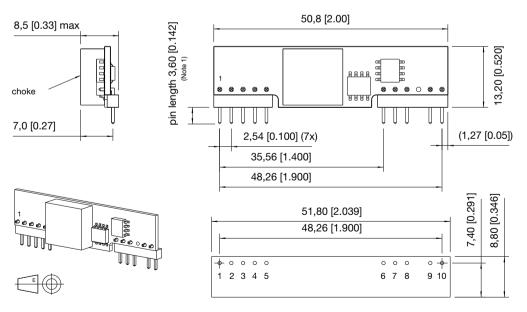
Option	Suffix	Example
Positive Remote Control logic	Р	PMB 4518T WPP

^{*} Input voltage limited to 3.8-5.5V for 3.3 Vout and for output voltages of 3.3V and 4.5-5.5 for output voltages above 3.3V.

Ordering Information

Delivery option	M.o.q.	Suffix	Example
Tray	100 pcs	/B	PMB 4xxxT WP /B

Mechanical Data



Dimensions in mm [inch]

Tolerances (unless specified): x,x +/-0,5 [0.02] x,xx +/-0,25 [0.01]

Recommended footprint (customer board), no components within border. Holes: \varnothing 1,0 [0.04] through plated holes with \varnothing 1,5 [0.06] pads on both sides.

Note 1: For other pin lengths, refer to Product program/Ordering information

Connections

Pin	Designation	Function
1	+ Out	Positive output
2	+ Out	Positive output
3	+ Sense	Positive sense
4	+ Out	Positive output
5	GND	Ground
6	GND	Ground
7	+ In	Positive input
8	+ In	Positive input
9	Vadj	Output voltage adjust
10	RC	Remote control

Weight

7.7g

Pins

Material: Copper alloy Plating: Matte tin over nickel

Absolute Maximum Ratings

Characte	Characteristics			max	Unit
T _{ref}	Operating Reference Temperature, see pg. 27	-45		+115	°C
T _S	Storage temperature	-55		+125	°C
VI	Input voltage	-0.3		+5.5	Vdc

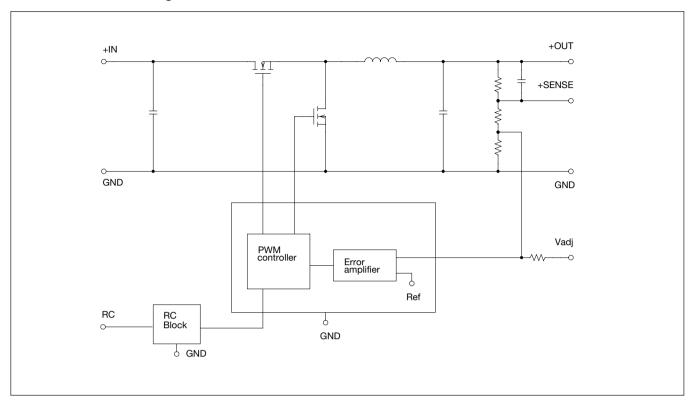
Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

 $Input \qquad \begin{array}{l} T_{ref} = -30 \; ... \; +90 \; ^{\circ}\text{C}, \, V_I = 3.0...5.5 \; V \; unless \; otherwise \; specified \\ Typ \; values \; specified \; at: \; T_{ref} = +25 \; ^{\circ}\text{C}, \, V_I nom, \, I_o max = 16 \; A \end{array}$

Characteristics		Conditions	min	typ	max	Unit	
VI	Input voltage range		3.0		5.5	Vdc	
	Time off input valtage	Ramp from higher voltage, V _{out} = 1.0- 2.5 V, V _{in} = 3.3 V		2.2		\/da	
V _{loff}	Turn-off input voltage	Ramp from higher voltage, $V_{out} = 3.3 \text{ V}$, $V_{in} = 5.0 \text{ V}$		3.4		Vdc	
V	Turn on input voltage	Ramp from lower voltage, V_{out} = 1.0- 2.5 V, V_{in} = 3.3 V		2.7		Vdc	
V _{lon}	Turn-on input voltage	Ramp from lower voltage, $V_{out} = 3.3 \text{ V}$, $V_{in} = 5.0 \text{ V}$		3.5		vac	
CI	Input capacitance			20		μF	
P _{li}	Input idling power	I _o = 0 A, V _I = 5 V			680	mW	
P _{RC}	Input stand-by power (RC active)	Non operation, V _I = 5 V			7.5	mW	
V _l ac	Input ripple 1)	20 Hz 5 MHz, I _o max, V _I = 5 V		400		mV	

¹⁾ Measured with $2 \times 22 \mu F$ ceramic capacitors

Fundamental Circuit Diagram



Product Qualification Specification

Characteristics			
Random Vibration	IEC 60068-2-64	Frequency Acceleration density	5 500 Hz 0.5 g ² /Hz
Mechanical shock (half sinus)	IEC 60068-2-27	Peak acceleration Duration	50 g 11 ms
Lead integrity	IEC 60068-2-21 Ub	Simultaneous bending	All leads
Temperature cycling	JESD22-A104-B _G	Temperature Number of cycles	-40 +125 °C 300
Accelerated damp heat	JESD22-A101-B	Temperature Humidity Duration Bias	+85 °C 85 % RH 1000 hours max input voltage
Solderability	IEC 60068-2-54 (Aged according to JESD22-A101- B, 240h no bias)	Solder immersion depth Time for onset of wetting Wetting force	2 mm < 2.5 s > 200 mN/m
Cold (in operation)	IEC 60068-2-1	Temperature Duration	-45 ℃ 72 h
High temperature storage	JESD22-A103-B _A	Temperature Duration	+125 °C 1000 h

Safety Specification

General information.

Flex DC/DC converters and DC/DC regulators are designed in accordance with safety standards

IEC/EN/UL 60 950, Safety of Information Technology Equipment.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 "Safety of information technology equipment".

There are other more product related standards, e.g. IEC61204-7 "Safety standard for power supplies", IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Flex DC/DC converters and DC/DC regulators are UL 60 950 recognized and certified in accordance with EN 60 950.

The flammability rating for all construction parts of the products meets UL 94V-0.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL 60 950.

Isolated DC/DC converters.

It is recommended that a fast blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.

In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage ($V_{\rm ISO}$) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification). Leakage current is less than 1µA at nominal input voltage.

24 V dc systems.

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V dc systems.

If the input voltage to Flex DC/DC converter is 75 V dc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 V dc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL 60 950.

Non-isolated DC/DC regulators.

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC regulator.

Adjusted to 1.0 V out - Data

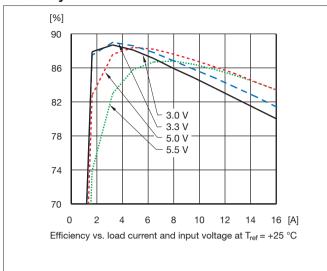
 T_{ref} = -30 ... +90 °C, V_{I} = 3.0 ... 5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{I} nom. I_{O} max = 16 A. Note: +Sense connected to +Out

		0 111		Output		
Chara	cteristics	Conditions	min	typ	max	Unit
V _{Oi}	Output voltage adjusted setting	T _{ref} = +25 °C, V _I nom, I _O max, R _{adj} 80 kΩ	0.98	1.00	1.02	V
	Output voltage tolerance band	I _O = 0.011.0 x I _O max	0.97		1.03	V
.,	Idling voltage	I _O = 0	0.98		1.02	V
Vo	Line regulation	V _I min V _I max, I _O max		11		mV
	Load regulation	I _O = 0.011.0 x I _O max, V _I nom		10		mV
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I _O max, dl/dt = 5 A/µs, C _O = 150 µF,		100		mV
t _{tr}	Load transient recovery time	V _I = 3.3 V		60		μs
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90 °C, I _O max	-0.1	0	+0.1	mV/°C
t _s	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{OI}$, $I_O = 0.1 \dots 1.0 \text{ x } I_O \text{max}, V_I \text{nom}$		8.5		ms
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I nom		4		ms
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I nom		5		s
t _{RC} off	RC shut-down time to V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _{RC} on	RC start-up time to V _O x 0.9	$I_O = I_O max, V_I nom$		8		ms
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I nom		10		s
lo	Output current		0		16	А
P _O max	Max output power	At V _O = V _{Onom}	16			W
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	28	35	А
V _{Oac}	Output ripple	20 Hz 5 MHz, I _O max		10	20	mV _{p-p}
η	Efficiency - 50% load	$I_0 = 0.5 \times I_0 \text{max}, V_1 = 5 \text{ V}$		87.2		%
η	Efficiency - 100% load	$I_O = I_O max$, $V_I = 5 V$	81	83.4		%
P _d	Power Dissipation	$I_O = I_O max$, $V_I = 5 V$		3.1	3.8	W
Fo	Switching frequency	I _O = 0 1.0 x I _O max	250	300	350	kHz
I _{sense}	Remote sense current				8	mA
II	Static input current	V _I = 3.0 V, I _O = I _O max, T _{ref} = 25 °C		6.7		А
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours

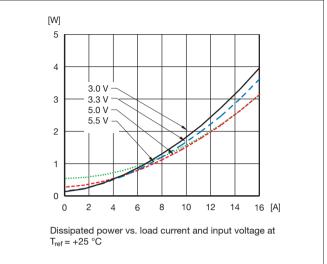
Adjusted to 1.0 V out - Typical Characteristics

General conditions: Input filter 2 x 22 μF, Output filter 1 x 150 μF

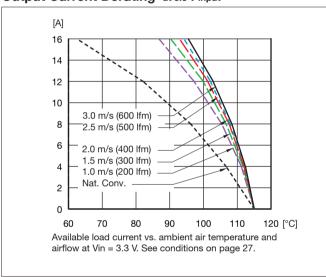
Efficiency



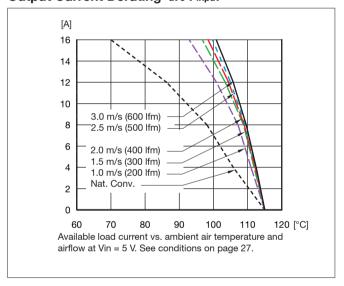
Power Dissipation



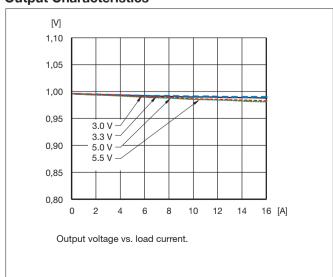
Output Current Derating at 3.3 V input

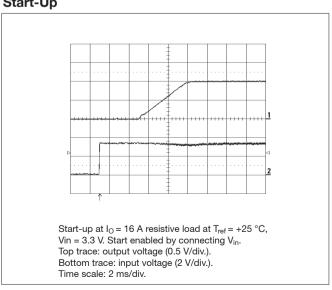


Output Current Derating at 5 V input



Output Characteristics

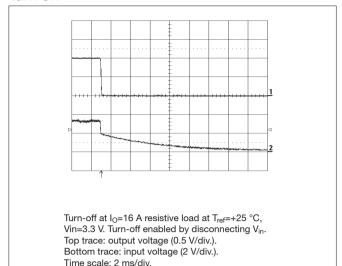




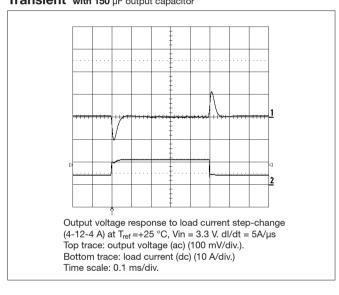
Adjusted to 1.0 V out - Typical Characteristics

General conditions: Input filter 2 x 22 μ F, Output filter 1 x 150 μ F

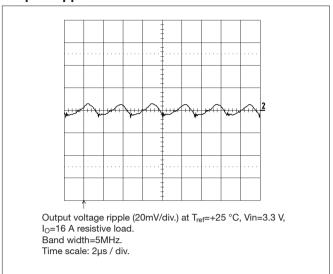
Turn Off



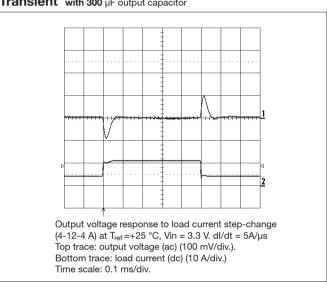
Transient with 150 µF output capacitor



Output Ripple



Transient with 300 µF output capacitor



Adjusted to 1.2 V out - Data

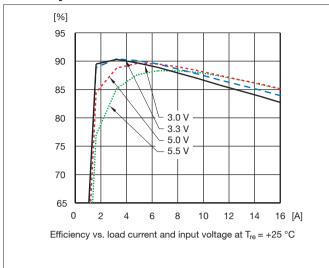
 T_{ref} = -30 ... +90 °C, V_{I} = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{I} nom, I_{O} max = 16 A. Note: +Sense connected to +Out

O.			Output			
Chara	cteristics	Conditions	min	typ	max	Unit
V _{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, V_{I} nom, I_{O} max, R_{adj} 42 $k\Omega$	1.176	1.20	1.224	V
	Output voltage tolerance band	I _O = 0.011.0 x I _O max	1.164		1.236	V
.,	Idling voltage	I _O = 0	1.18		1.22	V
Vo	Line regulation	V _I min V _I max, I _O max		11		mV
	Load regulation	I _O = 0.011.0 x I _O max, V _I nom		10		mV
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I _O max, dl/dt = 5 A/µs, C _O = 150 µF,		120		mV
t _{tr}	Load transient recovery time	V _I = 3.3 V		60		μs
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90°C, I _O max	-0.1	0	+0.1	mV/°C
ts	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{OI}$, $I_O = 0.1 \dots 1.0 \text{ x } I_O \text{max}, V_I \text{nom}$		8.5		ms
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I nom		4		ms
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I nom		5		s
t _{RC} off	RC shut-down time to V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _{RC} on	RC start-up time to V _O x 0.9	$I_{O} = I_{O}$ max, V_{I} nom		8		ms
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I nom		5		s
lo	Output current		0		16	А
P _O max	Max output power	At V _O = V _{Onom}	19.2			W
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	28	35	А
V _{Oac}	Output ripple	20 Hz 5 MHz, I _O max		10	20	mV _{p-p}
η	Efficiency - 50% load	$I_0 = 0.5 \text{ x } I_0 \text{max}, V_1 = 5 \text{ V}$		89		%
η	Efficiency - 100% load	$I_0 = I_0 max$, $V_1 = 5 V$	82.7	85.1		%
P _d	Power Dissipation	$I_0 = I_0 max$, $V_1 = 5 V$		3.3	4.0	W
Fo	Switching frequency	I _O = 0 1.0 x I _O max	260	300	340	kHz
I _{sense}	Remote sense current				8	mA
II	Static input current	V _I = 3.0 V, I _O = I _O max, T _{ref} = 25 °C		7.8		А
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours

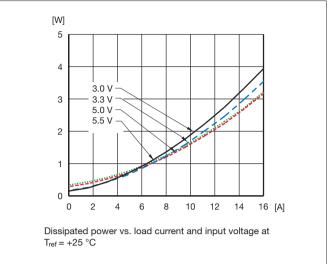
Adjusted to 1.2 V - Typical Characteristics

General conditions: Input filter 2 x 22 μF, Output filter 1 x 150 μF

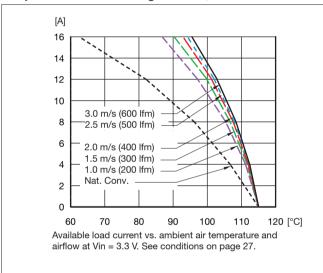
Efficiency



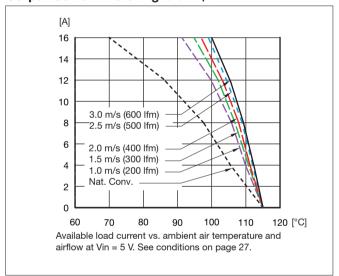
Power Dissipation



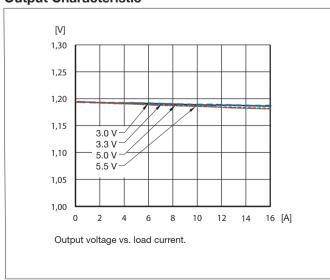
Output Current Derating at 3.3 V input

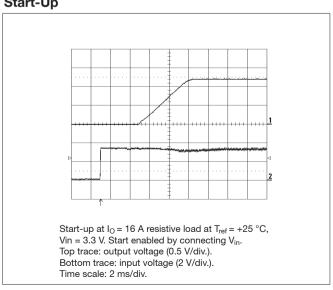


Output Current Derating at 5 V input



Output Characteristic

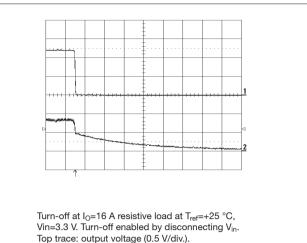




Adjusted to 1.2 V - Typical Characteristics

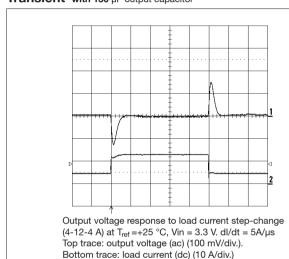
General conditions: Input filter 2 x 22 μ F, Output filter 1 x 150 μ F

Turn Off



Transient with 150 µF output capacitor

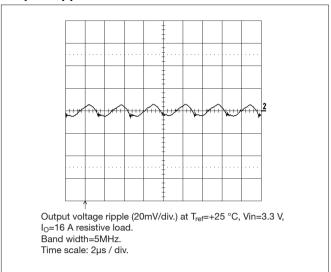
Time scale: 2 ms/div.



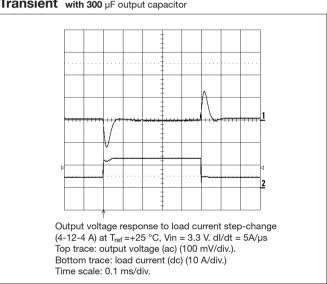
Time scale: 0.1 ms/div.

Bottom trace: input voltage (2 V/div.).

Output Ripple



Transient with 300 µF output capacitor



Adjusted to 1.5 V out - Data

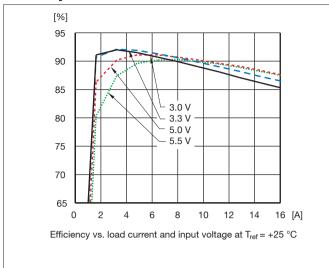
 T_{ref} = -30 ... +90 °C, V_{I} = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{I} nom. I_{O} max = 16 A. Note: +Sense connected to +Out

O.		0 111	Output			
Chara	cteristics	Conditions	min	typ	max	Unit
V _{Oi}	Output voltage adjusted setting	T _{ref} = +25 °C, V _I nom, I _O max, R _{adj} 23 kΩ	1.47	1.5	1.53	V
	Output voltage tolerance band	I _O = 0.011.0 x I _O max	1.455		1.545	V
.,	Idling voltage	I _O = 0	1.48		1.52	V
Vo	Line regulation	V _I min V _I max, I _O max		11		mV
	Load regulation	I _O = 0.011.0 x I _O max, V _I nom		10		mV
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I _O max, dl/dt = 5 A/µs, C _O = 150 µF,		120		mV
t _{tr}	Load transient recovery time	V _I = 3.3 V		60		μs
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90 °C, I _O max	-0.1	0	+0.1	mV/°C
t _s	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{OI}$, $I_O = 0.1 \dots 1.0 \text{ x } I_O \text{max}, V_I \text{nom}$		8.5		ms
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I nom		4		ms
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I nom		5		s
t _{RC} off	RC shut-down time to V _O x 0.1	I _O = I _O max, V _I nom		0.2		ms
t _{RC} on	RC start-up time to V _O x 0.9	$I_{O} = I_{O}$ max, V_{I} nom		8		ms
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I nom		5		s
lo	Output current		0		16	А
P _O max	Max output power	At V _O = V _{Onom}	24			W
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	28	35	А
V_{Oac}	Output ripple	20Hz 5MHz, I _O max		10	20	mV _{p-p}
η	Efficiency - 50% load	$I_0 = 0.5 \text{ x } I_0 \text{max}, V_1 = 5 \text{ V}$		91		%
η	Efficiency - 100% load	$I_0 = I_0 max$, $V_1 = 5 V$	85.5	87.7		%
P _d	Power Dissipation	$I_O = I_O max$, $V_I = 5 V$		3.2	4.1	W
Fo	Switching frequency	I _O = 0 1.0 x I _O max	260	300	340	kHz
I _{sense}	Remote sense current				8	mA
II	Static input current	V _I = 3.0 V, I _O = I _O max, T _{ref} = 25 °C		9.5		А
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours

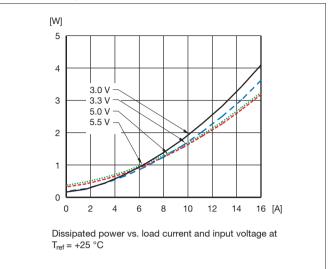
Adjusted to 1.5V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

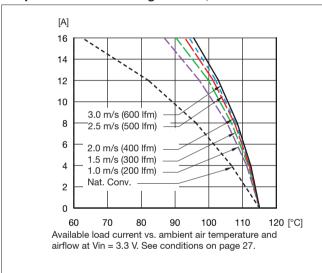
Efficiency



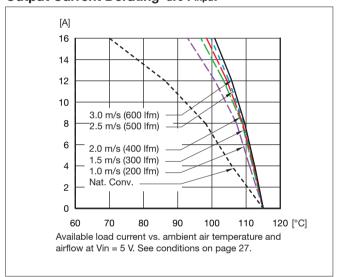
Power Dissipation



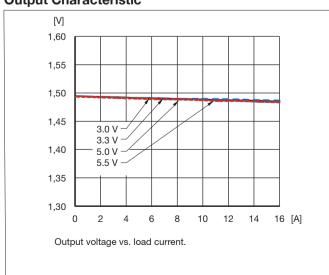
Output Current Derating at 3.3 V input

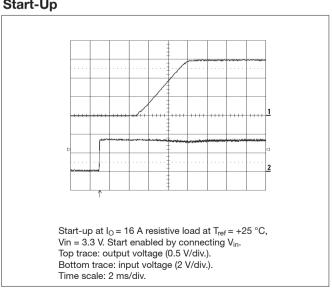


Output Current Derating at 5 V input



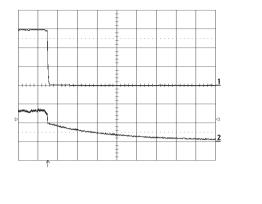
Output Characteristic





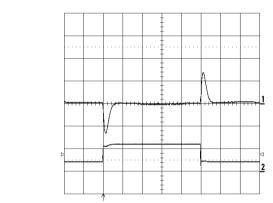
Adjusted to 1.5V out - Typical Characteristics General conditions: Input filter 2 x 22 μ F, Output filter 1 x 150 μ F

Turn Off



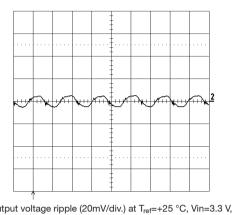
Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C, $Vin=3.3 V. Turn-off enabled by disconnecting <math>V_{in}$. Top trace: output voltage (0.5 V/div.). Bottom trace: input voltage (2 V/div.). Time scale: 2 ms/div.

Transient with 150 µF output capacitor



Output voltage response to load current step-change (4-12-4 A) at $T_{\rm ref}$ =+25 °C, Vin = 3.3 V. dl/dt = 5A/ μs Top trace: output voltage (ac) (100 mV/div.). Bottom trace: load current (dc) (10 A/div.) Time scale: 0.1 ms/div.

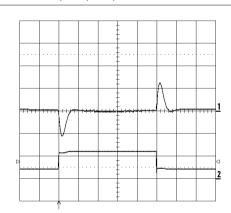
Output Ripple



Output voltage ripple (20mV/div.) at $T_{\rm ref}\!\!=\!\!+25~^{\circ}\rm{C}$, Vin=3.3 V, $I_{\rm O}\!\!=\!\!16$ A resistive load. Band width=5MHz.

Time scale: 2µs / div.

Transient with 300 µF output capacitor



Output voltage response to load current step-change (4-12-4 A) at T_{ref} =+25 °C, V_{ref} Vin = 3.3 V. V_{ref} dI/dt = V_{ref} Top trace: output voltage (ac) (100 mV/div.). Bottom trace: load current (dc) (10 A/div.) Time scale: 0.1 ms/div.

Adjusted to 1.8 V out - Data

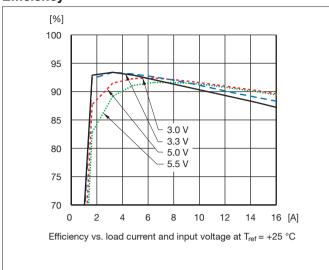
 T_{ref} = -30...+90 °C, V_{I} = 3.0 ... 5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{I} nom. I_{O} max = 16 A. Note: +Sense connected to +Out

				Output		11-7	
Chara	cteristics	Conditions	min	typ	max	Unit	
V _{Oi}	Output voltage adjusted setting	T _{ref} = +25 °C, V _I nom, I _O max, R _{adj} 15 kΩ	1.764	1.80	1.836	V	
	Output voltage tolerance band	I _O = 0.011.0 x I _O max	1.746		1.854	V	
,,	Idling voltage	I _O = 0	1.78		1.82	V	
Vo	Line regulation	V _I min V _I max, I _O max		11		mV	
	Load regulation	I _O = 0.011.0 x I _O max, V _I nom		10		mV	
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I _O max, dl/dt = 5 A/µs, C _O = 150 µF,		110		mV	
t _{tr}	Load transient recovery time	V _I = 3.3 V		60		μs	
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90 °C, I _O max	-0.1	0	+0.1	mV/°C	
t _s	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{OI_j}$ $I_O = 0.1 \dots 1.0 \text{ x } I_{O} \text{max}, V_I \text{nom}$		8.5		ms	
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I nom		4		ms	
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms	
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I nom		5		s	
t _{RC} off	RC shut-down time to V _O x 0.1	$I_{O} = I_{O}$ max, V_{I} nom		0.2		ms	
t _{RC} on	RC start-up time to V _O x 0.9	$I_{O} = I_{O}$ max, V_{I} nom		8		ms	
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I nom		5		s	
Io	Output current		0		16	A	
P _O max	Max output power	At V _O = V _{Onom}	28.8			W	
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	28	35	A	
V _{Oac}	Output ripple	20 Hz 5 MHz, I _O max		14	24	mV _{p-p}	
η	Efficiency - 50% load	$I_0 = 0.5 \times I_0 \text{max}, V_1 = 5 \text{ V}$		92		%	
η	Efficiency - 100% load	$I_0 = I_0 max$, $V_1 = 5 V$	88.5	89.8		%	
P _d	Power Dissipation	$I_0 = I_0 max$, $V_1 = 5 V$		3.3	3.8	W	
Fo	Switching frequency	I _O = 0 1.0 x I _O max	260	300	340	kHz	
I _{sense}	Remote sense current				8	mA	
I _I	Static input current	V _I = 3.0 V, I _O = I _O max, T _{ref} = 25 °C		10.9		А	
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours	

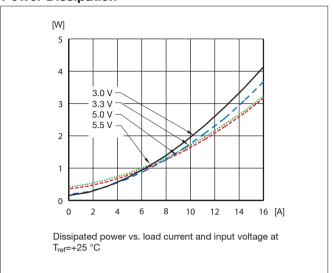
Adjusted to 1.8 V - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

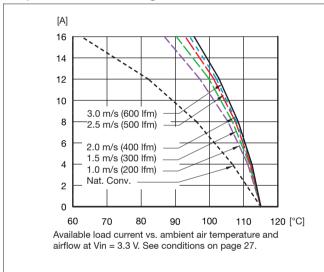
Efficiency



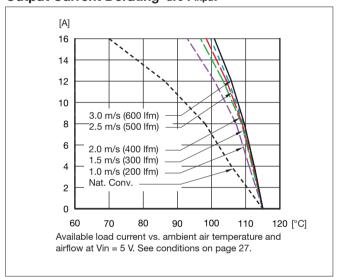
Power Dissipation



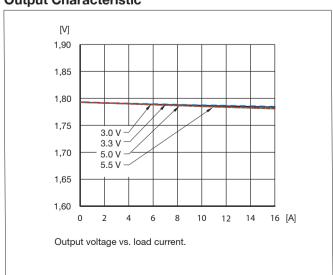
Output Current Derating at 3.3 V input

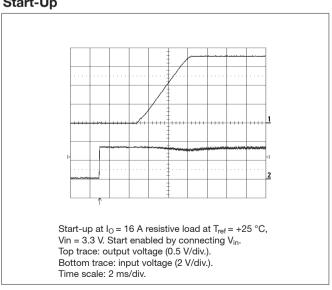


Output Current Derating at 5 V input



Output Characteristic

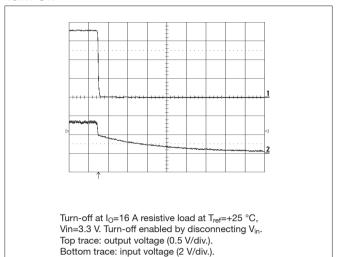




Adjusted to 1.8 V out - Typical Characteristics

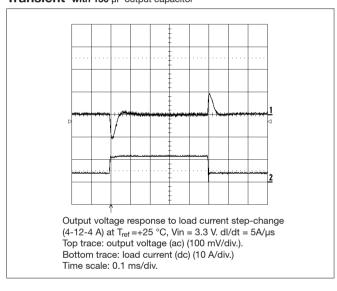
General conditions: Input filter 2 x 22 μ F, Output filter 1 x 150 μ F

Turn Off

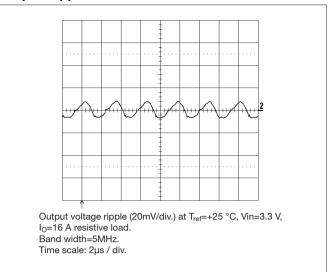


Transient with 150 µF output capacitor

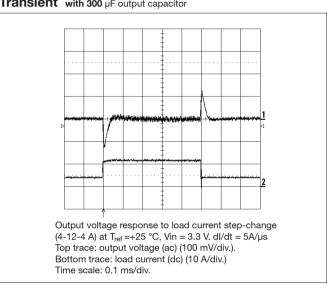
Time scale: 2 ms/div.



Output Ripple



Transient with 300 µF output capacitor



Adjusted to 2.5 V out - Data

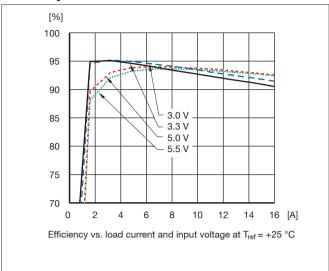
 T_{ref} = -30 ... +90 °C, V_{l} = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{l} nom. I_{O} max = 16 A. Note: +Sense connected to +Out

			Output			
Characteristics		Conditions	min	typ	max	Unit
V_{Oi}	Output voltage adjusted setting	T _{ref} = +25 °C, V _I nom, I _O max, R _{adj} 7 kΩ	2.45	2.5	2.55	V
	Output voltage tolerance band	I _O = 0.011.0 x I _O max	2.425		2.575	V
Vo	Idling voltage	I _O = 0	2.48		2.52	V
	Line regulation	V _I min V _I max, I _O max		11		mV
	Load regulation	I _O = 0.011.0 x I _O max, V _I nom		10		mV
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I_{O} max, dl/dt = 5 A/ μ s, C_{O} = 150 μ F, V_{I} = 3.3 V		160		mV
t _{tr}	Load transient recovery time			60		μs
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90 °C, I _O max	-0.1	0	+0.1	mV/°C
ts	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{OI}$, $I_O = 0.1 \dots 1.0 \text{ x } I_O \text{max}, V_I \text{nom}$		8.5		ms
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I nom		4		ms
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I nom		5		s
t _{RC} off	RC shut-down time to V _O x 0.1	$I_0 = I_0 max, V_1 nom$		0.2		ms
t _{RC} on	RC start-up time to V ₀ x 0.9	$I_0 = I_0 max, V_1 nom$		8		ms
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I nom		5		s
lo	Output current		0		16	А
P _O max	Max output power	At V _O = V _{Onom}	40			W
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	28	35	А
V _{Oac}	Output ripple	20 Hz 5 MHz, I _O max		20	35	mV _{p-p}
η	Efficiency - 50% load	I _O = 0.5 x I _O max, V _I = 5 V		94		%
η	Efficiency - 100% load	$I_0 = I_0 \text{max}, V_1 = 5 \text{ V}$	90	92		%
P _d	Power Dissipation	$I_O = I_O max$, $V_I = 5 V$		3.4	4.5	W
Fo	Switching frequency	I _O = 0 1.0 x I _O max	260	300	340	kHz
I _{sense}	Remote sense current				8	mA
I _I	Static input current	V _I = 3.0 V, I _O = I _O max, T _{ref} = 25 °C		15.1		А
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours

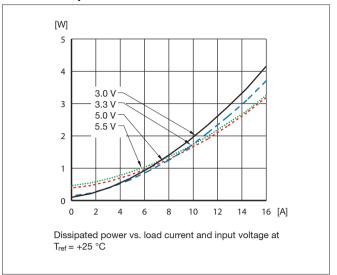
Adjusted to 2.5 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

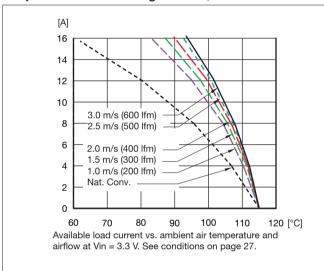
Efficiency



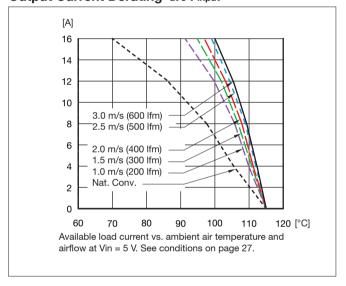
Power Dissipation



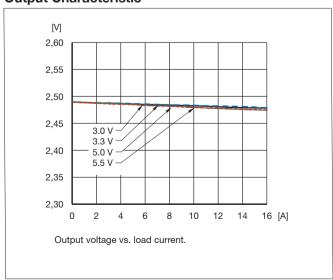
Output Current Derating at 3.3 V input

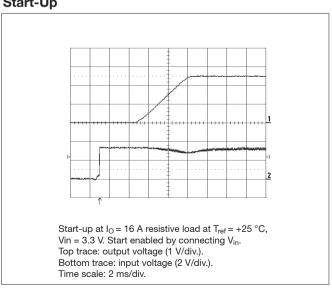


Output Current Derating at 5 V input



Output Characteristic

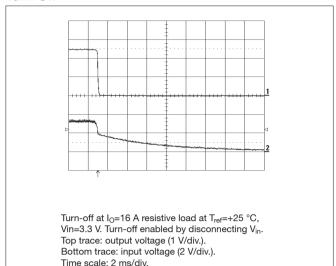


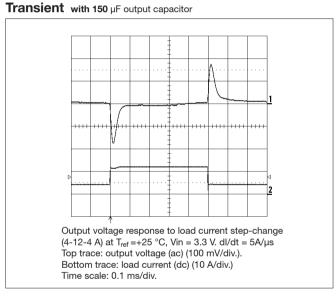


Adjusted to 2.5 V out - Typical Characteristics

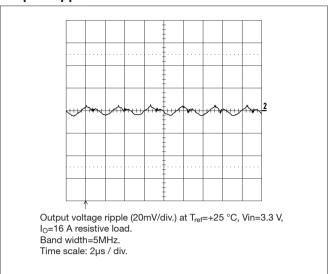
General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Turn Off

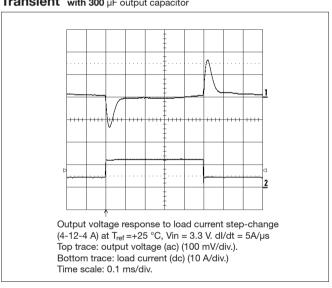




Output Ripple



Transient with 300 µF output capacitor



Adjusted to 3.3 V out - Data

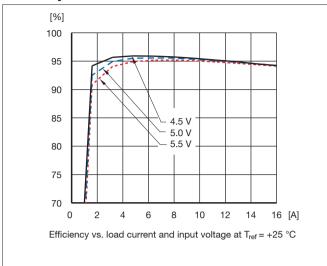
 T_{ref} = -30 ... +90 °C, V_{I} = 3.8 ... 5.5 V unless otherwise specified. Input filter 2 x 22 μ F, Output filter 1 x 150 μ F Typ values specified at: T_{ref} = +25 °C and V_{I} = 5.0 V. I_{O} max = 16 A. Note: +Sense connected to +Out

			Output			
Characteristics		Conditions	min	typ	max	Unit
V _{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, V_I > 3.8 V, I_{Omax} , R_{adj} 3.1 k Ω	3.234	3.3	3.366	V
	Output voltage tolerance band	I _O = 0.11.0 x I _O max	3.201		3.399	V
Vo	Idling voltage	I _O = 0	3.28		3.32	V
	Line regulation	V _I = 5 V V _I max, I _O max		11		mV
	Load regulation	I _O = 0.011.0 x I _O max, V _I = 5 V		20		mV
V _{tr}	Load transient voltage deviation	Load step = 0.25 0.75 x I_{O} max, dl/dt = 5 A/ μ s, C_{O} = 150 μ F, V_{I} = 5 V		120		mV
t _{tr}	Load transient recovery time			60		μs
T _{coeff}	Temperature coefficient	T _{ref} = -30 +90 °C, I _O max	-0.1	0	+0.1	mV/°C
t _s	Start-up time	From V_I connected to $V_O = 0.9 \text{ x } V_{Oadj}$, $I_O = 0.1 \dots 1.0 \text{ x } I_O max, V_I = 5 \text{ V}$		8.5		ms
t _r	Ramp-up time	0.10.9 x V _O , I _O = 0.11.0 x I _O max, V _I = 5 V		4		ms
t _r	Fall time, V _O x 0.1	$I_0 = I_0 max, V_1 = 5 V$		0.2		ms
t _r	Fall time, V _O x 0.1	I _O = 0 A, V _I = 5 V		5		s
t _{RC} off	RC shut-down time to V _O x 0.1	$I_0 = I_0 max$, $V_1 = 5 V$		0.2		ms
t _{RC} on	RC start-up time to V ₀ x 0.9	$I_0 = I_0 max, V_1 = 5 V$		8		ms
t _{RC}	RC fall time, V _O x 0.1 0.9	I _O = 0 A, V _I = 5 V		5		s
Io	Output current		0		16	А
P _O max	Max output power	At V _O = V _{Onom}	52.8			W
I _{lim}	Current limit threshold	T _{ref} < T _{ref} max	22	25	35	А
V _{Oac}	Output ripple	20 Hz 5 MHz, I _O max		25	40	mV _{p-p}
η	Efficiency - 50% load	I _O = 0.5 x I _O max, V _I = 5 V		95.4		%
η	Efficiency - 100% load	$I_O = I_O max$, $V_I = 5 V$	92	94		%
P _d	Power Dissipation	$I_O = I_O max$, $V_I = 5 V$		3.2	4.6	W
Fo	Switching frequency	I _O = 0 1.0 x I _O max	260	300	340	kHz
I _{sense}	Remote sense current				8	mA
II	Static input current	V _I = 3.8 V, I _O = I _O max, T _{ref} = 25 °C		15.0		А
MTBF	Predicted reliability	T _{ref} = 40 °C		6		million hours

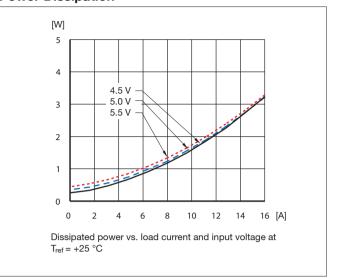
Adjusted to 3.3 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

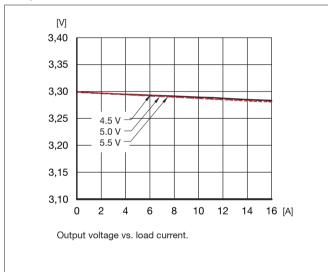
Efficiency



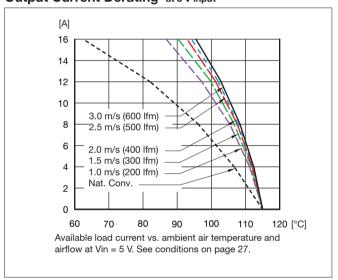
Power Dissipation

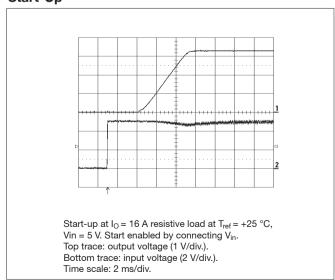


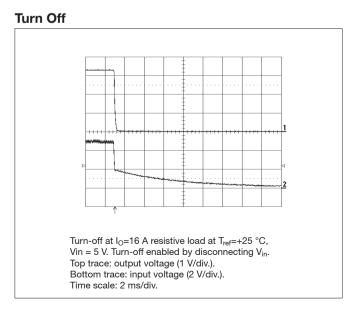
Output Characteristic



Output Current Derating at 5 V input



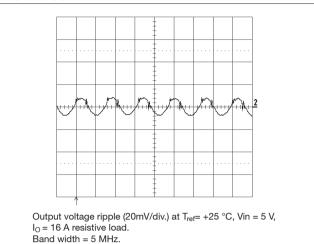




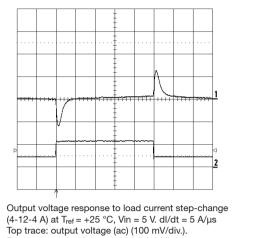
Adjusted to 3.3 V out - Typical Characteristics

General conditions: Input filter 2 x 22 μ F, Output filter 1 x 150 μ F

Output Ripple



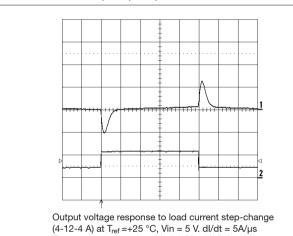
Transient with 150 µF output capacitor



Bottom trace: load current (dc) (10 A/div.) Time scale: 0.1 ms/div.

Transient with 300 µF output capacitor

Time scale: 2 µs / div.

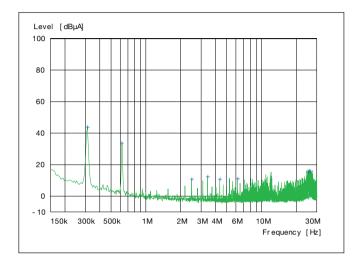


Top trace: output voltage (ac) (100 mV/div.). Bottom trace: load current (dc) (10 A/div.) Time scale: 0.1 ms/div.

EMC Specification

Layout Recommendation

The radiated EMI performance of the DC/DC regulator will be optimised by including a ground plane in the PCB area under the DC/DC regulator. This approach will return switching noise to ground as directly as possible, with improvements to both emission and susceptibility.



Operating Information

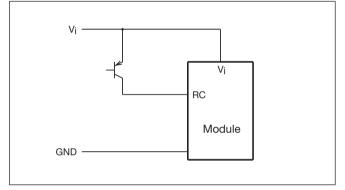
Remote Control (RC)

The RC pin may be used to turn on or turn off the regulator using a suitable open collector function.

Turn off is achieved by connecting the RC pin to the input voltage.

The regulator will run in normal operation when the RC pin is left open.

RC	Regulator condition	min	typ	max	Unit
High level referenced to GND	OFF	1.7		5.5	V
Open	ON				



Remote Sense

All PMB 4000 Series DC/DC regulators have a positive remote sense pin that can be used to compensate for moderate amounts of resistance in the distribution system and allow for voltage regulation at the load or other selected point. The remote sense line will carry very little current and does not need a large cross sectional area. However, the sense line on the PCB should be located close to a ground trace or ground plane. The remote sense circuitry will compensate for up to 10% voltage drop between the sense voltage and the voltage at the output pins from Vonom. If the remote sense is not needed the sense pin should be left open.

Operating Information

Output Voltage Adjust (Vadi)

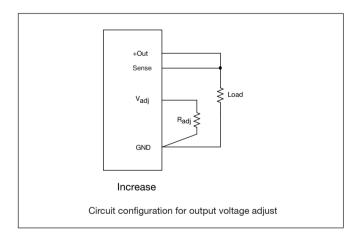
The output voltage can be set by means of an external resistor, connected to the V_{adj} pin. Nominal output voltage 0.75 V is set by leaving the V_{adj} pin open. Adjustment can only be made to increase the output voltage setting.

To increase

Connect a resistor between (Vadj) and (Gnd). The output voltage increases with decreasing resistor value as shown in the table below. Note that the maximum output voltage 3.63 V may not be exceeded.

 R_{ext} up (kohm) = (21.007 / (V_{O} - 0.75225)) - 5.1

Output Voltage (V)	Resistor (ohm)
0.75	Open
1.0	79.691 k
1.2	41.817 k
1.5	22.990 k
1.8	14.949 k
2.5	6.919 k
3.3	3.145 k



Current Limit Protection

The PMB 4000 Series DC/DC regulators include current limiting circuitry that allows them to withstand continuous overloads or short circuit conditions on the output. The current limit is of hick-up mode type.

The regulator will resume normal operation after removal of the overload. The load distribution system should be designed to carry the maximum output short circuit current specified.

Input And Output Impedance

The impedance of both the power source and the load will interact with the impedance of the DC/DC regulator. It is most important to have a low characteristic impedance, both at the input and output, as the regulators have a low energy storage capability. Use capacitors across the input if the source inductance is greater than 4.7 µH. Suitable input capacitors are 22 µF - 220 µF low ESR ceramics.

Minimum Required External Capacitors

External input capacitors are required to increase the lifetime of the internal capacitors and to further reduce the input ripple. A minimum of 2x22 µF external input capacitance with low ESR should be added.

A minimum of 150 μF external output capacitance, low ESR, should be added for the converter to operate properly at full load

Maximum Capacitive Load

When powering loads with significant dynamic current requirements, the voltage regulation at the load can be improved by addition of decoupling capacitance at the load. The most effective technique is to locate low ESR ceramic capacitors as close to the load as possible, using several capacitors to lower the total ESR. These ceramic capacitors will handle short duration high-frequency components of dynamic load changes. In addition, higher values of capacitors (electrolytic capacitors) should be used to handle the mid-frequency components. It is equally important to use good design practice when configuring the DC distribution system.

Low resistance and low inductance PCB layouts and cabling should be used. Remember that when using remote sensing, all resistance (including the ESR), inductance and capacitance of the distribution system is within the feedback loop of the regulator. This can affect on the regulators compensation and the resulting stability and dynamic response performance.

Very low ESR and high capacitance must be used with care. A "rule of thumb" is that the total capacitance must never exceed typically 500-700 μF if only low ESR (< 2 m Ω) ceramic capacitors are used. If more capacitance is needed, a combination of low ESR type and electrolytic capacitors should be used, otherwise the stability will be affected.

The PMB 4000 series regulator can accept up to 8 mF of capacitive load on the output at full load. This gives <500 $\mu\text{F/A}$ of I_O . When using that large capacitance it is important to consider the selection of output capacitors; the resulting behavior is a combination of the amount of capacitance and ESR.

Operating Information

A combination of low ESR and output capacitance exceeding 8 mF can cause the regulator into over current protection mode (hick-up) due to high start up current. The output filter must therefore be designed without exceeding the above stated capacitance levels if the ESR is lower than 30-40 m Ω .

Parallel Operation

The PMB 4000 Series DC/DC regulators can be connected in parallel with a common input. Paralleling is accomplished by connecting the output voltage pins directly and using a load sharing device on the input. Layout considerations should be made to avoid load imbalance. For more details on paralleling, please consult your local applications support.

Input Undervoltage Lockout

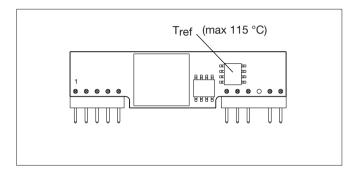
The PMB 4000 Series DC/DC regulators are equipped with a lockout function for low input voltage. When the input voltage is below the undervoltage lockout limit of the regulator it will shut off. When the input voltage increases above the lockout level the regulator will turn on.

Thermal Considerations

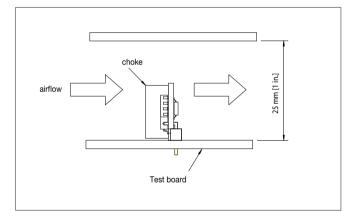
General

The PMB 4000 Series DC/DC regulators are designed to operate in a variety of thermal environments, however sufficient cooling should be provided to help ensure reliable operation. Heat is removed by conduction, convection and radiation to the surrounding environment. Increased airflow enhances the heat transfer via convection.

Proper cooling can be verified by measuring the temperature at the reference point (T_{ref}).



The PMB 4000 thermal testing is performed with the product mounted on an FR4 board 254 \times 254 mm with 8 layers of 35 μm copper. Airflow is perpendicular to the T_{ref} side.

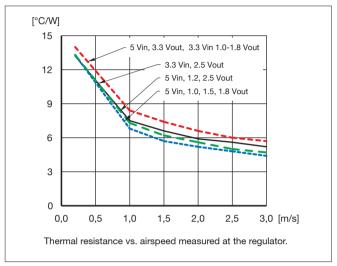


Calculation of ambient temperature

By using the thermal resistance the maximum allowed ambient temperature can be calculated.

A. The powerloss is calculated by using the formula $((1/\cdot) - 1) \times \text{output power} = \text{power losses}.$ $\eta = \text{efficiency of regulator. Example: } 95\% = 0.95$

B. Find the value of the thermal resistance $R_{th\ Tref-A}$ in the diagram by using the airflow speed at the module. Take the thermal resistance \times powerloss to get the temperature increase.



C. Max allowed calculated ambient temperature is: Max T_{ref} of DC/DC regulator - temperature increase.

Example: 5V input, 1.8 V output at 1m/s, full load:

A. $((1/0.9) - 1) \times 28.8 \text{ W} = 3.2 \text{ W}$

B. $3.2 \text{ W} \times 9 \text{ °C/W} = 28.8 \text{ °C}$

C. 90 °C - 28.8 °C = max ambient temperature is 61.2 °C

The real temperature will be dependent on several factors, like PCB size and type, direction of airflow, air turbulence etc. It is recommended to verify the temperature by testing.

Soldering Information

The PMB 4000 series DC/DC regulators are intended for manual or wave soldering. The plastic body of the pin connectors resists soldering heat for limited time up to 260 $^{\circ}$ C.

When hand soldering, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to avoid melting of the plastic.

Delivery Package Information

The PMB 4000 series regulators are delivered in antistatic trays with Jedec standard outer dimensions. Tray capacity 25 pcs.

Each box contains 4 trays.

Reliability

The Mean Time Between Failure (MTBF) of the PMB 4000 series DC/DC regulator family is calculated to be greater than 6 million hours at full output power and a reference temperature of +40 °C using TelCordia SR 332.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2011/65/EU and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Flex products are found in the Statement of Compliance document.

Flex fulfills and will continuously fulfill all its obligations under regulation (EC) No 1907/2006 concerning the registration, evaluation, authorization and restriction of chemicals (REACH) as they enter into force and is through product materials declarations preparing for the obligations to communicate information on substances in the products.

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