BMR316

4:1 Intermediate Bus Converter (1000W)

The BMR316 is a powerful and compact digital non-isolated, unregulated DC/DC converter designed to support Datacom and AI applications.

It can also be used for other high-power IBC requirements which have limited boardspace available.

The converter has a ratio conversion of 4:1 and provides 1000 W continuous power and has peak power capabilities of up to 2800 W.

This converter can deliver a power density up to 900 W/cm³ (15 kW/in³) when delivering peak power to the load.

BMR316 IBC, is a part of a portfolio with multiple options of VRM and PoL solutions to further convert the intermediate bus to downstream core voltages.

This product is supported by the Flex Power Designer tool.

Key features

- Horizontal mounting non-isolated DC/DC converter
- High power density IBC up to 900 W/cm³
- Ratio conversion 4:1, 1 kW continuously, 2.8 kW peak power
- Peak efficiency 97.7 %
- LGA industry standard footprint and pinout
- Optimized thermal design for cold wall mounting
- MTBF 7.43 million hours
- DMTBF of 2 million hours
- Meets safety requirements per IEC/EN/UL 62368-1
- PMBus configuration

Key electrical information

Parameter	Values
Input Voltage range	38-60 V
Output Voltage range	9.5-15 V
Output current	80 A
Output power	1000 W
Peak power	2800 W

Mechanical

23.4 x 17.8 x 7.65 mm

Soldering methods

- Pb free SMD reflow

Application areas

- Designed for Datacom and AI applications

Product options

The table below describes the different product options.

Example: BMR316	1	01	1	/021	Definitions
Product family	BMR316				
Mech. solution	1				1 = Baseplate, LGA
Sequence number		01			01 = Internal Power Good Pull up 02 = External Power Good Pull up 03 = Internal Power Good Pull up & no Zener Diodes on EN, PG and Alert pin
Function			1		1 = Stacked module
Configuration code				/021	021 = PMBus base address 0x6n, Table 1 022 = PMBus base address 0x1n, Table 2 023 = PMBus base address 0x1n, Table 1 *Note, see resistor tables in PMBus addressing section of the Design & Application Guidelines.*
Packaging options					C = Antistatic tape and reel package

For more information, please refer to Part 3 Mechanical information. If you do not find the variant you are looking for, please contact us at Flex Power Modules .

Order number examples

Part number	V_{in}	Output	Configuration
BMR3161011/021	38-60 V	9.5-15 V / 80 A / 1000 W	Baseplate / Internal Power Good Pull up / PMBus base address 0x6n, Table 1
BMR3161021/021	38-60 V	9.5-15 V / 80 A / 1000 W	Baseplate / External Power Good Pull up / PMBus base address 0x6n, Table 1
BMR3161011/022	38-60 V	9.5-15 V / 80 A / 1000 W	Baseplate / Internal Power Good Pull up / PMBus base address 0x1n, Table 2
BMR3161011/023	38-60 V	9.5-15 V / 80 A / 1000 W	Baseplate / Internal Power Good Pull up / PMBus base address 0x1n, Table 1
BMR3161031/023	38-60 V	9.5-15 V / 80 A / 1000 W	Baseplate / Internal Power Good Pull up & no Zener Diodes on EN, PG and Alert pin / PMBus base address 0x1n, Table 1

Part 1: Electrical specifications

Absolute maximum ratings

Stress in excess of our defined *absolute maximum ratings* may cause permanent damage to the converter. Absolute maximum ratings, also referred to as *non-destructive limits,* are normally tested with one parameter at a time exceeding the limits in the electrical specification.

Characteristics	min	max	Unit
Operating temperature (T_{P1})	-20	110	°C
Storage temperature	-40	125	°C
Input voltage (V_{in}) continious operation	-0.3	65	V
Input voltage transient	-0.3	64	V
C_{out}	0.1	6	mF
Signal I/O voltage (EN, PG, ALERT/SYNC, ADDR)	-0.3	3.7	V
SCL, SDA	-0.3	5.5	V

Reliability

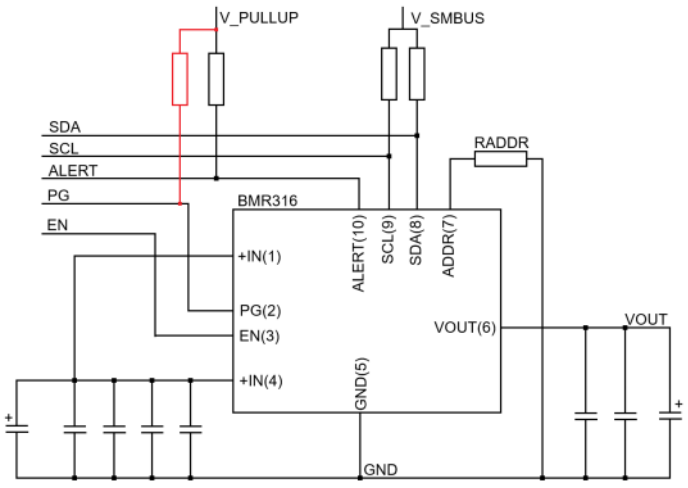
The failure rate (λ) and mean time between failures (MTBF= 1/λ) is calculated at max output power and an operating ambient temperature (T_A) of +40 °C. Flex Power Modules uses Telcordia SR-332 Issue 4 Method 1 to calculate the mean steady-state failure rate and standard deviation (σ). Telcordia SR-332 Issue 4 also provides techniques to estimate the upper confidence levels of failure rates based on the mean and standard deviation.

	Mean	90% confidence level	Unit
Steady-state failure rate (λ)	127	156	nfailures/h
Standard deviation (σ)	22.1		nfailures/h
MTBF	7.85	6.42	MHr

The BMR316 module has demonstrated a MTBF of 2x10^6 hours at 64A, 3 m/s airflow (1 inch heat sink) and 25°C ambient reference temperature with 90% confidence level.

Typical application diagram

Optional Pull-up resistor for PG is showed in red.
See product options.
Capacitor values are defined in the Electrical Specification tables. The EMI filter is defined in the EMC Part 2.



Electrical specifications for BMR316

13.5 V, 80 A (210 A peak) / 1000 W (2800 W peak)

Min and max values are valid for: T_{P1} = -20 to +110°C, V_{in} = 38 to 60 V, I_{out} = 80 A, unless otherwise specified under conditions. Typical values given at: T_{P1} = +25 °C, V_{in} = 54 V, max P_{out_TDP}, unless otherwise specified under conditions, see *Note 1*.

Additional external C_{in} = 2 x100 µF + 5 x 2.2 µF ceramic, C_{out} = 2 x 470 µF + 10 x 10 µF ceramic

Characteristic	conditions	minimum	typical	maximum	unit
Key features					
Efficiency (η)	Peak @ 40% P_{out_TDP}		97.7		%
	100 % of P_{out_TDP}		96.7		%
	50 % of P_{out_TDP}		97.6		%
P_{out_TDP} thermal design power (TDP)			1000		W
P_{out_MAX} peak power			2800		W
Power dissipation	40% of P_{out_TDP}		10.2		W
	100 % of P_{out_TDP}		36.6		W
	50 % of P_{out_TDP}		13.1		W
Switching frequency (f_s)	0-100 % of P_{out_TDP}		1000		kHz
Recommended capacitive load	Note 2	220	1000	6000	µF
Input characteristics					
Input voltage range (V_{in})		38		60	V
Input idling power	Vin=54V, Iout=0A, EN on		5.5		W
Input standby power	Vin=54V, Iout=0A, EN off		520		mW
Input OVP				65	V
Internal input capacitance	Nominal capacitance		18.8		µF
Recommended external input capacitance		100	470		µF

Note 1: Max peak power is ≤ 2800 W and continuous power (thermal design power TDP) is ≤ 1000 W depending on thermal conditions.

Note 2: Paralleling reduced max cap

Electrical specifications for **BMR316**

13.5 V, 80 A (210 A peak) / 1000 W (2800 W peak)

Min and max values are valid for: T_{P1} = -20 to +110 degC, V_{in} = 38 to 60 V, I_{out} = 80 A, unless otherwise specified under conditions. Typical values given at: T_{P1} = +25 °C, V_{in} = 54 V, max P_{out_TDP}, unless otherwise specified under conditions, see *Note 1*.

Additional external C_{in} = 2 x 100 µF + 5 x 2.2 µF ceramic, C_{out} = 2 x 470 µF + 10 x 10 µF ceramic

Characteristic	conditions	minimum	typical	maximum	unit
Output characteristics					
Output voltage	P_{out} = 0 W		13.5		V
Output voltage	Disabled, no load		0.5		V
Output voltage	Disabled, 1 kΩ load		0		V
Output current (I_{out})	V_{in} = 38 - 60 V, PG asserted			80	A
Output voltage droop	I_{out} step from 0 to 80 A		460		mV
Output ripple & noise	20 MHz BW		120		mV_{p-p}
Internal output capacitance	V_{out} = 0V			160	µF
On/off control					
Turn-off input voltage	Decreasing input voltage		32		V
Turn-on input voltage	Increasing input voltage		37		V
Ramp-up time	From 10% to 90% of V_{out}, I_{out} = 0 A		4.3		ms
Start-up time	from V_{in} connection to 90% of V_{out}		20		ms
Enable start-up time	From EN asserted to 100% of Vout, I_{out} = 0 A		4		ms
Logic high: trigger level	EN pin	1.7			V
Logic low: trigger level	EN pin			1.55	V
Source current	EN pin (Internal pull up), see Note 2			5	mA
Sink current	EN pin			4	mA

Note 1: Max. output current is rated at 210 A. Max power is ≤ 2800 W and continuous power (thermal design power (TDP) is ≤ 1000 W depending on thermal conditions).

*Note 2: A protection Zener diode is connected to GND, which would influence the need for external source current on EN pin. Modules with PN: BMR316XX**3**X/XXX is not equipped with Zener diode and the pull-up resistor value is higher, thus significantly less source current is needed on BMR316XX**3**X/XXX. Internal pull-up is default, so no additional pull up is needed for modules with or without the protective Zener diode.*

Electrical specifications for **BMR316**

13.5 V, 80 A (210 A peak) / 1000 W (2800 W peak)

Characteristic	conditions	minimum	typical	maximum	unit
Protection features					
Input Over Voltage fault limit (IOVP)	Latch (0x80)		65		V
Input Over Voltage warning limit			62		V
Output undervoltage fault limit (UVP)	Latch (0x80)		7.5		V
Output undervoltage warning limit			8.5		V
Output overvoltage fault limit (OVP)	Latch (0x80)		16.25		V
Output overvoltage warning limit			15.5		V
Over temperature fault limit (OTP)	Latch (0x80)		125		°C
Over temperature warning limit (OTW)			110		°C
Over Current Protection (OCP) Note 1	Average OCP Limit		100		A
	IOUT_OC_FAULT_LIMIT (Normal OCP)		210		A
	IOUT_OC_FAST_FAULT_LIMIT (Fast OCP)		220		A
Protection Response Time	IOVP		2		µs
	OVP		0.04		µs
	UVP		0.16		µs
	OTP		2		µs
	Average OCP @ base current=60A to overcurrent 120A Note 2		42		ms
	OCP		70		µs
	FAST OCP		5		µs

Note 1: The module can run less than 80us at fast OC level (IOUT_OC_FAST_FAULT_LIMIT), and between 80us to 50ms at the normal OC level (IOUT_OC_FAULT_LIMIT), and above 50ms the average OC level. Note the time module can run before the average OCP depends on the IOUT telemetry reading, base current level, peak current level and peak current length. The average OCP defines the over current level at which the module can run continuously, without thermally stressing the module. The detailed explanation can be found on the technical reference document.

Note 2: The time specified is based on the calculation at base current 60A and overcurrent 120A for 50ms. The detailed explanation can be found on the technical reference document.

Electrical specifications for **BMR316**

13.5 V, 80 A (210 A peak) / 1000 W (2800 W peak)

Characteristic	conditions	minimum	typical	maximum	unit
Monitoring & Control					
$UVLO_{VI}$ - Under Voltage Lock-Out	V_{in} rising threshold		37		V
	Hysteresis		5.4		V
Power Good Delay Time	From V_{out} = 100 % to PG asserted		15		ms
Power Good Threshold	Low to high transition		9.3		V_{out}
	High to low transition, Note 1		9.1		V_{out}
V_{IL} - Logic input low	SDA, SCL, ALERT			1.0	V
V_{IL}- Logic input high	SDA, SCL, ALERT	2.3			V
V_{OL} - Logic output low	SDA, SCL, ALERT			400	mV
I_{OL} - Logic output low sink current	SDA, SCL, ALERT			20	mA
I_{LEAK} - Logic leakage current	SDA, SCL, ALERT	-5		5	µA
C_{I_PIN} - Logic input capacitance	SDA, SCL, ALERT		7		pF
f_{SMB} - SMBus Operating frequency		100		400	kHz
EN - Enable	See page 5 "On/Off control"				

Note 1: Power Good is deasserted when any protection and warning is triggered, regardless of the output voltage level.

In the table below all PMBus commands are written in capital letters.

T_{P1} = -20 to + 110°C, V_{in}= 38 to 60 V, unless otherwise specified under conditions.
Typical values given at: T_{P1} = +25 °C, V_{in} = 54 V, max P_{out_TDP}, unless otherwise specified under conditions

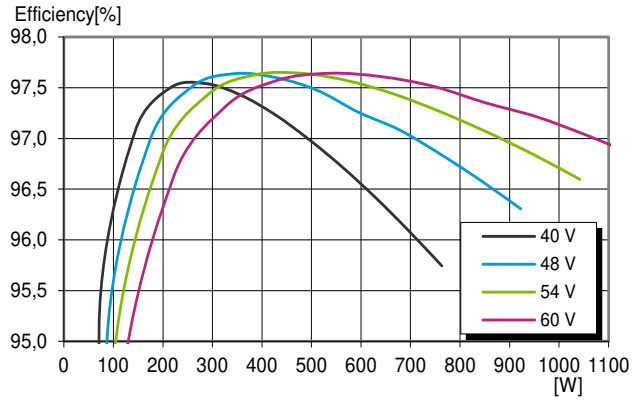
For more detailed information please refer to Technical Reference Document: PMBus commands. This product is supported by the Flex Power Designer tool.

Command	Conditions	minimum	typical	maximum	Unit
Monitoring accuracy					
Input voltage READ_VIN			±1		%
Output voltage READ_VOUT			±0.1		%
Output current READ_IOUT	V_{in} = 54 V, I_{out} = 50-80 A		±5		%
Output current READ_IOUT	V_{in} = 54 V, I_{out} = 0-50 A		±10		A
Temperature READ_TEMPERATURE_1	T ≥ 25 °C		±1		°C

Electrical graphs for BMR316

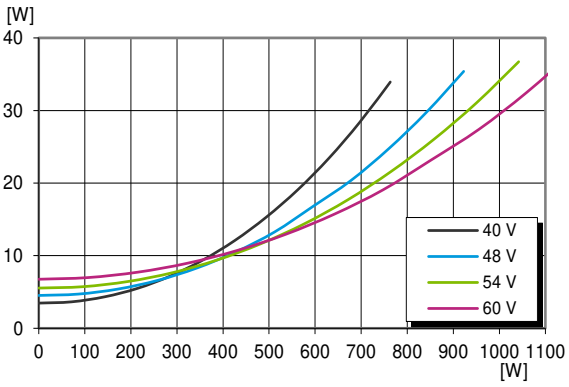
13.5 V, 80 A (210 A peak) / 1000 W (2800 W peak)

Efficiency



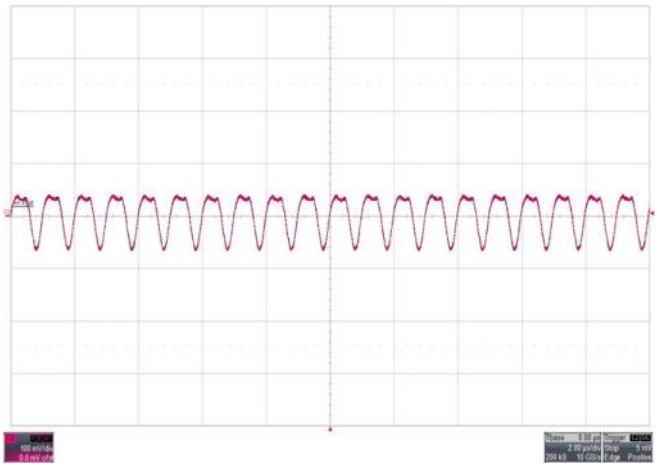
Efficiency vs. output power and input voltage at T_{P1} = +25 °C.

Power dissipation



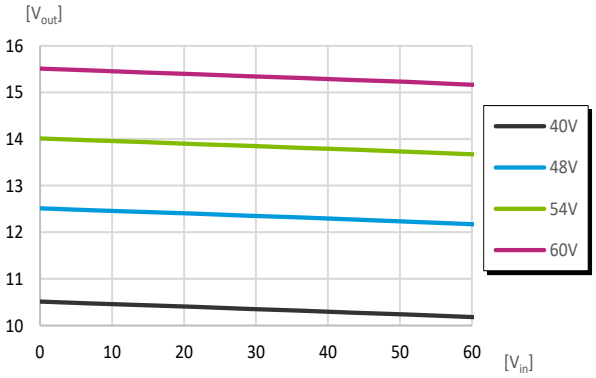
Dissipated power vs. load power at T_{P1} = +25 °C.

Output Ripple and Noise



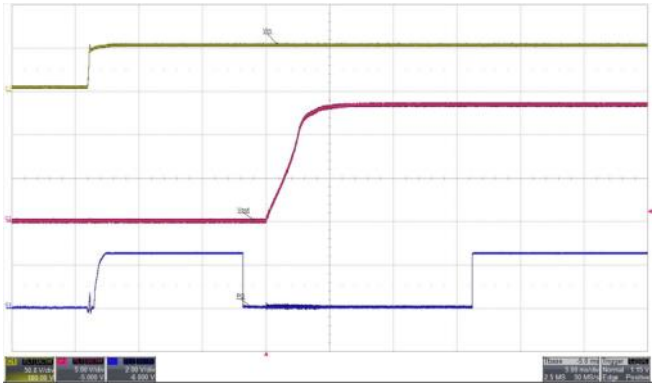
V_{in} = 54 V, I_{out} = 80 A, 20 MHz BW. Scale 100 mV/div, 2 us/div.

Output voltage droop



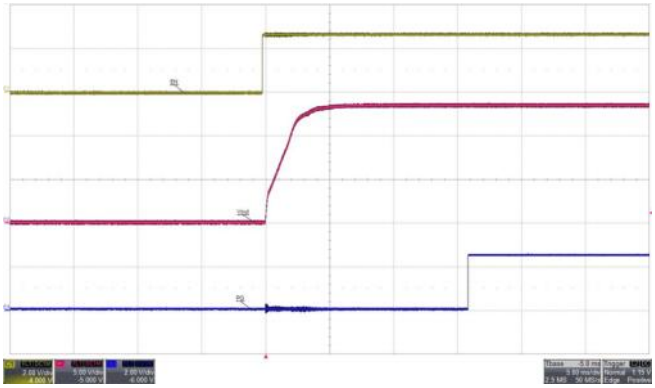
Output voltage vs output current.

Startup, Vin



Output enabled by EN pin. V_{in} = 54 V, I_{out} = 2 A
Scale from top: 50, 5, 2 V/div, 5 ms/div.

Startup, EN

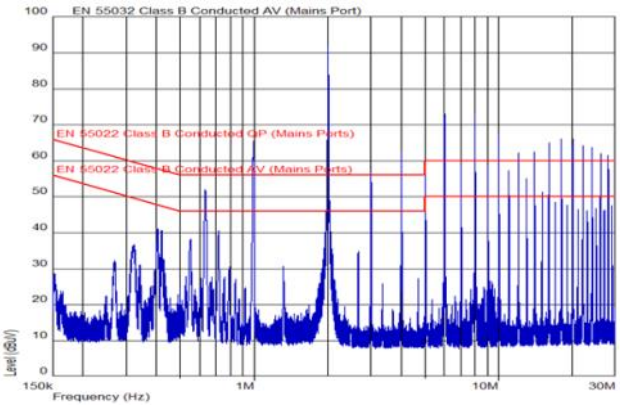


Output disabled by EN pin. V_{in} = 54 V, I_{out} = 80 A
Scale from top: 2, 5, 2 V/div, 5 ms/div.

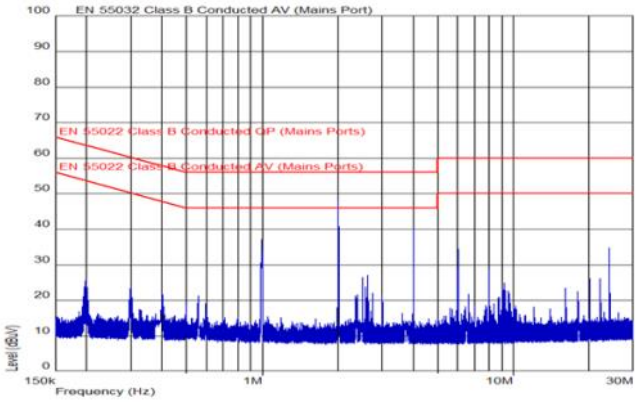
Part 2: EMC

EMC specifications

Conducted EMI measured according to EN55022 / EN55032, CISPR 22 / CISPR 32 and FCC part 15J (see test set-up below). The fundamental switching frequency is 1MHz for BMR316. The EMI characteristics below is measured at V_{in} = 54 V and max I_{out}. Note the provided filter ensures the module is below average limit, but not below quasi-peak limit.



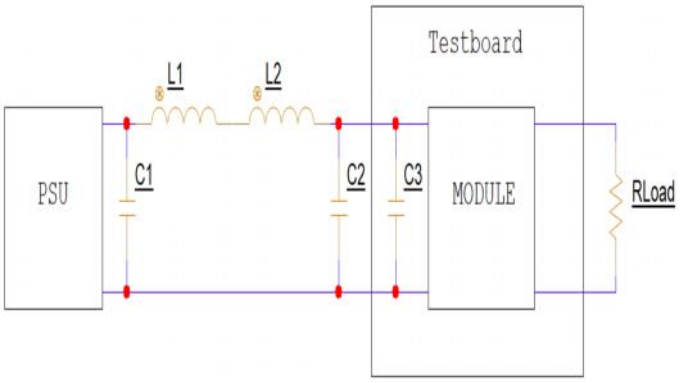
EMI without filter. (graph = average values)



EMI with an optional external filter, EN55032. Test method and limits are the same as EN55022. (graph : average values)

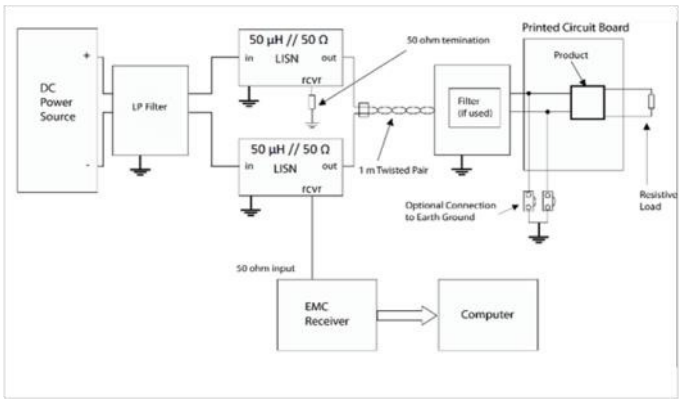
Optional external filter for Class B

Suggested external input filter in order to meet Class B in EN 55022 / EN 55032, CISPR 22 / CISPR 32 and FCC part 15J.



Filter components:
L1 and L2 = 100 nH
C1 = 5 x 10 µF
C2 = 5 x 10 µF +2 x 2.2µF
C3 = 100 µF

Filter components:
100 nH : IHLP5050FDERR10M01
10 µF : GRM32ER71J106KA12L
2.2µF: GRF32ER72A225KA11L



Test set-up

**paird 1m cable out from LISN*

Layout recommendations

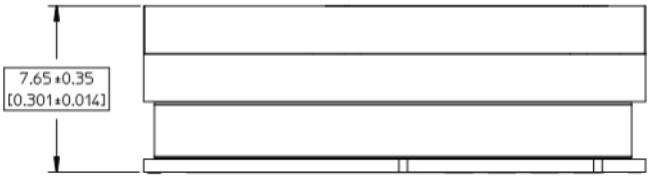
The radiated EMI performance of the product will depend on the customer PCB layout and ground layer design. It is also important to consider the stand-off of the product. If a ground layer is used, it should be connected to the output of the product and the equipment ground or chassis. A ground layer will increase the stray capacitance in the PCB and improve the high frequency EMC performance.

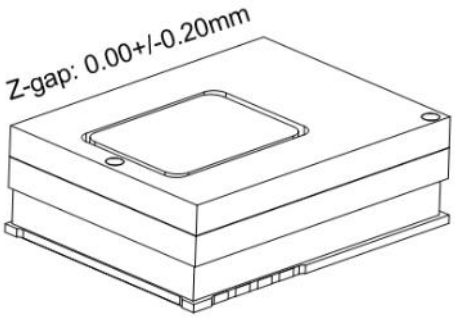
Part 3: Mechanical information

BMR316: SMD mounted, baseplate version

The mechanical information is based on a module which is SMD mounted and has a baseplate.

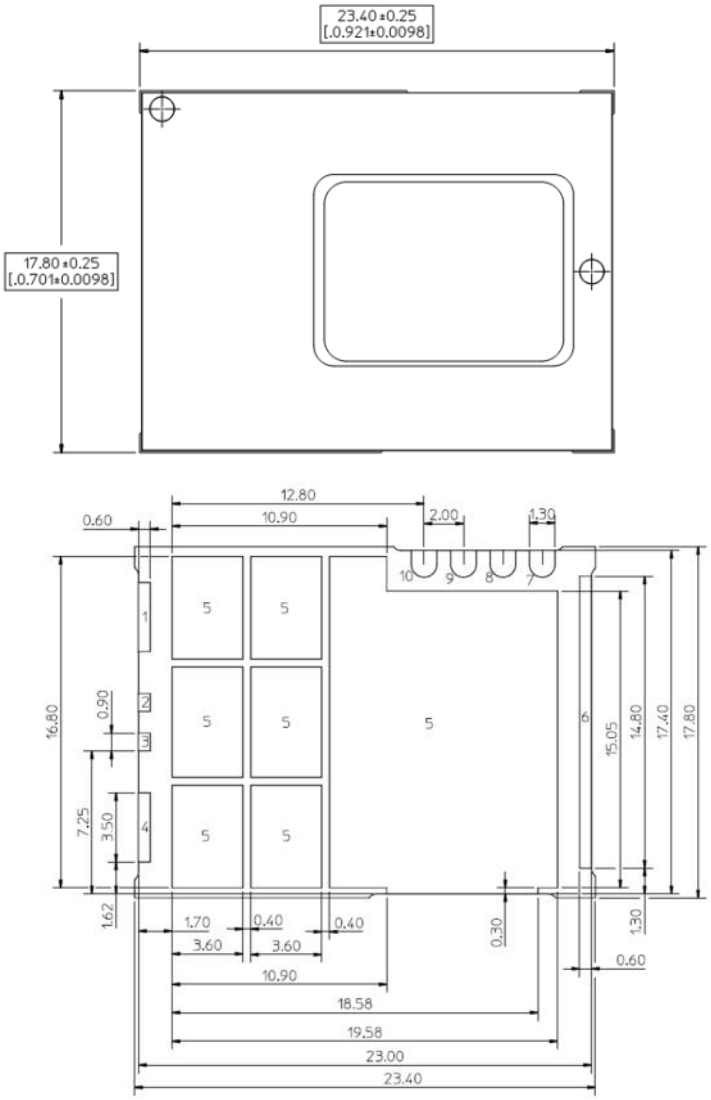
Side view





Top view

Product overall X/Y dimension including both top and bottom boards.



BASEPLATE INTERFACE

Material: Aluminium (anodized, black)

PAD SPECIFICATION

Material: Copper alloy

Plating: ENIG

(Electroless nickel/immersion gold plating)

WEIGHT

Typical 10.1g

All dimensions in mm [inches]

Tolerances unless specified:

x.x ±0.5 mm [0.02 inch]

x.xx ±0.25 mm [0.01 inch]

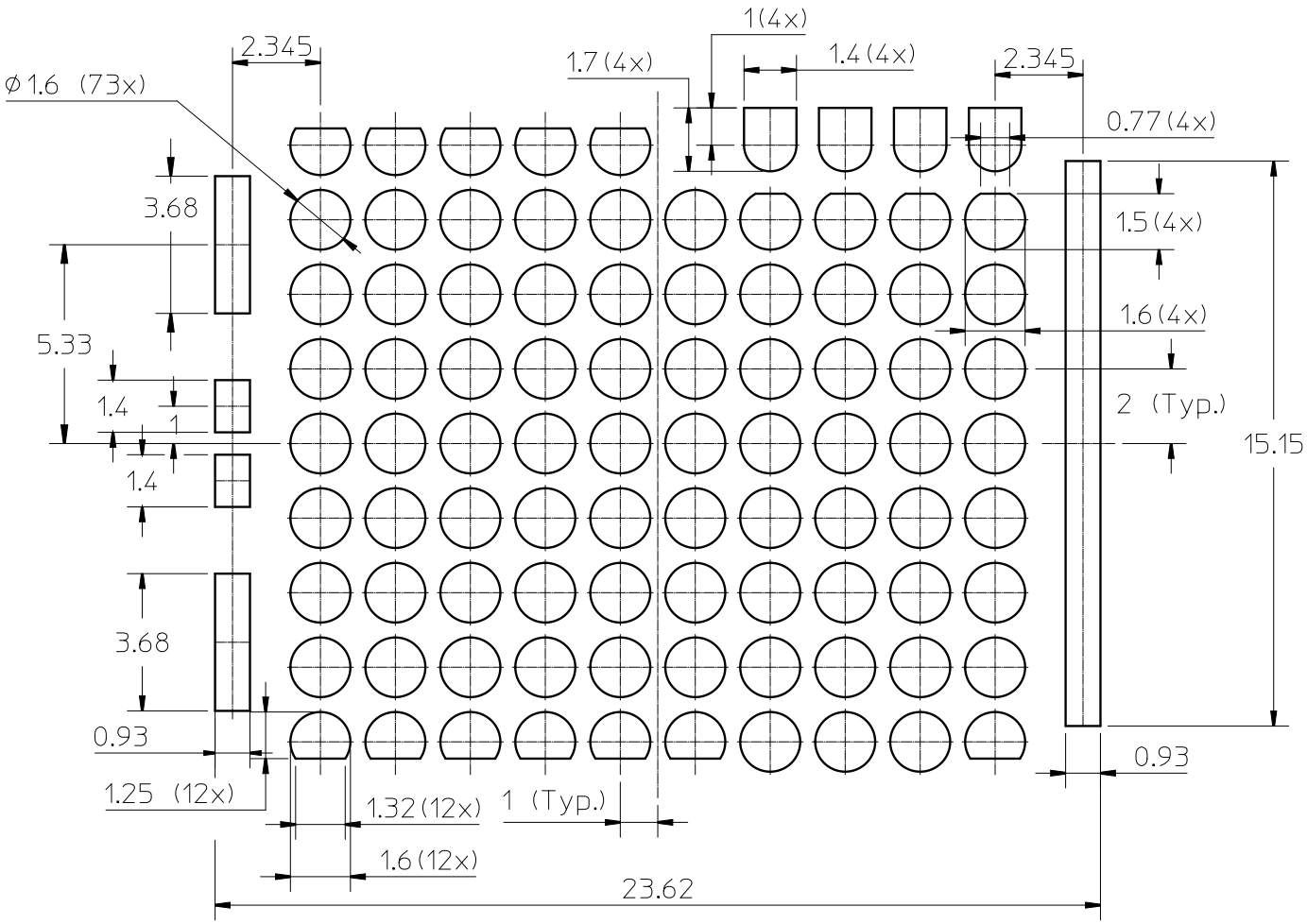
(not applied on footprint or typical values)

All component placements – whether shown as physical components or symbolical outline – are for reference only and are subject to change throughout the product's life cycle, unless explicitly described and dimensioned in this drawing.

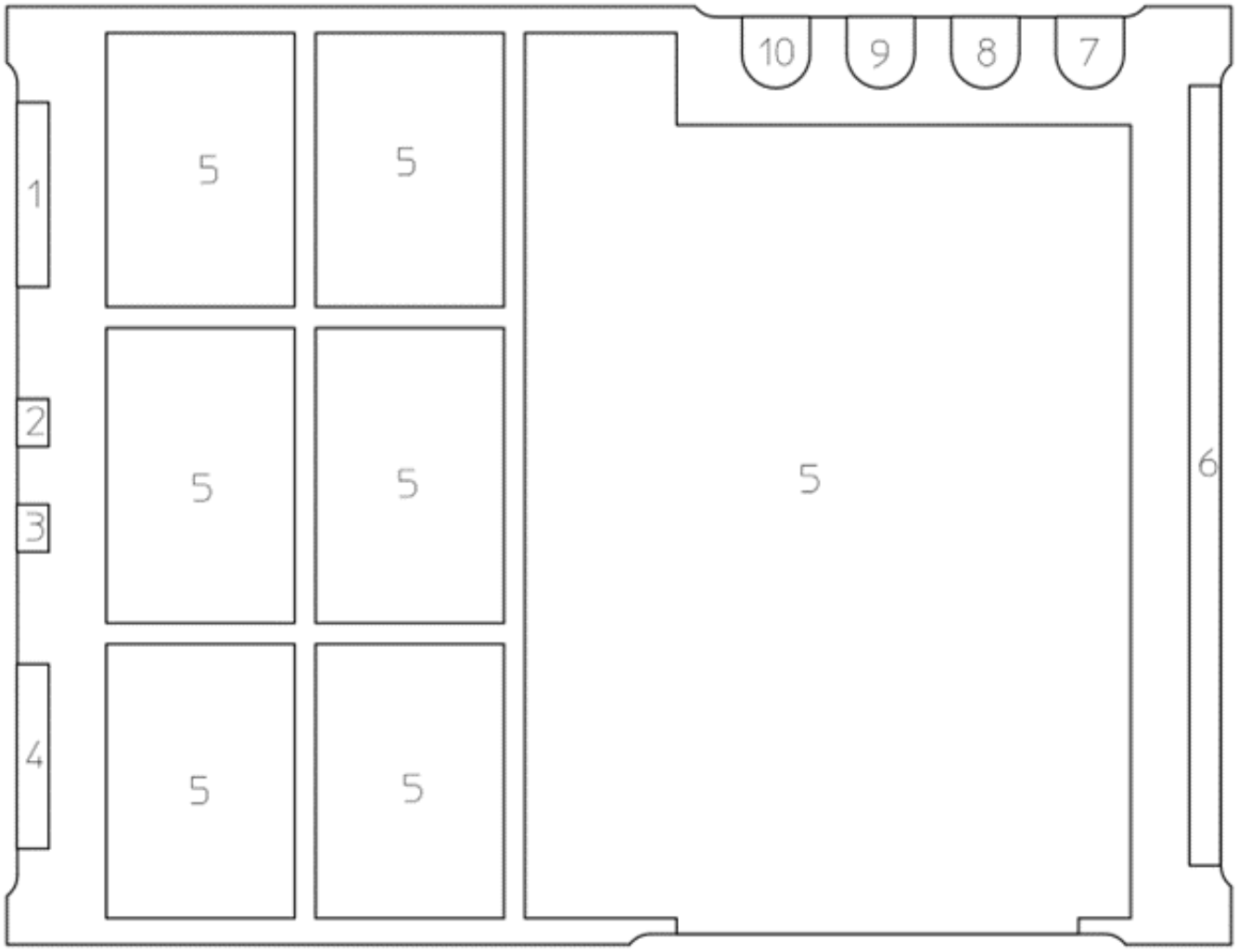
BMR316: SMD mounted, baseplate version

The mechanical information is based on a module which is SMD mounted and has a baseplate.

Recommended footprint top view through the product



TOP VIEW - Pin-out description and pin positions



Pin	Designation	Type	Function
1	+IN	Power	Input voltage
2	PG	Open Drain	Power good, active high
3	EN	Input	Enable, active high
4	+IN	Power	Input voltage
5	GND	Power	Power ground
6	VOUT	Power	Output voltage
7	ADDR	Input	PMBus address pin strap
8	SDA	Input/Output	PMBus data
9	SCL	Input	PMBus clock
10	ALERT/SYNC	Open Drain	See technical reference description.

Part 4: Thermal considerations

Thermal considerations

General

The product is designed with power switches on top to operate with top side cooling towards a heat sink or a liquid cooled plate. This is required to handle operation with high load. Cooling is also achieved by conduction to the host board and surrounding air. Sufficient cooling must be provided to ensure reliable operation.

The Output Current Derating graph found in the Electrical Specification section provides the available output current versus case temperature and host board temperature.

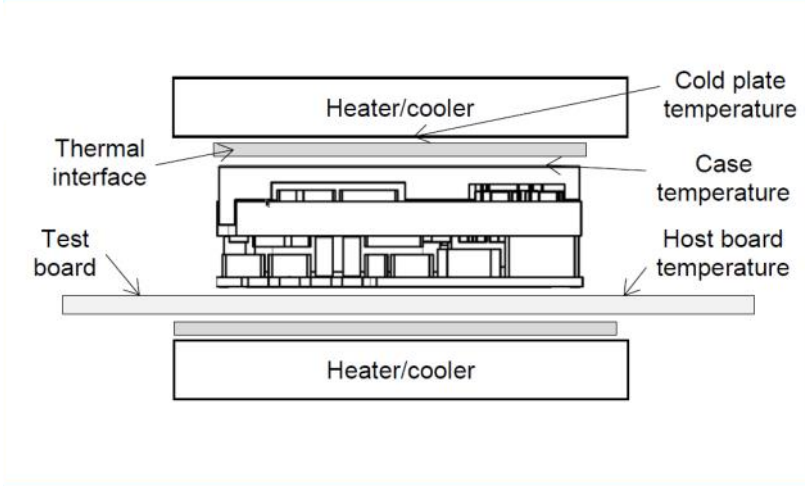
The windspeed and temperature are measured in a point upstream the device. The output current derating graph provides the derated power vs ambient temperature and air velocity at Vin = 54V.

For products using any form of heat sink structure a top spacing board and side airflow guides are used to ensure airflow hitting the device and not divert away.

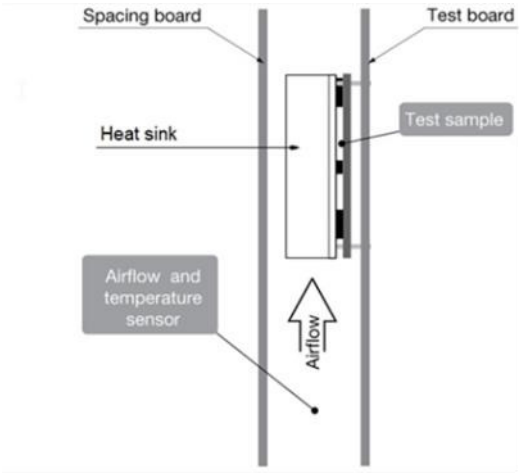
Distance between the tested device and top space and side air guides are 6.35mm ± 1mm.

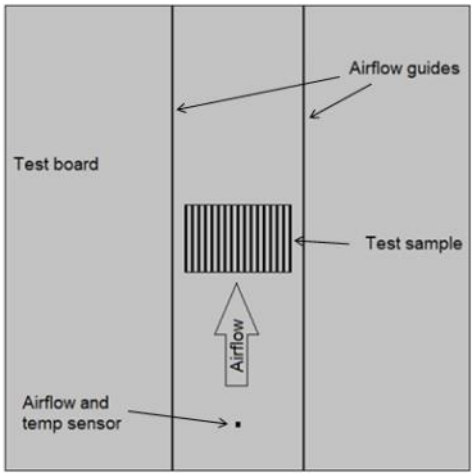
The device is tested on a 185*185mm. 105uM (3 os) 6 layer test-board mounted vertical in a wind tunnel.

Test Setup – Liquid cooling



Test Setup – Wind tunnel cooling





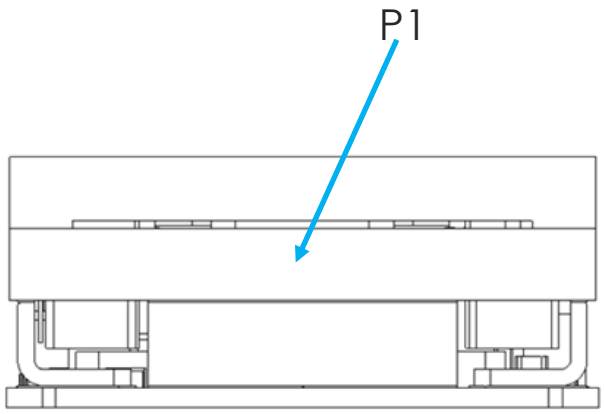
Definition of product operating temperature

The product operating temperatures are used to monitor the temperature of the product, and proper thermal conditions can be verified by measuring the temperature at position P1. The temperature at these position (T_{P1}) should not exceed the maximum temperatures in the table below. The number of measurement points may vary with different thermal design and topology. Temperatures above maximum are not allowed and may cause permanent damage.

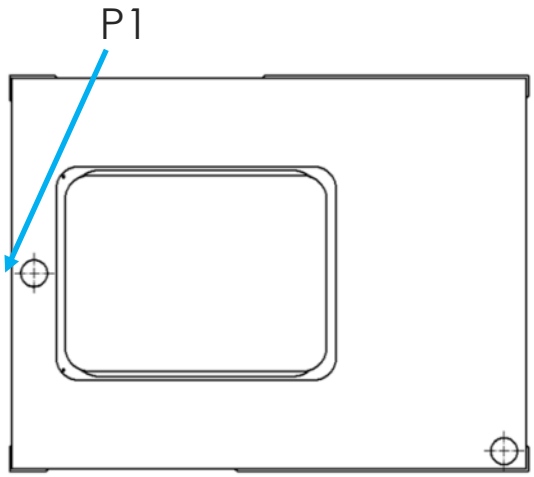
Position	Description	Max. Temp.
P1	Board edge on the output voltage side.	T_{P1} = 110 °C *

*This is the measurement spot that shall correspond with hot spot reaching up to OTP level,, which is 125*C.

Side view

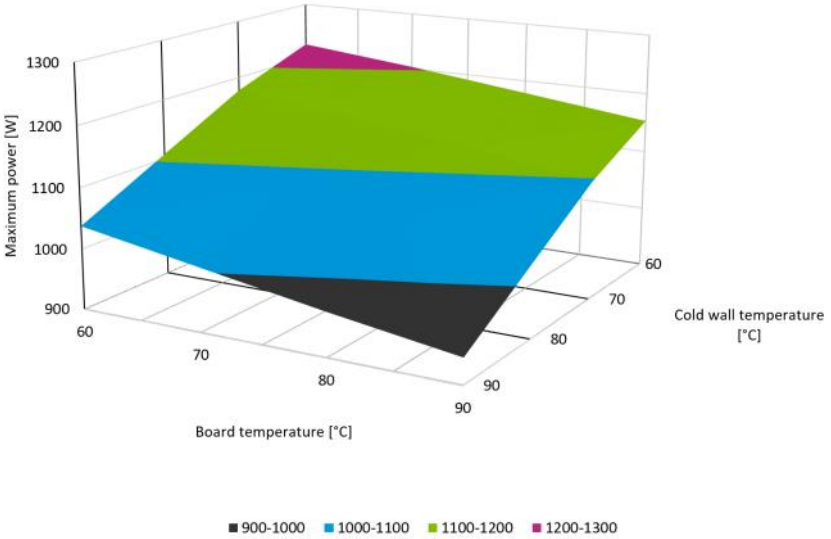


Top view

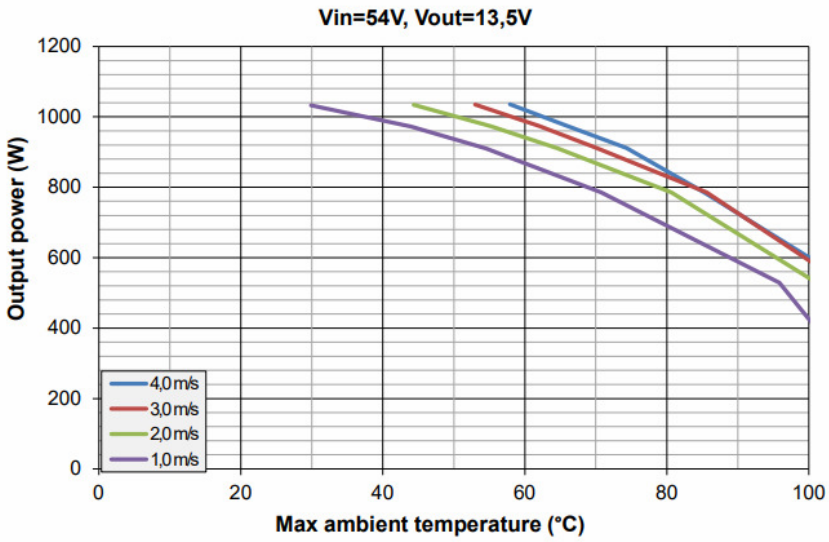


Thermal graphs

Output current derating



Max average output current vs. cold plate temperature (x-axis) and host board temperature.
Thermal interface gap pad 1.0 mm, 8 W/mK.

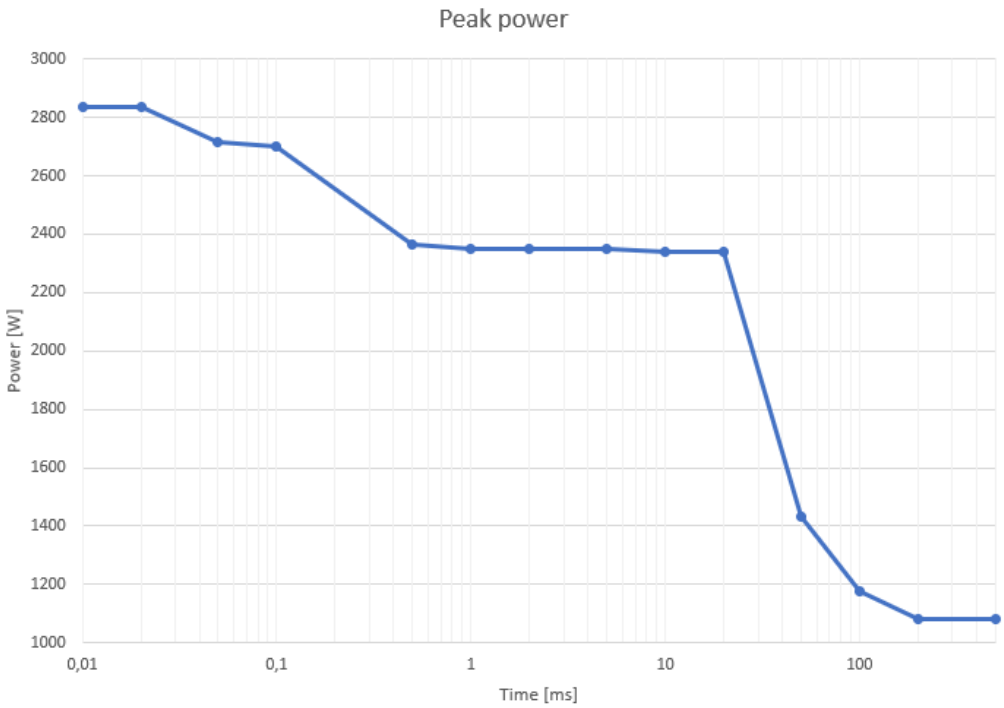


Max avreage output power vs. ambient temperature
Thermal interface gap pad to heat sink is 1.0 mm, 8 W/mK.

For more information, please refer to our thermal models on the website.

Peak Power

Peak current capability



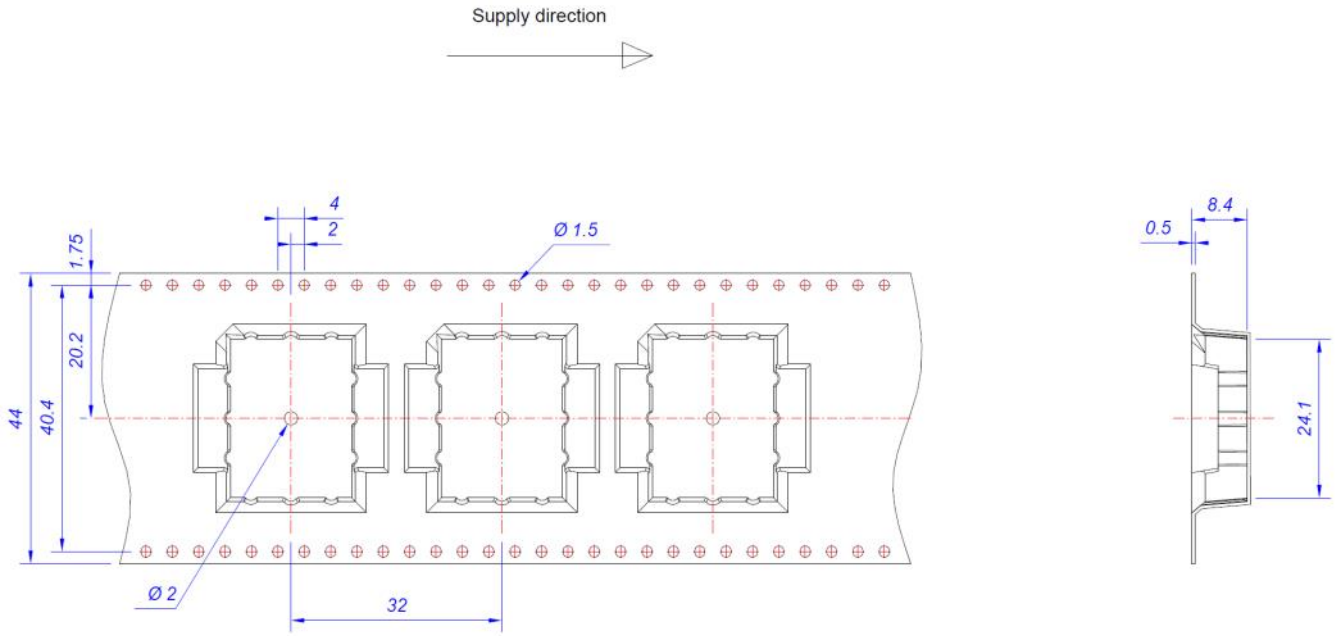
Max peak output current vs pulse duration and PMBus monitored temperature when pulse starts. Input voltage 54V and initial Iout = 60 A.

Part 5: Packaging

Packaging information

The products are delivered in antistatic carrier tape (EIA 481 standard).

Carrier Tape Specifications	
Material	PS, Antistatic
Surface resistance	< 10^7 Ω/square
Bakability	The tape is not bakeable
Tape width, W	44 mm [1.73 inch]
Pocket pitch, P1	32 mm [1.26 inch]
Pocket depth, K0	8.4 mm [0.33 inch]
Reel diameter	330 mm [13.0 inch]
Reel capacity	180 products /reel
Reel weight	2400 g/full reel



Part 6: Revision history

Revision table

Revision number	revision change	date	revisor
Rev. A	Release	2025-03-05	Team 4
Rev. B	Added variant without Zener diode. See page 2. Start up descriptions updated on page 8. Note 2 on page 5 is updated. Tp1 description on page 14 is improved. Mechancial drawing description updated and correction of typo on the inch measure on page 10. Iout reading accuracy on page 7 is improved to describe low, medium and high load.	2025-05-16	karjnils
Rev. C	Updated Iout reading range on page 7.	2025-05-20	Karjnils
Rev. D	Ordernumber example updated with all options.	2025-06-25	Karjnils

Flex Power Modules, a business line of Flex, is a leading manufacturer and solution provider of scalable DC/DC converter primarily serving the data processing, communications, industrial and transportation markets. Offering a wide range of both isolated and non-isolated solutions, its digitally-enabled DC/DC converters include PMBus compatibility supported by the powerful Flex Power Designer.

TECHNICAL REFERENCE DOCUMENT: DESIGN & APPLICATION GUIDELINES

OPERATING INFORMATION

Input Voltage

The input voltage range 38V to 60V (dc) meets the requirements for normal input voltage range in 48V systems, 40.5V to 57.0V.

Short duration transient disturbances can occur on the DC distribution and input of the product when a short circuit fault occurs on the equipment side of a protective device (fuse or circuit breaker). The voltage level, duration and energy of the disturbance are dependent on the particular DC distribution network characteristics and can be sufficient to damage the product unless measures are taken to suppress or absorb this energy. The transient voltage can be limited by capacitors and other energy absorbing devices such as zener diodes connected across the positive and negative input conductors at strategic points in the distribution network. The end-user must ensure that the transient voltage will not exceed the value stated in the Absolute maximum ratings. ETSI TR 100 283 examines the parameters of DC distribution networks and provides guidelines for controlling the transient and reduce its harmful effect.

Turn on and off input voltage

The product monitors the input voltage and will turn on and turn off at configured thresholds (see Electrical Specification). The turn-on input voltage voltage threshold, defined by command VIN_ON (0x35), is set higher than the corresponding turn-off threshold, defined by command VIN_OFF (0x36). Hence, there is a hysteresis between turn-on and turn-off input voltage levels.

Input voltage transient

The end-user must ensure that the transient voltage will not exceed the value stated in the *Datasheet under Absolute maximum ratings* of each product. ETSI TR 100 283 examines the parameters of DC distribution networks and provides guidelines for controlling the transient and reduce its harmful effect.

Input and Output Impedance

The impedance of both the input source and the load will interact with the impedance of the product. It is important that the input source has low characteristic impedance. The electrolytic capacitors will be degraded in low temperature. The needed input capacitance in low temperature should be equivalent to minimum recomended input and output capacitance at 20 °C. The performance in some applications can be enhanced by the addition of external capacitance as described under External decoupling capacitors . If the input voltage source contains significant inductance, the addition of a low ESR ceramic capacitor of 22 – 100 µF capacitor across the input of the product will ensure stable operation. The minimum required capacitance value depends on the output power and the input voltage. The higher output power the higher input capacitance is needed.

External decoupling capacitors

The voltage at the load can be improved by adding decoupling capacitors at the load if loads with significant dynamic currents are required. The most effective technique is to place low ESR capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR.
Ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. It is equally important to use low resistance and low inductance PCB layouts and cabling.

Enabling Output Voltage

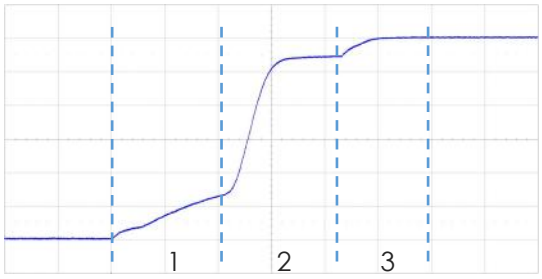
The output voltage is controlled by the EN pin and/or the PMBus command OPERATION, depending on the settings of the standard PMBus command ON_OFF_CONFIG. Both active high and active low logic of the EN pin is supported. By default the output voltage is enabled by the EN pin using active low or high logic (OPERATION is ignored).

The EN pin has an open collector circuit with internal pull-up resistor. External EN switching device must have a sufficient sink current ability to be able to pull the EN pin voltage down below the logic low threshold level (see Electrical Characteristics). As the BMR316 intentionally was intended as a drop in replacement for the BMR313 module an over Voltage protection Zener diode is connected to GND.
This as BMR313 uses 5V logic and BMR316 uses 3.3V logic levels.
This would influence the need for an external source current on EN pin if an external pull-up is needed.
Default is the pull-up internal so no additional pull up is needed.

Soft-start

Once enabled, the output voltage will ramp up to a 4:1 ratio of the input voltage. The ramp up is controlled monotonic and performed in three steps:

1. FETs start switching at minimum Duty cycle and switching frequency and updated based on Vout/ Vin ratio with low energy transfer. The ramp is monitored to detect short circuits on the output.
2. The output voltage starts ramping up slowly by increasing the switching frequency to nom.
3. Approaching the end of the soft start, the duty cycle is slowly increasing the duty cycle to nom.



Ramp up waveform

The BMR 316 start-up sequence does not allow to load the module more than 5A at startup.
To fully load the BMR316 (1000W) it needs to have ended the startup sequence, be in continuous operation mode and have initiated a Power Good signal.
The load during startup is output capacitor dependant and a max allowed output capacitor is 6mF.

Note: The soft-start sequence can not be changed by the user.

Pre-bias start-up

The product has a pre-bias start up functionality and will not sink current during start up if a pre-bias source is present at the output terminals. If the pre-bias voltage is lower than the output voltage, the product will ramp up to the target value.

Over temperature protection (OTP)

The product is protected from thermal overload by an internal over temperature shutdown function. The temperature sensor is located to provide a temperature representative of the module hot spot, see section Thermal Considerations in the datasheet.

The temperature is continuously monitored and when the temperature rises above the configured fault threshold level the product will respond as configured. The product can respond in ways as follows:

1. Immediate shutdown of output voltage until the module is re-enabled (latch). Default setting.
2. Ignore fault and continue operation. Not recomended.

The default OTP limit is specified in section Electrical Characteristics in the datasheet. The OTP fault and warning limits and response are configured using the PMBus commands OT_FAULT_LIMIT(0x4F), OT_WARN_LIMIT(0x51) and OT_FAULT_RESPONSE(0x50).

Input Voltage Protections (IUVP, IOVP)

The product monitors the input voltage continuously. If the output voltage is enabled, and the input voltage falls below or rises above the configured threshold levels (see Electrical Specification) the product will respond as configured. The response can be configured in different ways:

1. Immediate shutdown of output voltage until the module is re-enabled (latch). Default setting.
2. Ignore fault and continue operation. Not recomended.

The protections are configured using the PMBus commands: VIN_UV_FAULT_LIMIT(0x59), VIN_UV_FAULT_RESPONSE(0x5A), VIN_OV_FAULT_LIMIT(0x55) and VIN_OV_FAULT_RESPONSE(0x56).

Output Voltage Protection (UVP, OVP)

The product includes functionality for under and over voltage warnings and protection of the output voltage. The product can be configured to respond in different ways when the UVP/OVP fault limit is passed:

1. Immediate shutdown of output voltage until the module is re-enabled (latch). Default setting.
2. Ignore fault and continue operation. Not recomended

The limits and fault responses are configured using the PMBus commands VOUT_UV_FAULT_LIMIT (0x44), VOUT_OV_FAULT_LIMIT(0x40), VOUT_UV_WARN_LIMIT(0x43), VOUT_OV_WARN_LIMIT(0x42), VOUT_UV_FAULT_RESPONSE(0x45) and VOUT_OV_FAULT_RESPONSE(0x41).

For more information, see *Technical Reference Document: PMBus*.

Over current protection (OCP)

The product includes robust current limiting functionality for protection of overload at continous operation as well as transients during peak power operation. The OCP function has 4 parts:

1. Fast OCP that reacts on pulses down to a few microsecond to approximately 100us and used for protection of higher currents at very fast peak power operation.
2. Normal standard PMBus OCP triggers fron 100us up to approximately 20ms
3. From 20ms and to continous max TDP, a slow OCP protection is enabled based on averaging power over time. The function is configured to allow a load step from TDP to Peak Power level between 20ms and 200ms
4. After 200ms the protection level is configured for continuous operation.

Short circuit protection

During soft start the output voltage ramp is continuously monitored to detect a short circuit on the output. If the output voltage is not rising as expected, switching will stop and raise Startup OC fault. When there is a short circuit during operation, the module is protected by Over Current Protection.

Power good

The power good pin (PG) indicates when the product is ready to provide output voltage to the load. After initialization, the PG pin is asserted low until the output voltage is enabled and the soft-start procedure has finished. The product provides a Power Good flag in the Status Word register that indicates the output voltage is within a specified tolerance of its target level and no-fault condition exists.

By default, the PG pin is configured as open drain output, but it is also possible to set the output in push/pull mode by the command MFR_MULTI_PIN_CONFIG (0xF9). The polarity is by default set to active high, the polarity of PG can be set to active low in the command MFR_CONFIG_PMBUS(0xC9).

The BMR316 has an options to use open drain circuit with internal or external pull-up resistor on the PG pin. *See product options in BMR316 datasheet.*

If external PG is chosen, switching device must have a sufficient sink current ability to be able to pull EN pin voltage down below logic low threshold level (see Electrical Characteristics). As the BMR316 intentionally was intended as a drop in replacement for the BMR313 module, an over Voltage protection Zener diode is connected from PG pin to GND.
This as BMR313 uses 5V logic and BMR316 uses 3.3V logic levels.
This would influence the need for external source current on the PG pin if an external pull-up is needed.

Note on PG pin:
It is not recommended to use push-pull when paralleling PG- pins.

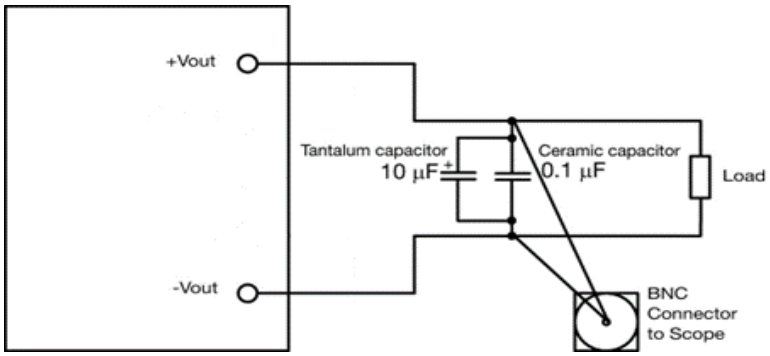
Peak power considerations

The DC/DC converter has a peak power level vs time limit set in the technical specification. This in order to handle higher power than the thermal design power (TDP) for the converter during shorter times. The Peak power level is also set in consideration of overshoot from a fast transient, tested from 0 to peak current with a specified di/dt. Thus a higher peak power, up to few standard OCP level, can be achieved but for a shorter period of time. Faster di/dt and higher peak load than specified, might cause current overshoot resulting in OCP fault.

For further assistance, contact your local Flex Power Modules' representative or email us to pm.info@flex.com

Output ripple and noise

Output ripple and noise measured is performed according to the figure below using evaluation board. See Flex Design Note 022 for detailed information



Output ripple and noise test setup

Non-Volatile Memory (NVM)

The product incorporates two Non-Volatile Memory areas for storage of the PMBus command values; the Default NVM and the User NVM. The Default NVM is pre-loaded with Flex factory default values. The Default NVM is write-protected and can be used to restore the Flex factory default values through the command RESTORE_DEFAULT_ALL (0x12).

The User NVM is pre-loaded with Flex factory default values. The User NVM is writable and open for customization. The values in NVM are loaded during initialization, where after commands can be changed through the PMBus Interface.

The module contains a one-time programmable memory (OTP) used to store configuration settings, which will not be programmed into the device OTP automatically. The STORE_USER_ALL(0x15) commands must be used to commit the current settings are transferred from RAM to OTP as device defaults.

Note: The one-time programmable memory (OTP) has limited storing times, frequent use of STORE_USER_ALL command can lead to memory space exhaustion. Remaining available memory is displayed in Flex Power Designer.

Parallel operation Droop Load Share (DLS)

Two or more products may be paralleled. The products provide output voltage droop resistance in secondary transformer winding, which enables direct paralleling. To achieve optimum operation when paralleling modules, it is important to ensure the same PCB routing path resistance between the input terminals and merged output terminals. The output voltage will decrease with increased load current. This feature allows the product to be connected in parallel and share current within 10% accuracy at max output power. This means that up to 90% of max rated current from each module can be utilized.

In applications with several modules in parallel, the PG signal of all modules should be connected together. Further, load shall not be applied unless PG signal is high (= all modules have successfully ramped up).

For further information please contact your local Flex Power Modules' representative or email us at pm.info@flex.com.

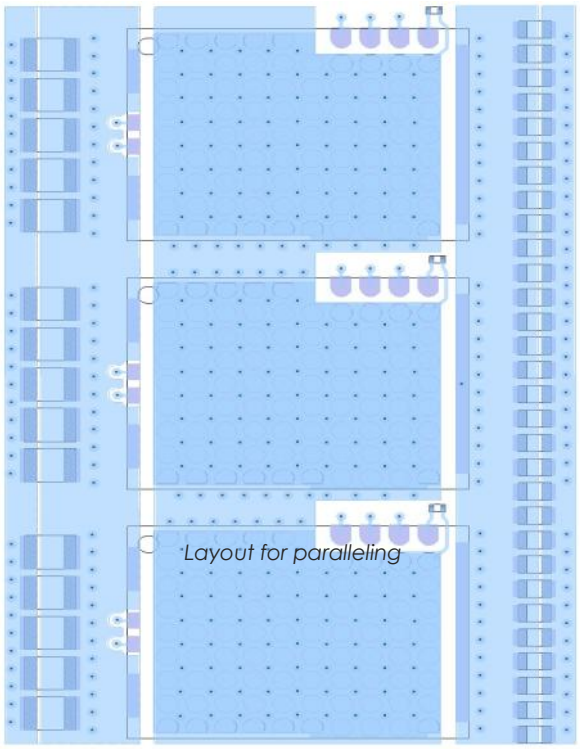
ALERT/SYNC

Alert/Sync pin can configured by MFR_MULTI_PIN_CONFIG (0xF9) command. Alert function can be configured by SMBALERT_MASK (0x1B). See *Technical Reference Document: PMBus*.

Synchronizing multiple converters to the same frequency with proper phase shift is often used to reduce the size of input filter, lower the EMI and reduce the voltage and current ripple. The SYNC allows to synchronize the PWM outputs to an external signal or provide a sync signal for other converters to act in synchronization. The BMR 316 needs to have the SYNC pin set to either sync in or sync out by the MFR_MULTI_PIN_CONFIG (0xF9) command. The INTERLEAVE(0x37) command is used to configure the phase of interleaved topologies or the phase of multiple devices in parallel unit applications. It is applied by connecting Nmodules SYNC pins together and programming interleave parallelable module. Alert/SYNC pin is open collector circuit.

Default setting is ALERT function.

Layout recommendation for parallelling



Layout for paralleling

Switching frequency

The product is optimized at the frequency given in the Technical Specification under part 1- Electrical Specification. The frequency can not be changed by the user. Please contact your local Flex Power Modules FAE for more details.

POWER MANAGEMENT

PMBUS overview

This product is equipped with a PMBus interface. The product incorporates a wide range of readable and configurable power management features that are simple to implement with a minimum of external components. Additionally, the product includes protection features that continuously safeguard the load from damage due to unexpected system faults. A fault is also shown as an alert on the SALERT pin. The following product parameters can continuously be monitored by a host: Input voltage, output voltage/ current and internal temperature.

The product is delivered with a default configuration suitable for a wide range operation in terms of input voltage, output voltage, and load. The configuration is stored in an internal Non-Volatile Memory (NVM). All power management functions can be reconfigured using the PMBus interface.

Throughout this document, different PMBus commands are referenced. The Flex Power Designer software suite can be used to configure and monitor this product via the PMBus interface. More information is found on our website.

SMBus interface

This product provides a PMBus digital interface that enables the user to configure many aspects of the device operation as well as to monitor the input and output voltages, output current and device temperature. The product can be used with any standard two-wire I²C (master must allow for clock stretching) or SMBus host device. In addition, the product is compatible with SMBus version 3.0 and includes an SALERT line to help mitigate bandwidth limitations related to continuous fault monitoring. The product supports 100 kHz and 400 kHz bus clock frequency only. The SMBus signals, SCL, SDA and SALERT require passive pull-up resistors as stated in the SMBus Specification. Pull-up resistors are required to guarantee the rise time as follows:

$$\tau = R_p C_p \leq 1\ \mu s$$

where R_p is the pull-up resistor value and C_p is the bus load. The maximum allowed bus load is 400 pF. The pull-up resistor should be tied to an external supply between 2.7V to 5.5V, which should be present prior to or during power-up. If the proper power supply is not available, voltage dividers may be applied. Note that in this case, the resistance in the equation above corresponds to parallel connection of the resistors forming the voltage divider.

PMBus addressing

The PMBus address is configured with a resistor, RADDR, connected between the ADDR pin and GND. The value of the resistor decides an index according to the table below. The tolerance of the resistor must be 1% or better.

Index	Min R	Typ R	Max R
0	0	0	0,27
1	0,33	0,47	0,68
2	0,91	1	1,2
3	1,5	1,6	1,87
4	2,05	2,2	2,7
5	3	3,3	3,65
6	3,9	4,3	4,87
7	5,36	5,6	6,49
8	7,15	8,2	9,1
9	9,53	10	12
A	13,3	15	16,9
B	18	22	24
C	26,1	33	36
D	39	47	56
E	68	100	110
F	120	open	open

Table 1.

Index	Min R	Typ R	Max R
0	0	0	0,43
1	0,47	0.75	1
2	1,1	1.33	1,6
3	1,8	2	2,2
4	2,4	2.74	3
5	3,3	3.65	4,02
6	4,42	4.87	5,36
7	5,62	6.34	7,15
8	7,5	8.06	9,1
9	9,53	10.5	11,5
A	12,1	13.3	14,7
B	15,4	16.9	17,8
C	19,6	21.5	23,7
D	24,9	27.4	30,1
E	33,2	34.8	36,5
F	44,2	open	open

Table 2.

Two resistors tables are available. See Product options on page 2 in the data sheet.

The PMBus address is calculated as:

PMBus Address = Base Address + Index

Where the base address is defined by bits [31:24] in the PMBus command PMBUS_ADDRESS (0xC9). Default base addresses for individual product variants are defined in the Product options, giving an address range from 0x10 to 0x1F.

If changing the base address, the change will take effect after the input voltage is cycled.

If address range 0x2n is wanted, the I2C base address needs to be changed as well.
For further information please contact your local Flex Power Modules' representative or email us at pm.info@flex.com.

I2C/SMBus timing

The setup time, t_{set}, is the time data, SDA, must be stable before the rising edge of the clock signal, SCL. The hold time t_{hold}, is the time data, SDA, must be stable after the rising edge of the clock signal, SCL. If these times are violated incorrect data may be captured or meta-stability may occur and the bus communication may fail. All standard SMBus protocols must be followed, including clock stretching.

This product supports the BUSY flag in the status commands to indicate product being too busy for SMBus response. A bus-free time delay between every SMBus transmission (between every stop & start condition) must occur. Refer to the SMBus specification, for SMBus electrical and timing requirements.

Monitoring via PMBus

It is possible to continuously monitor a wide variety of parameters through the PMBus interface. These include, but are not limited to, the parameters listed in the table below.

Parameter	PMBus command
Input voltage	READ_VIN
Output voltage	READ_VOUT
Output current	READ_IOUT
Temperature	READ_TEMPERATURE_1

These PMBus commands are updated every 0.25 ms.

The temperature sensor is located to provide a temperature reading representative of the module hot spot, see section Thermal Considerations in the datasheet.

Monitoring faults

The user may read PMBus status commands to find out what fault or warning condition occurred, see table below:

Fault and warning status	PMBus command
Overview, Power Good	STATUS_BYTE STATUS_WORD
Output voltage level	STATUS _VOUT
Output current level	STATUS_IOUT
Input voltage level	STATUS_INPUT
Temperature level	STATUS_TEMPERATURE
PMBus communication	STATUS_CML
Miscellaneous	STATUS_MFR_SPECIFIC

Status bits are asserted until faults and/or warnings are cleared by the CLEAR_FAULTS (0x03) command. A re-enable of the output voltage will not clear the status bits.

General PMBus comand summary

PMBus signal interfaces characteristics

Characteristic	conditions	minimum	typical	maximum	unit
PMBus signal interface characteristics					
External sync pulse width		150			ns
Input clock frequency drift tolerance	External sync.	-4		4	%
Initialization time	From V_{in} > 27V to ready to be enabled		30		ms
Output voltage total on delay time	Enable by input voltage		T_{INIT} + T_{ONdel}		
	Enable by RC or CTRL pin		T_{ONdel}		
Logic output low signal level	SCL, DA, SYNC, GCB, SALERT, PG, sink/source current = 4 mA			0.25	V
Logic output high signal level		2.7			V
Logic output low sink current				4	mA
Logic output high source current				4	mA
Logic input low threshold	SCL, SDA, CTRL, SYNC			1.1	V
Logic input high threshold		2.1			V
Logic pin input capacitance	SCL, SDA, CTRL, SYNC		10		pF
Supported SMBus operating frequency		100		400	kHz
SMBus bus free time	STOP bit to START bit		1.3		µs
SMBus SDA setup time from SCL			100		µs
SMDBus SDA hold time from SCL			0		ns
SMBus START/STOP condition setup/hold time from SCL			600		ns
SCL low period		1.3			µs
SCL high period			0.6	50	µs

TECHNICAL REFERENCE DOCUMENT: GENERAL INFORMATION

Compatibility with RoHS requirements

The product is compatible with the relevant clauses and requirements of the *RoHS directive 2011/65/EU* and *2015/863* have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB, PBDE, DEHP, BBP, DBP, DIBP and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Flex Power Modules products are found in the Statement of Compliance document.

Flex Power Modules fulfills and will continuously fulfill all its obligations under regulation (EC) No 1907/2006 concerning the registration, evaluation, authorization and restriction of chemicals (REACH) as they enter into force and is through product materials declarations preparing for the obligations to communicate information on substances in the products.

Quality statement

The products are designed and manufactured in an industrial environment where quality systems and methods like *ISO 9001*, *ISO 14001*, *ISO 45001,* *Six Sigma* and *SPC* are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged workforce, contribute to the high quality of the products.

Warranty

Warranty period and conditions are defined in *Flex Power Modules' General Terms and Conditions of Sales*.

Limitation of Liability

Flex Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

Product qualification specifications

Characteristics			
External visual inspection	IPC-A-610		
Temperature shock test (Temperature cycling)	Recalculated to match IEC 60068-2-14 Na	Temperature range Number of cycles Dwell/transfer time	-40 to 125°C 700 15 min/0-1 min
Cold (in operation)	IEC 60068-2-1 Ad	Temperature T_A Duration	-45°C 72 h
Damp heat	IEC 60068-2-67 Cy	Temperature Humidity Duration	85°C 85% RH 1000 hours
Dry heat	IEC 60068-2-2 Bd	Temperature Duration	125°C 1000 h
Electrostatic discharge susceptibility	IEC 61340-3-1, JESD 22-A114 IEC 61340-3-2, JESD 22-A115	Human body model (HBM) Machine Model (MM)	Class 2, 2000 V Class 3, 200 V
Immersion in cleaning solvents	IEC 60068-2-45 XA, method 2	Water Flux Cleaner	55°C 23°C
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration Duration	100 g 6 ms
Moisture reflow sensitivity [1]	J-STD-020E	Level 1 (SnPb-eutectic) Level 3 (Pb Free)	225°C 245°C
Operational Life test Rapid Temp.	MIL-STD-202G, method 108A	Duration	1000 h
Robustness of terminations	IEC 60068-2-21 Test Ua1 IEC 60068-2-21 Test Ue1	Through-hole mount products Surface-mount products	All leads All leads
Solderability	IEC 60068-2-20 test Ta	Preconditioning Temperature, Pb-free	Steam ageing 245°C
Vibration, broad band random	IEC 60068-2-64 Fh, method 1	Frequency Spectral density Duration	10 to 500 Hz 0.07 g^2/Hz 10 min in each direction

Note 1: only for products intended for reflow soldering (surface mount products & pin-in paste products)

TECHNICAL REFERENCE DOCUMENT: SAFETY

Safety specifications

Flex Power Modules' DC/DC converters and DC/DC regulators are designed in accordance with the safety standards *IEC 62368-1, EN 62368-1* and *UL 62368-1 Audio/video, information and communication technology equipment - Part 1: Safety requirements*

IEC/EN/UL 62368-1 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Electrically-caused fire
- Injury caused by hazardous substances
- Mechanically-caused injury
- Skin burn
- Radiation-caused injury

On-board DC/DC converters, Power Interface Modules and DC/DC regulators are defined as component power supplies. As components they cannot fully comply with the provisions of any safety requirements without "conditions of acceptability". Clearance between conductors and between conductive parts of the component power supply and conductors on the board in the final product must meet the applicable safety requirements. Certain conditions of acceptability apply for component power supplies with limited stand-off (please refer to *Technical Specification under Mechanical Information* for further information). It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable safety standards and regulations for the final product.

Component power supplies for general use shall comply with the requirements in *IEC/EN/UL 62368-1*. Product related standards, e.g. *IEEE 802.3af Power over Ethernet*, and *ETS-300132-2* Power *interface at the input to telecom equipment, operated by direct current (dc)* are based on *IEC/EN/UL 62368-1* with regards to safety.

All Flex Power Modules' DC/DC converters, Power Interface Modules and DC/DC regulators are recognized and certified in accordance with *IEC/EN/UL 62368-1*. The flammability rating for all construction parts of the products meet requirements for V-1 class material according to "*IEC 60695-11-10 Fire hazard testing, test flames – 50 W horizontal and vertical flame test methods*".

Non-isolated DC/DC converters

The DC/DC converter output is ES1 energy source if the input source meets the requirements for ES1 according to *IEC/EN/UL 62368-1*.

TECHNICAL REFERENCE DOCUMENT: SOLDERING

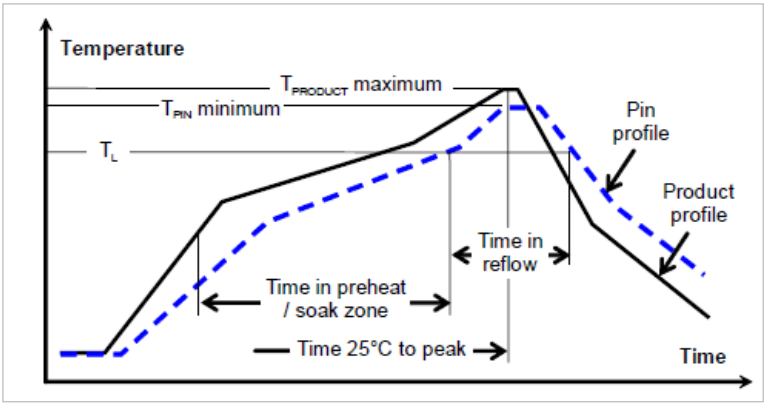
Reflow soldering - surface mount

Products intended for surface mount assembly are qualified for use in a Pb-free forced convection or vapor phase reflow soldering process.

The reflow profile should be optimised to avoid excessive heating of the product. It is recommended to have a sufficiently extended preheat time to ensure an even temperature across the host PCB and it is also recommended to minimize the time in reflow.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board, since cleaning residues may affect long time reliability and isolation voltage.

General reflow process specification			Pb-free
Average temperature ($T_{product}$)			3 °C/s max
Typical solder melting temp.	T_L		221 ° C
Min. Reflow time above T_L	T_{pin}		60 s
Min. pin temp.	T_{pin}		235 °C
Peak product temp.	$T_{product}$		245 °C
Average ramp-down ($T_{product}$)			6 °C/s max
Max. time 25° C to peak			8 minutes



Typical soldering profile

For Pb-free solder processes, a pin temperature (T_{pin}) in excess of the solder melting temperature (TL, 217 to 221°C for SnAgCu solder alloys) for more than 60 seconds and a peak temperature of 245 °C on all pins is recommended to ensure a reliable solder joint.

Thermocoupler attachment

$T_{PRODUCT}$ is measured on the baseplate top side since this will likely be the warmest part of the product during the reflow process.
T_{PIN} temperature is measured on the power module output power pins solder joints at the customer board.

Product reflow classification

The product has been tested for the following:

Pb-free solder classification
For Pb-free solder processes, the product is qualified for MSL 3 according to IPC/JEDEC standard J-STD-020E.

Dry pack information

Products intended for Pb-free reflow soldering processes are delivered in standard moisture barrier bags according to IPC/JEDEC standard J-STD-033 (handling, packing, shipping and use of moisture/reflow sensitivity surface mount devices).

Using products in high temperature Pb-free soldering processes requires dry pack storage and handling. In case the products have been stored in an uncontrolled environment and no longer can be considered dry, floor life according to MSL 3, the modules must be baked according to J-STD-033.

Surface mount assembly and repair

The LGA of the product require particular care during assembly since the LGAs are hidden between the host board and the product's PCB. Special procedures are required for successful rework of these products.

Assembly

Automatic pick and place equipment should be used to mount the product on the host board. The use of a vision system, utilizing the fiducials on the bottom side of the product, will ensure adequate accuracy. Manual mounting of solder bump products is not recommended.

Repair

For a successful repair (removal and replacement) of an LGA product, a dedicated rework system should be used. The rework system should preferably utilize a reflow station and a bottom side heater might also be needed for the operation.

The product is a base plate design with a pick-up surface on a large central component (in this case the ferrite). However, use of this pick up surface for removal of the module when it's hot is not recommended. The best method is to use a tool to lift the module by its bottom PCB.

Revision number	revision change	date	revisor
Rev. A	Release	2025-03-05	Team 4
Rev B	Restriction regarding upside-down reflow removed	2025-06-26	Team 4

Technical Reference PMBus BMR 316 XXX1/021

This appendix contains a detailed reference of the PMBus commands supported by the product.

Data Formats

The products make use of a few standardized numerical formats, along with custom data formats. A detailed walkthrough of the above formats is provided in AN304, as well as in sections 7 and 8 of the PMBus Specification Part II. The custom data formats vary depending on the command, and are detailed in the command description.

Standard Commands

The functionality of commands with code 0x00 to 0xCF is usually based on the corresponding command specification provided in the PMBus Standard Specification Part II (see Power System Management Bus Protocol Documents below). However there might be different interpretations of the PMBus Standard Specification or only parts of the Standard Specification applied, thus the detailed command description below should always be consulted.

Forum Websites

The System Management Interface Forum (SMIF)
http://www.powersig.org/
The System Management Interface Forum (SMIF) supports the rapid advancement of an efficient and compatible technology base that promotes power management and systems technology implementations. The SMIF provides a membership path for any company or individual to be active participants in any or all of the various working groups established by the implementer forums.

Power Management Bus Implementers Forum
(PMBUS-IF)
http://pmbus.org/
The PMBus-IF supports the advancement and early adoption of the PMBus protocol for power management. This website offers recent PMBus specification documents, PMBus articles, as well as upcoming PMBus presentations and seminars, PMBus Document Review Board (DRB) meeting notes, and other PMBus related news.

PMBus – Power System Management Bus Protocol Documents

These specification documents may be obtained from the PMBus-IF website described above. These are required reading for complete understanding of the PMBus implementation. This appendix will not re-address all of the details contained within the two PMBus Specification documents.

Specification Part I – General Requirements Transport And Electrical Interface
Includes the general requirements, defines the transport and electrical interface and timing requirements of hard wired signals.

Specification Part II – Command Language
Describes the operation of commands, data formats, fault management and defines the command language used with the PMBus.

SMBus – System Management Bus Documents

System Management Bus Specification, Version 2.0, August 3, 2000
This specification specifies the version of the SMBus on which Revision 1.2 of the PMBus Specification is based. This specification is freely available from the System Management Interface Forum Web site at:
http://www.smbus.org/specs/

PMBus Command Summary and Factory Default Values of Standard Configuration

The factory default values provided in the table below are valid for the Standard configuration. Factory default values for other configurations can be found using the Flex Power Designer tool.

Code	Name	Data Format	Factory Default Value Standard Configuration BMR316XXX1/021 R1		Min Set Value	Max Set Value	Unit
0x01	OPERATION	R/W Byte	0x80				
0x02	ON_OFF_CONFIG	R/W Byte	0x16				
0x03	CLEAR_FAULTS	Send Byte					
0x10	WRITE_PROTECT	R/W Byte					
0x12	RESTORE_DEFAULT_ALL	Send Byte					
0x15	STORE_USER_ALL	Send Byte					
0x16	RESTORE_USER_ALL	Send Byte					
0x19	CAPABILITY	Read Byte	0xB0				
0x1B	SMBALERT_MASK_VOUT (STATUS_VOUT)	SMBAlert Mask	0x6E				
0x1B	SMBALERT_MASK_IOUT (STATUS_IOUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_INPUT (STATUS_INPUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_TEMPERATURE (STATUS_TEMPERATURE)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_CML (STATUS_CML)	SMBAlert Mask	0xFB				
0x1B	SMBALERT_MASK_OTHER (STATUS_OTHER)	SMBAlert Mask	0x01				
0x1B	SMBALERT_MASK_MFR_SPECIFIC (STATUS_MFR_SPECIFIC)	SMBAlert Mask	0x09				
0x20	VOUT_MODE	Read Byte	0x15				
0x35	VIN_ON	R/W Word	0xF094	37.00			V
0x36	VIN_OFF	R/W Word	0xF080	32.00			V
0x37	INTERLEAVE	R/W Word	0x0120				
0x39	IOUT_CAL_OFFSET	Read Word	Unit Specific				
0x40	VOUT_OV_FAULT_LIMIT	R/W Word	0x8200	16.25	0	17	V
0x41	VOUT_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x42	VOUT_OV_WARN_LIMIT	R/W Word	0x7C00	15.50	0	17	V
0x43	VOUT_UV_WARN_LIMIT	R/W Word	0x4400	8.50	0	16	V
0x44	VOUT_UV_FAULT_LIMIT	R/W Word	0x3C00	7.50	0	16	V
0x45	VOUT_UV_FAULT_RESPONSE	R/W Byte	0x80				
0x46	IOUT_OC_FAULT_LIMIT	R/W Word	0x00D2	210.00	0	255	A
0x47	IOUT_OC_FAULT_RESPONSE	R/W Byte	0xC0				
0x48	IOUT_OC_LV_FAULT_LIMIT	R/W Word	0x0000	0.00			V
0x4A	IOUT_OC_WARN_LIMIT	R/W Word	0x005F	95.00	0	255	A
0x4B	IOUT_UC_FAULT_LIMIT	R/W Word	0xE440	-60.00			A
0x4C	IOUT_UC_FAULT_RESPONSE	R/W Byte	0x00				
0x4F	OT_FAULT_LIMIT	R/W Word	0x007D	125.00	-50	150	°C
0x50	OT_FAULT_RESPONSE	R/W Byte	0x80				
0x51	OT_WARN_LIMIT	R/W Word	0x006E	110.00	-50	150	°C
0x52	UT_WARN_LIMIT	R/W Word	0x0000	0.00	-50	150	°C
0x53	UT_FAULT_LIMIT	R/W Word	0xE4E0	-50.00	-50	150	°C
0x54	UT_FAULT_RESPONSE	R/W Byte	0x00				
0x55	VIN_OV_FAULT_LIMIT	R/W Word	0xF104	65.00	0	128	V

0x56	VIN_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x57	VIN_OV_WARN_LIMIT	R/W Word	0xF0F8	62.00	0	128	V
0x58	VIN_UV_WARN_LIMIT	R/W Word	0xF002	0.50	0	128	V
0x59	VIN_UV_FAULT_LIMIT	R/W Word	0xF001	0.25	0	128	V
0x5A	VIN_UV_FAULT_RESPONSE	R/W Byte	0x00				
0x5E	POWER_GOOD_ON	R/W Word	0x4A66	9.30	0	16	V
0x5F	POWER_GOOD_OFF	R/W Word	0x48CD	9.10	0	16	V
0x60	TON_DELAY	R/W Word	0x0000	0.00	0	1023	ms
0x63	TON_MAX_FAULT_RESPONSE	R/W Byte	0x00				
0x64	TOFF_DELAY	R/W Word	0xF804	2.00	0	1023	ms
0x78	STATUS_BYTE	Read Byte					
0x79	STATUS_WORD	Read Word					
0x7A	STATUS_VOUT	Read Byte					
0x7B	STATUS_IOUT	Read Byte					
0x7C	STATUS_INPUT	Read Byte					
0x7D	STATUS_TEMPERATURE	Read Byte					
0x7E	STATUS_CML	Read Byte					
0x7F	STATUS_OTHER	Read Byte					
0x80	STATUS_MFR_SPECIFIC	Read Byte					
0x88	READ_VIN	Read Word					
0x8B	READ_VOUT	Read Word					
0x8C	READ_IOUT	Read Word					
0x8D	READ_TEMPERATURE_1	Read Word					
0x8E	READ_TEMPERATURE_2	Read Word					
0x94	READ_DUTY_CYCLE	Read Word					
0x95	READ_FREQUENCY	Read Word					
0x98	PMBUS_REVISION	Read Byte					
0x99	MFR_ID	Read Block12	Unit Specific				
0x9A	MFR_MODEL	Read Block20	Unit Specific				
0x9B	MFR_REVISION	Read Block12	Unit Specific				
0x9C	MFR_LOCATION	Read Block12	Unit Specific				
0x9D	MFR_DATE	Read Block12	Unit Specific				
0x9E	MFR_SERIAL	Read Block20	Unit Specific				
0xB0	USER_DATA_00	R/W Block16	Unit Specific				
0xC5	FW_CONFIG_REGULATION	Read Block14	0xF800F800D000F800F8000000A000				
0xC8	FW_CONFIG_FAULTS	Read Block25	0x000000000000000000000000000000000000000000000000				
0xC9	FW_CONFIG_PMBUS	R/W Block11	0x000000008800026001 2001				
0xCA	MFR_IOUT_OC_FAST_FAULT_RESPONSE	R/W Byte	0xC0				
0xD0	MFR_IOUT_AVG_OC_FAULT_LIMIT	Read Word	0x0064	100.00			A
0xD1	MFR_IOUT_OC_FAST_FAULT_LIMIT	Read Word	0x00DC	220			A
0xD2	MFR_IOUT_AVG_COEFF	Read Byte	0x1A				
0xDA	MFR_READ_VAUX	Read Word					
0xDC	MFR_SELECT_TEMPERATURE_SENSOR	Read Byte	0x00				
0xE0	MFR_FLEX_FIRMWARE_CMD	R/W Block8					
0xE7	MFR_TEMP_COEFF	Read Word	0x0189				
0xEA	MFR_IOUT_APC	Read Word	Unit Specific				
0xF9	MFR_MULTI_PIN_CONFIG	R/W Word	0x0002				

PMBus Command Details

OPERATION (0x01)
Description: Sets the desired PMBus enable and margin operations.

Bit	Function	Description	Value	Function	Description
7:6	Enable	Make the device enable or disable.	00	Immediate Off	Disable Immediately without sequencing.
			01	Soft Off	Disable "Softly" with sequencing.
			10	Enable	Enable device to the desired margin state.
5:4	Margin	Select between margin high/low states or nominal output.	00	Nominal	Operate at nominal output voltage.
			01	Margin Low	Operate at margin low voltage set in VOUT_MARGIN_LOW.
			10	Margin High	Operate at margin high voltage set in VOUT_MARGIN_HIGH.
3:2	Act on Fault	Set 10b to act on fault or set to 01b to ignore fault.	01	Ignore Faults	Ignore Faults when in a margined state. The device will ignore appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.
			10	Act on Faults	Act on Faults when in a margined state. The device will handle appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.

ON_OFF_CONFIG (0x02)
Description: Configures how the device is controlled by the CONTROL pin and the PMBus.

Bit	Function	Description	Value	Function	Description
4	Powerup Operation	Sets the default to either operate any time power is present or for the on/off to be controlled by CONTROL pin and serial bus commands.	0	Enable Always	Unit powers up any time power is present regardless of state of the CONTROL pin, taking the RC configuration into account, see command 0xE3.
			1	Enable pin or PMBus	Unit does not power up until commanded by the CONTROL pin and OPERATION command.
3	PMBus Enable Mode	Controls how the unit responds to commands received via the serial bus.	0	Ignore PMBus	Unit ignores the on/off portion of the OPERATION command from serial bus.

			1	Use PMBus	To start, the unit requires that the on/off portion of the OPERATION command is instructing the unit to run.
2	Enable Pin Mode	Controls how the unit responds to the CONTROL pin.	0	Ignore pin	Unit ignores the CONTROL/Enable pin.
			1	Use pin	Unit requires the CONTROL pin to be asserted to start the unit.
1	Enable Pin Polarity	Polarity of the CONTROL pin.	0	Active Low	Enable pin will cause device to enable when driven low.
			1	Active High	Enable pin will cause device to enable when driven high.
0	Disable Action	CONTROL pin action when commanding the unit to turn off.	0	Soft Off	Use the programmed turn off delay and fall time.
			1	Imm. Off	Turn off the output and stop transferring energy to the output as fast as possible. The device's product literature shall specify whether or not the device sinks current to decrease the output voltage fall time.

CLEAR_FAULTS (0x03)
Description: Clears all fault status bits

WRITE_PROTECT (0x10)
Description: The WRITE_PROTECT command is used to control writing to the PMBus device. The intent of this command is to provide protection against accidental changes. This command is not intended to provide protection against deliberate or malicious changes to a device's configuration or operation.

Bit	Description	Value	Function	Description
7:0	All supported commands may have their parameters read, regardless of the WRITE_PROTECT settings.	0x80	Disable all writes	Disable all writes except to the WRITE_PROTECT command.
		0x40	Enable operation	Disable all writes except to the WRITE_PROTECT, OPERATION and PAGE commands.
		0x20	Enable control and Vout commands	Disable all writes except to the WRITE_PROTECT, OPERATION, PAGE, ON_OFF_CONFIG and VOUT_COMMAND commands.
		0x00	Enable all commands	Enable writes to all commands.

RESTORE_DEFAULT_ALL (0x12)
Description: Commands the device to restore its configuration from the Default Store.

STORE_USER_ALL (0x15)
Description: Stores, at the USER level, all PMBus values that were changed since the last restore command.

RESTORE_USER_ALL (0x16)
Description: Restores PMBus settings that were stored using STORE_USER_ALL. This command is automatically performed at power up.

CAPABILITY (0x19)
Description: This command provides a way for a host system to determine some key capabilities of a PMBus device.

Bit	Function	Description	Value	Function	Description
7	Packet Error Checking	Packet error checking.	0	Not Supported	Packet Error Checking not supported.
			1	Supported	Packet Error Checking is supported.
6:5	Maximum Bus Speed	Maximum bus speed.	00	100kHz	Maximum supported bus speed is 100 kHz.
			01	400kHz	Maximum supported bus speed is 400 kHz.
			10	1MHz	Maximum supported bus speed is 1 MHz.
4	Smbalert	SMBALERT	00	No Smbalert	The device does not have a SMBALERT# pin and does not support the SMBus Alert Response protocol.
			01	Have Smbalert	The device does have a SMBALERT# pin and does support the SMBus Alert Response protocol.
3	Numeric Format	Numeric format.	0	LINEAR or DIRECT Format	Numeric data is in LINEAR or DIRECT format.
			1	IEEE Half Precision Floating Point Format	Numeric data is in IEEE half precision floating point format.
2	AVSBus Support	AVSBus support.	0	AVSBus Not Supported	AVSBus not supported.
			1	AVSBus Supported	AVSBus supported.

SMBALERT_MASK_VOUT (0x1B)
Status Registers: STATUS_VOUT (0x7A)
Description: SMBALERT_MASK bits for the STATUS_VOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vout Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vout Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vout Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vout Undervoltage Fault		0	Pull SALERT	
			1	Ignore	

3	Vout Max Warning		0	Pull SALERT	
			1	Ignore	
2	Ton Max Fault		0	Pull SALERT	
			1	Ignore	
1	Toff Max Warning		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_IOUT (0x1B)
Status Registers: STATUS_IOUT (0x7B)
Description: SMBALERT_MASK bits for the STATUS_IOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Iout Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Overcurrent And Low Voltage Fault		0	Pull SALERT	
			1	Ignore	
5	Iout Over Current Warning		0	Pull SALERT	
			1	Ignore	
4	Iout Undercurrent Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_INPUT (0x1B)
Status Registers: STATUS_INPUT (0x7C)
Description: SMBALERT_MASK bits for the STATUS_INPUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vin Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vin Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vin Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vin Undervoltage Fault		0	Pull SALERT	
			1	Ignore	
3	Insufficient Vin		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_TEMPERATURE (0x1B)
Status Registers: STATUS_TEMPERATURE (0x7D)
Description: SMBALERT_MASK bits for the STATUS_TEMPERATURE command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Overtemperature Fault		0	Pull SALERT	
			1	Ignore	

6	Overtemperature Warning		0	Pull SALERT	
			1	Ignore	
5	Undertemperature Warning		0	Pull SALERT	
			1	Ignore	
4	Undertemperature Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_CML (0x1B)
Status Registers: STATUS_CML (0x7E)
Description: SMBALERT_MASK bits for the STATUS_CML command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Invalid Or Unsupported Command Received		0	Pull SALERT	
			1	Ignore	
6	Invalid Or Unsupported Data Received		0	Pull SALERT	
			1	Ignore	
5	Packet Error Check Failed		0	Pull SALERT	
			1	Ignore	
4	Memory Fault Detected		0	Pull SALERT	
			1	Ignore	
3	Processor Fault Detected		0	Pull SALERT	
			1	Ignore	
1	Other Communication Fault		0	Pull SALERT	
			1	Ignore	
0	Memory Or Logic Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_OTHER (0x1B)
Status Registers: STATUS_OTHER (0x7F)
Description: SMBALERT_MASK bits for the STATUS_OTHER command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Description	Value	Function	Description
0		0	Pull SALERT	
		1	Ignore	

SMBALERT_MASK_MFR_SPECIFIC (0x1B)
Status Registers: STATUS_MFR_SPECIFIC (0x80)
Description: SMBALERT_MASK bits for the STATUS_MFR_SPECIFIC command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Sync Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Average Overcurrent Fault		0	Pull SALERT	
			1	Ignore	

5	Iout Fast Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
4	Short Circuit Protection Fault		0	Pull SALERT	
			1	Ignore	
3	Overtempera ture2 Fault		0	Pull SALERT	
			1	Ignore	
2	Auxiliary Voltage Fault		0	Pull SALERT	
			1	Ignore	
1	Startup Over Current Fault		0	Pull SALERT	
			1	Ignore	
0	Overtempera ture2 Warn		0	Pull SALERT	
			1	Ignore	

VOUT_MODE (0x20)
Description: Controls how future VOUT-related commands parameters will be interpreted.

Bit	Function	Description	Format
4:0		Five bit two's complement EXPONENT for the MANTISSA delivered as the data bytes for VOUT_COMMAND in VOUT_LINEAR Mode, five bit VID code identifier per in VID Mode or always set to 00000b in Direct Mode.	Integer Signed

Bit	Function	Description	Value	Function	Description
7:5		Set to 000b to select VOUT_LINEAR Mode (Five bit two's complement exponent for the MANTISSA delivered as the data bytes for an output voltage related command), set to 001b to select VID Mode (Five bit VID code identifier per) or set to 010b to select Direct Mode (Always set to 00000b).	000	Linear	Linear Mode Format.
			001	VID	VID Mode.
			010	Direct	Direct Mode.

VIN_ON (0x35)
Description: The VIN_ON command sets the value of the input voltage, in volts, at which the unit should start power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN ON threshold.	Linear	V

VIN_OFF (0x36)
Description: The VIN_OFF command sets the value of the input voltage, in volts, at which the unit, once operation has started, should stop power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN OFF threshold.	Linear	V

INTERLEAVE (0x37)
Description: Configures the phase offset with respect to a common SYNC clock. When multiple products share a common DC input supply, spreading of the switching phases between the products can be utilized. This reduces the input capacitance requirements and efficiency losses, since the peak current drawn from the input supply is effectively spread out over the whole switch period. If two or more units have their outputs connected in parallel, interleaving will reduce ripple currents. This requires that the products are synchronized using the SYNC pin.

Bit	Function	Description	Format
11:8	Group ID Number	Value 0-15. Sets an ID number to a group of interleaved rails.	Integer Unsigned
7:4	Number of Rails	Value 0-15. Sets the number of units in the group, including the SYNC OUT product.	Integer Unsigned
3:0	Rail Position	Value 0-15. Sets the interleave order for this unit. The product configured to SYNC OUT shall be assigned to number 0	Integer Unsigned

IOUT_CAL_OFFSET (0x39)
Description: Sets the current-sense offset.

Bit	Description	Format	Unit
15:0	Sets an offset to IOUT readings. Use to compensate for delayed measurements of current ramp.	Linear	A

VOUT_OV_FAULT_LIMIT (0x40)
Description: Output over voltage fault limit.

Bit	Description	Format	Unit
15:0	Output over voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_OV_FAULT_RESPONSE (0x41)
Description: Output over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VOUT_OV_WARN_LIMIT (0x42)
Description: Output over voltage warning limit.

Bit	Description	Format	Unit
15:0	Output over voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_WARN_LIMIT (0x43)
Description: Output under voltage warning limit.

Bit	Description	Format	Unit
15:0	Output under voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_LIMIT (0x44)
Description: Output under voltage fault limit.

Bit	Description	Format	Unit
15:0	Output under voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_RESPONSE (0x45)
Description: Output under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

		Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

IOUT_OC_FAULT_LIMIT (0x46)
Description: Output over current limit.

Bit	Description	Format	Unit
15:0	Output over current fault limit.	Linear	A

IOUT_OC_FAULT_RESPONSE (0x47)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			01	Conditioned constant current	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT as long as the output voltage remains above the minimum value specified by IOUT_OC_LV_FAULT_LIMIT. If the output voltage is pulled down to less than that value, then the PMBus device shuts down and responds according to the Retry setting in bits [5:3].
			10	Delay w/ Const. Current & Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time		0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	

		Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	6	64	
			7	128	

IOUT_OC_LV_FAULT_LIMIT (0x48)
Description: Set the output over-current low-voltage fault threshold.

Bit	Description	Format	Unit
15:0	Set the output over-current low-voltage fault threshold.	Vout Mode Unsigned (Exp = -11)	V

IOUT_OC_WARN_LIMIT (0x4A)
Description: Output over current warning limit.

Bit	Description	Format	Unit
15:0	Output over current warning limit.	Linear	A

IOUT_UC_FAULT_LIMIT (0x4B)
Description: Sets the output under-current peak limit.

Bit	Description	Format	Unit
15:0	Sets the IOUT under-current peak fault threshold.	Linear	A

IOUT_UC_FAULT_RESPONSE (0x4C)
Description: Configures the output undercurrent fault response. The command format is the same as the PMBus standard responses for voltage and temperature faults except that it sets the undercurrent status bit.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. For all modes set by bits [7:6], the device pulls SALERT low and sets the related fault bit in the status registers.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and Retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retry Setting	The device attempts to restart the number of times set by these bits.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared.
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device (10 ms/unit) is to continue operating after a fault is detected or for the amount of time (8.2 ms/unit) between attempts to restart.	0	0	
			1	1	
			2	2	
			3	3	
			4	4	
			5	5	
			6	6	
			7	7	

OT_FAULT_LIMIT (0x4F)
Description: Over temperature fault limit.

Bit	Description	Format	Unit
15:0	Over temperature fault limit.	Linear	°C

OT_FAULT_RESPONSE (0x50)
Description: Over temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

OT_WARN_LIMIT (0x51)
Description: Over temperature warning limit.

Bit	Description	Format	Unit
15:0	Over temperature warning limit.	Linear	°C

UT_WARN_LIMIT (0x52)
Description: Under temperature warning limit.

Bit	Description	Format	Unit
15:0	Under temperature warning limit.	Linear	°C

UT_FAULT_LIMIT (0x53)
Description: Under temperature fault limit.

Bit	Description	Format	Unit
15:0	Under temperature fault limit.	Linear	°C

UT_FAULT_RESPONSE (0x54)
Description: Under temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_FAULT_LIMIT (0x55)
Description: Input over voltage fault limit.

Bit	Description	Format	Unit
15:0	Input over voltage fault limit.	Linear	V

VIN_OV_FAULT_RESPONSE (0x56)
Description: Input over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_WARN_LIMIT (0x57)
Description: Input over voltage warning limit.

Bit	Description	Format	Unit
15:0	Input over voltage warning limit.	Linear	V

VIN_UV_WARN_LIMIT (0x58)
Description: Input under voltage warning limit. This command set also the input voltage threshold for the HRR function (Hybrid Ratio Regulation). The HRR function is enabled with command MFR_SPECIAL_OPTIONS (0xE0).

Bit	Description	Format	Unit
15:0	Input under voltage warning limit and/or HRR threshold.	Linear	V

VIN_UV_FAULT_LIMIT (0x59)
Description: Input under voltage fault limit.

Bit	Description	Format	Unit
15:0	Input under voltage fault limit.	Linear	V

VIN_UV_FAULT_RESPONSE (0x5A)
Description: Input under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

POWER_GOOD_ON (0x5E)
Description: Sets the output voltage threshold for asserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_ON command sets the output voltage at which an optional POWER_GOOD signal should be asserted.	Vout Mode Unsigned (Exp = -11)	V

POWER_GOOD_OFF (0x5F)
Description: Sets the output voltage threshold for deasserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_OFF command sets the output voltage at which an optional POWER_GOOD signal should be deasserted.	Vout Mode Unsigned (Exp = -11)	V

TON_DELAY (0x60)
Description: Sets the turn-on delay time

Bit	Description	Format	Unit
15:0	Sets the delay time from ENABLE to start of VOUT rise.	Linear	ms

TON_MAX_FAULT_RESPONSE (0x63)
Description: Only some of the response types are supported.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2. TON_MAX_FAULT_RESPONSE time unit is referenced to VOUT FAULT time unit.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

TOFF_DELAY (0x64)
Description: Sets the turn-off delay.

Bit	Description	Format	Unit
15:0	Sets the delay time from DISABLE to start of VOUT fall.	Linear	ms

STATUS_BYTE (0x78)
Description: Returns a brief fault/warning status byte.

Bit	Function	Description	Value	Description
7	Busy	This bit is asserted if the unit is busy	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No fault
			1	Fault
4		An output overcurrent fault has occurred.	0	No fault

	Iout Overcurrent Fault		1	Fault
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No fault
			1	Fault
2	Temperature	A temperature fault or warning has occurred.	0	No fault
			1	Fault
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault
			1	Fault
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault
			1	Fault

STATUS_WORD (0x79)
Description: Returns an extended fault/warning status byte.

Bit	Function	Description	Value	Description
15	Vout	An output voltage fault or warning has occurred.	0	No fault
			1	Fault
14	Iout/Pout	An output current or output power fault or warning has occurred.	0	No Fault.
			1	Fault.
13	Input	An input voltage, input current, or input power fault or warning has occurred.	0	No Fault.
			1	Fault.
12	Mfr Specific	A manufacturer specific fault or warning has occurred.	0	No fault.
			1	Fault.
11	Power-Good	The Power-Good signal, if present, is negated.	0	No Fault.
			1	Fault.
9	Other	A bit in Status-Other is set.	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No Fault.
			1	Fault.
4	Iout Overcurrent Fault	An output overcurrent fault has occurred.	0	No Fault.
			1	Fault.
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No Fault.
			1	Fault.
2	Temperature	A temperature fault or warning has occurred.	0	No Fault.
			1	Fault.
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault.
			1	Fault.
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault.
			1	Fault.

STATUS_VOUT (0x7A)
Description: Returns Vout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vout Overvoltage Fault	Vout Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vout Overvoltage Warning	Vout Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vout Undervoltage Warning	Vout Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vout Undervoltage Fault	Vout Undervoltage Fault.	0	No Fault.
			1	Fault.

3	Vout Max Warning	Vout Max Warning (An attempt has been made to set the output voltage to value higher than allowed by the Vout Max command (Section 13.5).	0	No Warning.
			1	Warning.
2	Ton Max Fault	Ton-Max Fault.	0	No Fault
			1	Fault.
1	Toff Max Warning	Toff Max Warning.	0	No Warning.
			1	Warning.

STATUS_IOUT (0x7B)
Description: Returns Iout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Iout Overcurrent Fault	Iout Overcurrent Fault.	0	No Fault.
			1	Fault.
6	Iout Overcurrent And Low Voltage Fault	Iout Overcurrent and low voltage fault.	0	No Fault.
			1	Fault.
5	Iout Over Current Warning	Iout Overcurrent Warning.	0	No Warning.
			1	Warning.
4	Iout Undercurrent Fault	Iout Undercurrent Fault.	0	No Fault.
			1	Fault.

STATUS_INPUT (0x7C)
Description: Returns VIN/IIN-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vin Overvoltage Fault	Vin Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vin Overvoltage Warning	VIN Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vin Undervoltage Warning	Vin Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vin Undervoltage Fault	Vin Undervoltage Fault.	0	No Fault.
			1	Fault.
3	Insufficient Vin	Asserted when either the input voltage has never exceeded the input turn-on threshold Vin-On, or if the unit did start, the input voltage decreased below the turn-off threshold.	0	No Insufficient VIN encountered yet.
			1	Insufficient Unit is off.

STATUS_TEMPERATURE (0x7D)
Description: Returns the temperature-related fault/warning status bits

Bit	Function	Description	Value	Description
7	Overtemperature Fault	Overtemperature Fault.	0	No Fault.
			1	Fault.
6	Overtemperature Warning	Overtemperature Warning.	0	No Warning.
			1	Warning.
5	Undertemperature Warning	Undertemperature Warning.	0	No Warning.
			1	Warning.
4	Undertemperature Fault	Undertemperature Fault.	0	No Fault.
			1	Fault.

STATUS_CML (0x7E)
Description: Returns Communication/Logic/Memory-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Invalid Or Unsupported Command Received	Invalid Or Unsupported Command Received.	0	No Invalid Command Received.
			1	Invalid Command Received.
6	Invalid Or Unsupported Data Received	Invalid Or Unsupported Data Received.	0	No Invalid Data Received.
			1	Invalid Data Received.
5	Packet Error Check Failed	Packet Error Check Failed.	0	No Failure.
			1	Failure.
4	Memory Fault Detected	Memory Fault Detected.	0	No Fault.
			1	Fault.
3	Processor Fault Detected	Processor fault detected.	0	No Fault.
			1	Fault.
1	Other Communication Fault	A communication fault other than the ones listed in this table has occurred.	0	No Fault.
			1	Fault.
0	Memory Or Logic Fault	Other Memory Or Logic Fault has occurred.	0	No Fault.
			1	Fault.

STATUS_OTHER (0x7F)
Description: Returns a brief other fault/warning status bits.

Bit	Description	Value	Description
0	The device was the first to assert SMBALERT.		

STATUS_MFR_SPECIFIC (0x80)
Description: Returns manufacturer specific status information.

Bit	Function	Description	Value	Description
7	Sync Fault	Sync fault.	0	No fault.
			1	Fault.
6	Iout Average Overcurrent Fault	Iout average overcurrent fault.	0	No fault.
			1	Fault.
5	Iout Fast Overcurrent Fault	Iout fast overcurrent fault.	0	No fault.
			1	Fault.
4	Short Circuit Protection Fault	Short circuit protection fault.	0	No fault.
			1	Fault.
3	Overtemperature2 Fault	Overtemperature2 fault.	0	No fault.
			1	Fault.
2	Auxiliary Voltage Fault	Auxiliary voltage fault.	0	No fault.
			1	Fault.
1	Startup Over Current Fault	Startup over current fault.	0	No fault.
			1	Fault.
0	Overtemperature2 Warn	Overtemperature2 warn.	0	No fault.
			1	Fault.

READ_VIN (0x88)
Description: Returns the measured input voltage.

Bit	Description	Format	Unit
15:0	Returns the input voltage reading.	Linear	V

READ_VOUT (0x8B)
Description: Returns the measured output voltage.

Bit	Description	Format	Unit
15:0	Returns the measured output voltage.	Vout Mode Unsigned (Exp = -11)	V

READ_IOUT (0x8C)
Description: Returns the measured output current.

Bit	Description	Format	Unit
15:0	Returns the measured output current.	Linear	A

READ_TEMPERATURE_1 (0x8D)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_TEMPERATURE_2 (0x8E)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_DUTY_CYCLE (0x94)
Description: Returns the actual duty cycle in percent.

Bit	Description	Format	Unit
15:0	Returns the actual duty cycle in percent.	Linear	%

READ_FREQUENCY (0x95)
Description: Returns the actual switching frequency.

Bit	Description	Format	Unit
15:0	Returns the actual switching frequency.	Linear	kHz

PMBUS_REVISION (0x98)
Description: Returns the PMBus revision number for this device.

Bit	Function	Description	Value	Function	Description
7:4	Part I Revision	Part I Revision.	0x0	1.0	Part I Revision 1.0.
			0x1	1.1	Part I Revision 1.1.
			0x2	1.2	Part I Revision 1.2.
			0x3	1.3	Part I Revision 1.3.
3:0	Part II Revision	Part II Revision.	0x0	1.0	Part II Revision 1.0.
			0x1	1.1	Part II Revision 1.1.
			0x2	1.2	Part II Revision 1.2.
			0x3	1.3	Part II Revision 1.3.

MFR_ID (0x99)
Description: Sets the Manufacturers ID

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_MODEL (0x9A)
Description: Sets the MFR MODEL string.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

MFR_REVISION (0x9B)
Description: Sets the MFR revision string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_LOCATION (0x9C)
Description: Sets the MFR location string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_DATE (0x9D)
Description: This command returns the date the regulator was manufactured.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_SERIAL (0x9E)
Description: This command returns a string of 13 characters and numbers that provides a unique identification of the regulator.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

USER_DATA_00 (0xB0)
Description: This command is available as generic read/write storage for customers.

Bit	Description	Format
127:0	16 bytes of user data.	Byte Array

FW_CONFIG_REGULATION (0xC5)
Description: FW CONFIG REGULATION parameter

Bit	Description	Value	Function	Description
0	Enable diode emulation at startup	0	Disabled	
		1	Enabled	

FW_CONFIG_FAULTS (0xC8)
Description: FW CONFIG FAULTS parameter

Bit	Function	Description	Value	Function	Description

7:6	Vout Delay Unit	Vout_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vout Delay Unit Time unit for retry responses
			01	4ms/unit	Vout Delay Unit Time unit for retry responses
			10	16ms/unit	Vout Delay Unit Time unit for retry responses
			11	256ms/unit	Vout Delay Unit Time unit for retry responses
5:4	Vin Delay Unit	Vin_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vin Delay Unit Time unit for retry responses
			01	4ms/unit	Vin Delay Unit Time unit for retry responses
			10	16ms/unit	Vin Delay Unit Time unit for retry responses
			11	256ms/unit	Vin Delay Unit Time unit for retry responses
3:2	Iout Delay Unit	IOUT_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	IOUT Delay Unit Time unit for retry responses
			01	4ms/unit	IOUT Delay Unit Time unit for retry responses
			10	16ms/unit	IOUT Delay Unit Time unit for retry responses
			11	256ms/unit	IOUT Delay Unit Time unit for retry responses
1:0	Temperature Delay Unit	Temperature_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Temperature Delay Unit Time unit for retry responses
			01	4ms/unit	Temperature Delay Unit Time unit for retry responses
			10	16ms/unit	Temperature Delay Unit Time unit for retry responses
			11	256ms/unit	Temperature Delay Unit Time unit for retry responses

FW_CONFIG_PMBUS (0xC9)
Description: This command contains various configurable settings related to PMBus address and digital pins.

Bit	Function	Description	Format
31:24	PMBus Base Addr	Base Address for PMBus offset to start from	Integer Unsigned
23:17	PMBus Addr Offset	PMBUS Address offset when resistor offset Not enabled	Integer Unsigned

Bit	Function	Description	Value	Function	Description
33	Power good polarity	Power good polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
32	Control pin polarity	Control pin polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
16	PMBus Addr Offset Resistor Enable	PMBus_addr_offset_enable Enable PMBUS Address Offset via resistor	0	Disabled	
			1	Enabled	

MFR_IOUT_OC_FAST_FAULT_RESPONSE (0xCA)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			11	Shutdown and Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAST_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAST_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xC8.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

MFR_IOUT_AVG_OC_FAULT_LIMIT (0xD0)
Description: Average output over current limit.

Bit	Description	Format	Unit
15:0	Average output over current fault limit.	Linear	A

MFR_IOUT_OC_FAST_FAULT_LIMIT (0xD1)
Description: The MFR_IOUT_OC_FAST_FAULT_LIMIT command sets or retrieves Iout fast overcurrent fault threshold, in Amperes.

Bit	Description	Format	Unit
15:0	Sets Iout fast over-current fault threshold.	Integer Unsigned	A

MFR_IOUT_AVG_COEFF (0xD2)
Description: Coefficient for controlling the averaging strength for the averaged Iout current limit.

Bit	Description	Format
5:0	Coefficient for controlling the averaging strength for the averaged Iout current limit. Value 0-63. Setting the coefficient to 0 will disable the averaging and the average current limit behaviour.	Integer Unsigned

MFR_READ_VAUX (0xDA)
Description: Returns the measured auxiliary input voltage.

Bit	Description	Format	Unit
15:0	Returns the auxiliary input voltage reading.	Linear	V

MFR_SELECT_TEMPERATURE_SENSOR (0xDC)
Description: Select which temperature sensor, internal one or external remote temperature sensor, is used.

Bit	Function	Description	Value	Function	Description
4:3	Fault Source Select	Select which temperature sensor, internal one or external remote temperature sensor, is used.	00	Temp A	Temp A temperature sensor selected.
			01	Temp B	Temp B temperature sensor selected.
			10	Temp I	Temp I temperature sensor selected.
2:0	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select.	000	TempA TempB	TempA (External Temperature sensor A) TempB (External Temperature sensor B).
			001	TempA TempI	TempA (External Temperature sensor A) TempI (Internal Temperature sensor).
			010	TempB TempA	TempB (External Temperature sensor B) TempA (External Temperature sensor A).
			011	TempB TempI	TempB (External Temperature sensor B) TempI (Internal Temperature sensor).
			100	TempI TempA	TempI (Internal Temperature sensor) TempA (External Temperature sensor A).
			101	TempI TempB	TempI (Internal Temperature sensor) TempB (External Temperature sensor B).

MFR_FLEX_FIRMWARE_CMD (0xE0)
Description: Mfr. firmware command.

Bit	Description	Format
63:0	Mfr. firmware command.	Byte Array

MFR_TEMP_COEFF (0xE7)
Description: Coefficient for Iout temperature compensation.

Bit	Description	Format
15:0	Coefficient in Q16. Iout compensation factor calculated according to: 1 / (1 + t_coeff * (T - 20))	Fixed Point Unsigned

MFR_IOUT_APC (0xEA)
Description: The iout apc gain.

Bit	Description	Format	Unit
15:0	SSet the iout apc gain. the format is Linear 11, Exponent is -9 or -8 (User selection possible). The LSB varies with isen_gain_mode - ISEN_LSB/Secondary current sense resistor (Rsense).	Linear	A

MFR_MULTI_PIN_CONFIG (0xF9)
Description: The MFR_MULTI_PIN_CONFIG command can be re-configured to enable or disable different functions and set the pin configuration.

Bit	Function	Description	Value	Function	Description
7	SMBAlert pin function	Selects if the SMBAlert pin should be used for ALERT or SYNC.	00	Alert	SMBAlert pin used as SMBAlert.
			01	Sync	SMBAlert pin used as SYNC.
6:5	Sync Mode	These bits configures the direction of the sync pin as either SYNC OUT or SYNC IN. Use the INTERLEAVE command to enable/disable the sync function and to configure phase offset.	00	Disabled	Sync Pin disabled.
			01	Sync in	When the product is configured as SYNC IN it will synchronize its switching frequency to an external sync signal. The switching phases can be spread individually using the INTERLEAVE command 0x37.
			10	Sync out	When the product is configured as SYNC OUT it will send out a SYNC signal. Only 1 product in a group can be configured as SYNC OUT.
4	Sync Output	Selects the output type of the Sync pin.	0	Open Drain	Sync output configured as Open Drain.
2	Power Good Pull-down	This bit enables or disables Power Good pin pull-down.	0	Disabled	
			1	Enabled	
1	Power Good Output	Two output options are available for Power Good output, they are Push/Pull or Open Drain.	0	Push/Pull	Power Good configured Push/Pull.
			1	Open Drain	Power Good configured Open Drain.

Technical Reference PMBus BMR 316 XXX1/022

This appendix contains a detailed reference of the PMBus commands supported by the product.

Data Formats

The products make use of a few standardized numerical formats, along with custom data formats. A detailed walkthrough of the above formats is provided in AN304, as well as in sections 7 and 8 of the PMBus Specification Part II. The custom data formats vary depending on the command, and are detailed in the command description.

Standard Commands

The functionality of commands with code 0x00 to 0xCF is usually based on the corresponding command specification provided in the PMBus Standard Specification Part II (see Power System Management Bus Protocol Documents below). However there might be different interpretations of the PMBus Standard Specification or only parts of the Standard Specification applied, thus the detailed command description below should always be consulted.

Forum Websites

The System Management Interface Forum (SMIF)
http://www.powersig.org/
The System Management Interface Forum (SMIF) supports the rapid advancement of an efficient and compatible technology base that promotes power management and systems technology implementations. The SMIF provides a membership path for any company or individual to be active participants in any or all of the various working groups established by the implementer forums.

Power Management Bus Implementers Forum
(PMBUS-IF)
http://pmbus.org/
The PMBus-IF supports the advancement and early adoption of the PMBus protocol for power management. This website offers recent PMBus specification documents, PMBus articles, as well as upcoming PMBus presentations and seminars, PMBus Document Review Board (DRB) meeting notes, and other PMBus related news.

PMBus – Power System Management Bus Protocol Documents

These specification documents may be obtained from the PMBus-IF website described above. These are required reading for complete understanding of the PMBus implementation. This appendix will not re-address all of the details contained within the two PMBus Specification documents.

Specification Part I – General Requirements Transport And Electrical Interface
Includes the general requirements, defines the transport and electrical interface and timing requirements of hard wired signals.

Specification Part II – Command Language
Describes the operation of commands, data formats, fault management and defines the command language used with the PMBus.

SMBus – System Management Bus Documents

System Management Bus Specification, Version 2.0, August 3, 2000
This specification specifies the version of the SMBus on which Revision 1.2 of the PMBus Specification is based. This specification is freely available from the System Management Interface Forum Web site at:
http://www.smbus.org/specs/

PMBus Command Summary and Factory Default Values of Standard Configuration

The factory default values provided in the table below are valid for the Standard configuration. Factory default values for other configurations can be found using the Flex Power Designer tool.

Code	Name	Data Format	Factory Default Value Standard Configuration BMR316XXX1/022 R1		Min Set Value	Max Set Value	Unit
0x01	OPERATION	R/W Byte	0x80				
0x02	ON_OFF_CONFIG	R/W Byte	0x16				
0x03	CLEAR_FAULTS	Send Byte					
0x10	WRITE_PROTECT	R/W Byte					
0x12	RESTORE_DEFAULT_ALL	Send Byte					
0x15	STORE_USER_ALL	Send Byte					
0x16	RESTORE_USER_ALL	Send Byte					
0x19	CAPABILITY	Read Byte	0xB0				
0x1B	SMBALERT_MASK_VOUT (STATUS_VOUT)	SMBAlert Mask	0x6E				
0x1B	SMBALERT_MASK_IOUT (STATUS_IOUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_INPUT (STATUS_INPUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_TEMPERATURE (STATUS_TEMPERATURE)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_CML (STATUS_CML)	SMBAlert Mask	0xFB				
0x1B	SMBALERT_MASK_OTHER (STATUS_OTHER)	SMBAlert Mask	0x01				
0x1B	SMBALERT_MASK_MFR_SPECIFIC (STATUS_MFR_SPECIFIC)	SMBAlert Mask	0x09				
0x20	VOUT_MODE	Read Byte	0x15				
0x35	VIN_ON	R/W Word	0xF094	37.00			V
0x36	VIN_OFF	R/W Word	0xF080	32.00			V
0x37	INTERLEAVE	R/W Word	0x0120				
0x39	IOUT_CAL_OFFSET	Read Word	Unit Specific				
0x40	VOUT_OV_FAULT_LIMIT	R/W Word	0x8200	16.25	0	17	V
0x41	VOUT_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x42	VOUT_OV_WARN_LIMIT	R/W Word	0x7C00	15.50	0	17	V
0x43	VOUT_UV_WARN_LIMIT	R/W Word	0x4400	8.50	0	16	V
0x44	VOUT_UV_FAULT_LIMIT	R/W Word	0x3C00	7.50	0	16	V
0x45	VOUT_UV_FAULT_RESPONSE	R/W Byte	0x80				
0x46	IOUT_OC_FAULT_LIMIT	R/W Word	0x00D2	210.00	0	255	A
0x47	IOUT_OC_FAULT_RESPONSE	R/W Byte	0xC0				
0x48	IOUT_OC_LV_FAULT_LIMIT	R/W Word	0x0000	0.00			V
0x4A	IOUT_OC_WARN_LIMIT	R/W Word	0x005F	95.00	0	255	A
0x4B	IOUT_UC_FAULT_LIMIT	R/W Word	0xE440	-60.00			A
0x4C	IOUT_UC_FAULT_RESPONSE	R/W Byte	0x00				
0x4F	OT_FAULT_LIMIT	R/W Word	0x007D	125.00	-50	150	°C
0x50	OT_FAULT_RESPONSE	R/W Byte	0x80				
0x51	OT_WARN_LIMIT	R/W Word	0x006E	110.00	-50	150	°C
0x52	UT_WARN_LIMIT	R/W Word	0x0000	0.00	-50	150	°C
0x53	UT_FAULT_LIMIT	R/W Word	0xE4E0	-50.00	-50	150	°C
0x54	UT_FAULT_RESPONSE	R/W Byte	0x00				
0x55	VIN_OV_FAULT_LIMIT	R/W Word	0xF104	65.00	0	128	V

0x56	VIN_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x57	VIN_OV_WARN_LIMIT	R/W Word	0xF0F8	62.00	0	128	V
0x58	VIN_UV_WARN_LIMIT	R/W Word	0xF002	0.50	0	128	V
0x59	VIN_UV_FAULT_LIMIT	R/W Word	0xF001	0.25	0	128	V
0x5A	VIN_UV_FAULT_RESPONSE	R/W Byte	0x00				
0x5E	POWER_GOOD_ON	R/W Word	0x4A66	9.30	0	16	V
0x5F	POWER_GOOD_OFF	R/W Word	0x48CD	9.10	0	16	V
0x60	TON_DELAY	R/W Word	0x0000	0.00	0	1023	ms
0x63	TON_MAX_FAULT_RESPONSE	R/W Byte	0x00				
0x64	TOFF_DELAY	R/W Word	0xF804	2.00	0	1023	ms
0x78	STATUS_BYTE	Read Byte					
0x79	STATUS_WORD	Read Word					
0x7A	STATUS_VOUT	Read Byte					
0x7B	STATUS_IOUT	Read Byte					
0x7C	STATUS_INPUT	Read Byte					
0x7D	STATUS_TEMPERATURE	Read Byte					
0x7E	STATUS_CML	Read Byte					
0x7F	STATUS_OTHER	Read Byte					
0x80	STATUS_MFR_SPECIFIC	Read Byte					
0x88	READ_VIN	Read Word					
0x8B	READ_VOUT	Read Word					
0x8C	READ_IOUT	Read Word					
0x8D	READ_TEMPERATURE_1	Read Word					
0x8E	READ_TEMPERATURE_2	Read Word					
0x94	READ_DUTY_CYCLE	Read Word					
0x95	READ_FREQUENCY	Read Word					
0x98	PMBUS_REVISION	Read Byte					
0x99	MFR_ID	Read Block12	Unit Specific				
0x9A	MFR_MODEL	Read Block20	Unit Specific				
0x9B	MFR_REVISION	Read Block12	Unit Specific				
0x9C	MFR_LOCATION	Read Block12	Unit Specific				
0x9D	MFR_DATE	Read Block12	Unit Specific				
0x9E	MFR_SERIAL	Read Block20	Unit Specific				
0xB0	USER_DATA_00	R/W Block16	Unit Specific				
0xC5	FW_CONFIG_REGULATION	Read Block14	0xF800F800D000F800F8000000A000				
0xC8	FW_CONFIG_FAULTS	Read Block25	0x000000000000000000000000000000000000000000000000				
0xC9	FW_CONFIG_PMBUS	R/W Block11	0x000000008800021001 2001				
0xCA	MFR_IOUT_OC_FAST_FAULT_RESPONSE	R/W Byte	0xC0				
0xD0	MFR_IOUT_AVG_OC_FAULT_LIMIT	Read Word	0x0064	100.00			A
0xD1	MFR_IOUT_OC_FAST_FAULT_LIMIT	Read Word	0x00DC	220			A
0xD2	MFR_IOUT_AVG_COEFF	Read Byte	0x1A				
0xDA	MFR_READ_VAUX	Read Word					
0xDC	MFR_SELECT_TEMPERATURE_SENSOR	Read Byte	0x00				
0xE0	MFR_FLEX_FIRMWARE_CMD	R/W Block8					
0xE7	MFR_TEMP_COEFF	Read Word	0x0189				
0xEA	MFR_IOUT_APC	Read Word	Unit Specific				
0xF9	MFR_MULTI_PIN_CONFIG	R/W Word	0x0002				

PMBus Command Details

OPERATION (0x01)
Description: Sets the desired PMBus enable and margin operations.

Bit	Function	Description	Value	Function	Description
7:6	Enable	Make the device enable or disable.	00	Immediate Off	Disable Immediately without sequencing.
			01	Soft Off	Disable "Softly" with sequencing.
			10	Enable	Enable device to the desired margin state.
5:4	Margin	Select between margin high/low states or nominal output.	00	Nominal	Operate at nominal output voltage.
			01	Margin Low	Operate at margin low voltage set in VOUT_MARGIN_LOW.
			10	Margin High	Operate at margin high voltage set in VOUT_MARGIN_HIGH.
3:2	Act on Fault	Set 10b to act on fault or set to 01b to ignore fault.	01	Ignore Faults	Ignore Faults when in a margined state. The device will ignore appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.
			10	Act on Faults	Act on Faults when in a margined state. The device will handle appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.

ON_OFF_CONFIG (0x02)
Description: Configures how the device is controlled by the CONTROL pin and the PMBus.

Bit	Function	Description	Value	Function	Description
4	Powerup Operation	Sets the default to either operate any time power is present or for the on/off to be controlled by CONTROL pin and serial bus commands.	0	Enable Always	Unit powers up any time power is present regardless of state of the CONTROL pin, taking the RC configuration into account, see command 0xE3.
			1	Enable pin or PMBus	Unit does not power up until commanded by the CONTROL pin and OPERATION command.
3	PMBus Enable Mode	Controls how the unit responds to commands received via the serial bus.	0	Ignore PMBus	Unit ignores the on/off portion of the OPERATION command from serial bus.

			1	Use PMBus	To start, the unit requires that the on/off portion of the OPERATION command is instructing the unit to run.
2	Enable Pin Mode	Controls how the unit responds to the CONTROL pin.	0	Ignore pin	Unit ignores the CONTROL/Enable pin.
			1	Use pin	Unit requires the CONTROL pin to be asserted to start the unit.
1	Enable Pin Polarity	Polarity of the CONTROL pin.	0	Active Low	Enable pin will cause device to enable when driven low.
			1	Active High	Enable pin will cause device to enable when driven high.
0	Disable Action	CONTROL pin action when commanding the unit to turn off.	0	Soft Off	Use the programmed turn off delay and fall time.
			1	Imm. Off	Turn off the output and stop transferring energy to the output as fast as possible. The device's product literature shall specify whether or not the device sinks current to decrease the output voltage fall time.

CLEAR_FAULTS (0x03)
Description: Clears all fault status bits

WRITE_PROTECT (0x10)
Description: The WRITE_PROTECT command is used to control writing to the PMBus device. The intent of this command is to provide protection against accidental changes. This command is not intended to provide protection against deliberate or malicious changes to a device's configuration or operation.

Bit	Description	Value	Function	Description
7:0	All supported commands may have their parameters read, regardless of the WRITE_PROTECT settings.	0x80	Disable all writes	Disable all writes except to the WRITE_PROTECT command.
		0x40	Enable operation	Disable all writes except to the WRITE_PROTECT, OPERATION and PAGE commands.
		0x20	Enable control and Vout commands	Disable all writes except to the WRITE_PROTECT, OPERATION, PAGE, ON_OFF_CONFIG and VOUT_COMMAND commands.
		0x00	Enable all commands	Enable writes to all commands.

RESTORE_DEFAULT_ALL (0x12)
Description: Commands the device to restore its configuration from the Default Store.

STORE_USER_ALL (0x15)
Description: Stores, at the USER level, all PMBus values that were changed since the last restore command.

RESTORE_USER_ALL (0x16)
Description: Restores PMBus settings that were stored using STORE_USER_ALL. This command is automatically performed at power up.

CAPABILITY (0x19)
Description: This command provides a way for a host system to determine some key capabilities of a PMBus device.

Bit	Function	Description	Value	Function	Description
7	Packet Error Checking	Packet error checking.	0	Not Supported	Packet Error Checking not supported.
			1	Supported	Packet Error Checking is supported.
6:5	Maximum Bus Speed	Maximum bus speed.	00	100kHz	Maximum supported bus speed is 100 kHz.
			01	400kHz	Maximum supported bus speed is 400 kHz.
			10	1MHz	Maximum supported bus speed is 1 MHz.
4	Smbalert	SMBALERT	00	No Smbalert	The device does not have a SMBALERT# pin and does not support the SMBus Alert Response protocol.
			01	Have Smbalert	The device does have a SMBALERT# pin and does support the SMBus Alert Response protocol.
3	Numeric Format	Numeric format.	0	LINEAR or DIRECT Format	Numeric data is in LINEAR or DIRECT format.
			1	IEEE Half Precision Floating Point Format	Numeric data is in IEEE half precision floating point format.
2	AVSBus Support	AVSBus support.	0	AVSBus Not Supported	AVSBus not supported.
			1	AVSBus Supported	AVSBus supported.

SMBALERT_MASK_VOUT (0x1B)
Status Registers: STATUS_VOUT (0x7A)
Description: SMBALERT_MASK bits for the STATUS_VOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vout Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vout Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vout Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vout Undervoltage Fault		0	Pull SALERT	
			1	Ignore	

3	Vout Max Warning		0	Pull SALERT	
			1	Ignore	
2	Ton Max Fault		0	Pull SALERT	
			1	Ignore	
1	Toff Max Warning		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_IOUT (0x1B)
Status Registers: STATUS_IOUT (0x7B)
Description: SMBALERT_MASK bits for the STATUS_IOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Iout Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Overcurrent And Low Voltage Fault		0	Pull SALERT	
			1	Ignore	
5	Iout Over Current Warning		0	Pull SALERT	
			1	Ignore	
4	Iout Undercurrent Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_INPUT (0x1B)
Status Registers: STATUS_INPUT (0x7C)
Description: SMBALERT_MASK bits for the STATUS_INPUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vin Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vin Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vin Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vin Undervoltage Fault		0	Pull SALERT	
			1	Ignore	
3	Insufficient Vin		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_TEMPERATURE (0x1B)
Status Registers: STATUS_TEMPERATURE (0x7D)
Description: SMBALERT_MASK bits for the STATUS_TEMPERATURE command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Overtemperature Fault		0	Pull SALERT	
			1	Ignore	

6	Overtemperature Warning		0	Pull SALERT	
			1	Ignore	
5	Undertemperature Warning		0	Pull SALERT	
			1	Ignore	
4	Undertemperature Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_CML (0x1B)
Status Registers: STATUS_CML (0x7E)
Description: SMBALERT_MASK bits for the STATUS_CML command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Invalid Or Unsupported Command Received		0	Pull SALERT	
			1	Ignore	
6	Invalid Or Unsupported Data Received		0	Pull SALERT	
			1	Ignore	
5	Packet Error Check Failed		0	Pull SALERT	
			1	Ignore	
4	Memory Fault Detected		0	Pull SALERT	
			1	Ignore	
3	Processor Fault Detected		0	Pull SALERT	
			1	Ignore	
1	Other Communication Fault		0	Pull SALERT	
			1	Ignore	
0	Memory Or Logic Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_OTHER (0x1B)
Status Registers: STATUS_OTHER (0x7F)
Description: SMBALERT_MASK bits for the STATUS_OTHER command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Description	Value	Function	Description
0		0	Pull SALERT	
		1	Ignore	

SMBALERT_MASK_MFR_SPECIFIC (0x1B)
Status Registers: STATUS_MFR_SPECIFIC (0x80)
Description: SMBALERT_MASK bits for the STATUS_MFR_SPECIFIC command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Sync Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Average Overcurrent Fault		0	Pull SALERT	
			1	Ignore	

5	Iout Fast Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
4	Short Circuit Protection Fault		0	Pull SALERT	
			1	Ignore	
3	Overtempera ture2 Fault		0	Pull SALERT	
			1	Ignore	
2	Auxiliary Voltage Fault		0	Pull SALERT	
			1	Ignore	
1	Startup Over Current Fault		0	Pull SALERT	
			1	Ignore	
0	Overtempera ture2 Warn		0	Pull SALERT	
			1	Ignore	

VOUT_MODE (0x20)
Description: Controls how future VOUT-related commands parameters will be interpreted.

Bit	Function	Description	Format
4:0		Five bit two's complement EXPONENT for the MANTISSA delivered as the data bytes for VOUT_COMMAND in VOUT_LINEAR Mode, five bit VID code identifier per in VID Mode or always set to 00000b in Direct Mode.	Integer Signed

Bit	Function	Description	Value	Function	Description
7:5		Set to 000b to select VOUT_LINEAR Mode (Five bit two's complement exponent for the MANTISSA delivered as the data bytes for an output voltage related command), set to 001b to select VID Mode (Five bit VID code identifier per) or set to 010b to select Direct Mode (Always set to 00000b).	000	Linear	Linear Mode Format.
			001	VID	VID Mode.
			010	Direct	Direct Mode.

VIN_ON (0x35)
Description: The VIN_ON command sets the value of the input voltage, in volts, at which the unit should start power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN ON threshold.	Linear	V

VIN_OFF (0x36)
Description: The VIN_OFF command sets the value of the input voltage, in volts, at which the unit, once operation has started, should stop power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN OFF threshold.	Linear	V

INTERLEAVE (0x37)
Description: Configures the phase offset with respect to a common SYNC clock. When multiple products share a common DC input supply, spreading of the switching phases between the products can be utilized. This reduces the input capacitance requirements and efficiency losses, since the peak current drawn from the input supply is effectively spread out over the whole switch period. If two or more units have their outputs connected in parallel, interleaving will reduce ripple currents. This requires that the products are synchronized using the SYNC pin.

Bit	Function	Description	Format
11:8	Group ID Number	Value 0-15. Sets an ID number to a group of interleaved rails.	Integer Unsigned
7:4	Number of Rails	Value 0-15. Sets the number of units in the group, including the SYNC OUT product.	Integer Unsigned
3:0	Rail Position	Value 0-15. Sets the interleave order for this unit. The product configured to SYNC OUT shall be assigned to number 0	Integer Unsigned

IOUT_CAL_OFFSET (0x39)
Description: Sets the current-sense offset.

Bit	Description	Format	Unit
15:0	Sets an offset to IOUT readings. Use to compensate for delayed measurements of current ramp.	Linear	A

VOUT_OV_FAULT_LIMIT (0x40)
Description: Output over voltage fault limit.

Bit	Description	Format	Unit
15:0	Output over voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_OV_FAULT_RESPONSE (0x41)
Description: Output over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VOUT_OV_WARN_LIMIT (0x42)
Description: Output over voltage warning limit.

Bit	Description	Format	Unit
15:0	Output over voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_WARN_LIMIT (0x43)
Description: Output under voltage warning limit.

Bit	Description	Format	Unit
15:0	Output under voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_LIMIT (0x44)
Description: Output under voltage fault limit.

Bit	Description	Format	Unit
15:0	Output under voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_RESPONSE (0x45)
Description: Output under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

		Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

IOUT_OC_FAULT_LIMIT (0x46)
Description: Output over current limit.

Bit	Description	Format	Unit
15:0	Output over current fault limit.	Linear	A

IOUT_OC_FAULT_RESPONSE (0x47)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			01	Conditioned constant current	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT as long as the output voltage remains above the minimum value specified by IOUT_OC_LV_FAULT_LIMIT. If the output voltage is pulled down to less than that value, then the PMBus device shuts down and responds according to the Retry setting in bits [5:3].
			10	Delay w/ Const. Current & Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time		0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	

		Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	6	64	
			7	128	

IOUT_OC_LV_FAULT_LIMIT (0x48)
Description: Set the output over-current low-voltage fault threshold.

Bit	Description	Format	Unit
15:0	Set the output over-current low-voltage fault threshold.	Vout Mode Unsigned (Exp = -11)	V

IOUT_OC_WARN_LIMIT (0x4A)
Description: Output over current warning limit.

Bit	Description	Format	Unit
15:0	Output over current warning limit.	Linear	A

IOUT_UC_FAULT_LIMIT (0x4B)
Description: Sets the output under-current peak limit.

Bit	Description	Format	Unit
15:0	Sets the IOUT under-current peak fault threshold.	Linear	A

IOUT_UC_FAULT_RESPONSE (0x4C)
Description: Configures the output undercurrent fault response. The command format is the same as the PMBus standard responses for voltage and temperature faults except that it sets the undercurrent status bit.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. For all modes set by bits [7:6], the device pulls SALERT low and sets the related fault bit in the status registers.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and Retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retry Setting	The device attempts to restart the number of times set by these bits.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared.
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device (10 ms/unit) is to continue operating after a fault is detected or for the amount of time (8.2 ms/unit) between attempts to restart.	0	0	
			1	1	
			2	2	
			3	3	
			4	4	
			5	5	
			6	6	
			7	7	

OT_FAULT_LIMIT (0x4F)
Description: Over temperature fault limit.

Bit	Description	Format	Unit
15:0	Over temperature fault limit.	Linear	°C

OT_FAULT_RESPONSE (0x50)
Description: Over temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

OT_WARN_LIMIT (0x51)
Description: Over temperature warning limit.

Bit	Description	Format	Unit
15:0	Over temperature warning limit.	Linear	°C

UT_WARN_LIMIT (0x52)
Description: Under temperature warning limit.

Bit	Description	Format	Unit
15:0	Under temperature warning limit.	Linear	°C

UT_FAULT_LIMIT (0x53)
Description: Under temperature fault limit.

Bit	Description	Format	Unit
15:0	Under temperature fault limit.	Linear	°C

UT_FAULT_RESPONSE (0x54)
Description: Under temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_FAULT_LIMIT (0x55)
Description: Input over voltage fault limit.

Bit	Description	Format	Unit
15:0	Input over voltage fault limit.	Linear	V

VIN_OV_FAULT_RESPONSE (0x56)
Description: Input over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_WARN_LIMIT (0x57)
Description: Input over voltage warning limit.

Bit	Description	Format	Unit
15:0	Input over voltage warning limit.	Linear	V

VIN_UV_WARN_LIMIT (0x58)
Description: Input under voltage warning limit. This command set also the input voltage threshold for the HRR function (Hybrid Ratio Regulation). The HRR function is enabled with command MFR_SPECIAL_OPTIONS (0xE0).

Bit	Description	Format	Unit
15:0	Input under voltage warning limit and/or HRR threshold.	Linear	V

VIN_UV_FAULT_LIMIT (0x59)
Description: Input under voltage fault limit.

Bit	Description	Format	Unit
15:0	Input under voltage fault limit.	Linear	V

VIN_UV_FAULT_RESPONSE (0x5A)
Description: Input under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

POWER_GOOD_ON (0x5E)
Description: Sets the output voltage threshold for asserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_ON command sets the output voltage at which an optional POWER_GOOD signal should be asserted.	Vout Mode Unsigned (Exp = -11)	V

POWER_GOOD_OFF (0x5F)
Description: Sets the output voltage threshold for deasserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_OFF command sets the output voltage at which an optional POWER_GOOD signal should be deasserted.	Vout Mode Unsigned (Exp = -11)	V

TON_DELAY (0x60)
Description: Sets the turn-on delay time

Bit	Description	Format	Unit
15:0	Sets the delay time from ENABLE to start of VOUT rise.	Linear	ms

TON_MAX_FAULT_RESPONSE (0x63)
Description: Only some of the response types are supported.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2. TON_MAX_FAULT_RESPONSE time unit is referenced to VOUT FAULT time unit.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

TOFF_DELAY (0x64)
Description: Sets the turn-off delay.

Bit	Description	Format	Unit
15:0	Sets the delay time from DISABLE to start of VOUT fall.	Linear	ms

STATUS_BYTE (0x78)
Description: Returns a brief fault/warning status byte.

Bit	Function	Description	Value	Description
7	Busy	This bit is asserted if the unit is busy	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No fault
			1	Fault
4		An output overcurrent fault has occurred.	0	No fault

	Iout Overcurrent Fault		1	Fault
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No fault
			1	Fault
2	Temperature	A temperature fault or warning has occurred.	0	No fault
			1	Fault
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault
			1	Fault
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault
			1	Fault

STATUS_WORD (0x79)
Description: Returns an extended fault/warning status byte.

Bit	Function	Description	Value	Description
15	Vout	An output voltage fault or warning has occurred.	0	No fault
			1	Fault
14	Iout/Pout	An output current or output power fault or warning has occurred.	0	No Fault.
			1	Fault.
13	Input	An input voltage, input current, or input power fault or warning has occurred.	0	No Fault.
			1	Fault.
12	Mfr Specific	A manufacturer specific fault or warning has occurred.	0	No fault.
			1	Fault.
11	Power-Good	The Power-Good signal, if present, is negated.	0	No Fault.
			1	Fault.
9	Other	A bit in Status-Other is set.	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No Fault.
			1	Fault.
4	Iout Overcurrent Fault	An output overcurrent fault has occurred.	0	No Fault.
			1	Fault.
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No Fault.
			1	Fault.
2	Temperature	A temperature fault or warning has occurred.	0	No Fault.
			1	Fault.
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault.
			1	Fault.
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault.
			1	Fault.

STATUS_VOUT (0x7A)
Description: Returns Vout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vout Overvoltage Fault	Vout Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vout Overvoltage Warning	Vout Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vout Undervoltage Warning	Vout Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vout Undervoltage Fault	Vout Undervoltage Fault.	0	No Fault.
			1	Fault.

3	Vout Max Warning	Vout Max Warning (An attempt has been made to set the output voltage to value higher than allowed by the Vout Max command (Section 13.5).	0	No Warning.
			1	Warning.
2	Ton Max Fault	Ton-Max Fault.	0	No Fault
			1	Fault.
1	Toff Max Warning	Toff Max Warning.	0	No Warning.
			1	Warning.

STATUS_IOUT (0x7B)
Description: Returns Iout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Iout Overcurrent Fault	Iout Overcurrent Fault.	0	No Fault.
			1	Fault.
6	Iout Overcurrent And Low Voltage Fault	Iout Overcurrent and low voltage fault.	0	No Fault.
			1	Fault.
5	Iout Over Current Warning	Iout Overcurrent Warning.	0	No Warning.
			1	Warning.
4	Iout Undercurrent Fault	Iout Undercurrent Fault.	0	No Fault.
			1	Fault.

STATUS_INPUT (0x7C)
Description: Returns VIN/IIN-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vin Overvoltage Fault	Vin Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vin Overvoltage Warning	VIN Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vin Undervoltage Warning	Vin Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vin Undervoltage Fault	Vin Undervoltage Fault.	0	No Fault.
			1	Fault.
3	Insufficient Vin	Asserted when either the input voltage has never exceeded the input turn-on threshold Vin-On, or if the unit did start, the input voltage decreased below the turn-off threshold.	0	No Insufficient VIN encountered yet.
			1	Insufficient Unit is off.

STATUS_TEMPERATURE (0x7D)
Description: Returns the temperature-related fault/warning status bits

Bit	Function	Description	Value	Description
7	Overtemperature Fault	Overtemperature Fault.	0	No Fault.
			1	Fault.
6	Overtemperature Warning	Overtemperature Warning.	0	No Warning.
			1	Warning.
5	Undertemperature Warning	Undertemperature Warning.	0	No Warning.
			1	Warning.
4	Undertemperature Fault	Undertemperature Fault.	0	No Fault.
			1	Fault.

STATUS_CML (0x7E)
Description: Returns Communication/Logic/Memory-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Invalid Or Unsupported Command Received	Invalid Or Unsupported Command Received.	0	No Invalid Command Received.
			1	Invalid Command Received.
6	Invalid Or Unsupported Data Received	Invalid Or Unsupported Data Received.	0	No Invalid Data Received.
			1	Invalid Data Received.
5	Packet Error Check Failed	Packet Error Check Failed.	0	No Failure.
			1	Failure.
4	Memory Fault Detected	Memory Fault Detected.	0	No Fault.
			1	Fault.
3	Processor Fault Detected	Processor fault detected.	0	No Fault.
			1	Fault.
1	Other Communication Fault	A communication fault other than the ones listed in this table has occurred.	0	No Fault.
			1	Fault.
0	Memory Or Logic Fault	Other Memory Or Logic Fault has occurred.	0	No Fault.
			1	Fault.

STATUS_OTHER (0x7F)
Description: Returns a brief other fault/warning status bits.

Bit	Description	Value	Description
0	The device was the first to assert SMBALERT.		

STATUS_MFR_SPECIFIC (0x80)
Description: Returns manufacturer specific status information.

Bit	Function	Description	Value	Description
7	Sync Fault	Sync fault.	0	No fault.
			1	Fault.
6	Iout Average Overcurrent Fault	Iout average overcurrent fault.	0	No fault.
			1	Fault.
5	Iout Fast Overcurrent Fault	Iout fast overcurrent fault.	0	No fault.
			1	Fault.
4	Short Circuit Protection Fault	Short circuit protection fault.	0	No fault.
			1	Fault.
3	Overtemperature2 Fault	Overtemperature2 fault.	0	No fault.
			1	Fault.
2	Auxiliary Voltage Fault	Auxiliary voltage fault.	0	No fault.
			1	Fault.
1	Startup Over Current Fault	Startup over current fault.	0	No fault.
			1	Fault.
0	Overtemperature2 Warn	Overtemperature2 warn.	0	No fault.
			1	Fault.

READ_VIN (0x88)
Description: Returns the measured input voltage.

Bit	Description	Format	Unit
15:0	Returns the input voltage reading.	Linear	V

READ_VOUT (0x8B)
Description: Returns the measured output voltage.

Bit	Description	Format	Unit
15:0	Returns the measured output voltage.	Vout Mode Unsigned (Exp = -11)	V

READ_IOUT (0x8C)
Description: Returns the measured output current.

Bit	Description	Format	Unit
15:0	Returns the measured output current.	Linear	A

READ_TEMPERATURE_1 (0x8D)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_TEMPERATURE_2 (0x8E)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_DUTY_CYCLE (0x94)
Description: Returns the actual duty cycle in percent.

Bit	Description	Format	Unit
15:0	Returns the actual duty cycle in percent.	Linear	%

READ_FREQUENCY (0x95)
Description: Returns the actual switching frequency.

Bit	Description	Format	Unit
15:0	Returns the actual switching frequency.	Linear	kHz

PMBUS_REVISION (0x98)
Description: Returns the PMBus revision number for this device.

Bit	Function	Description	Value	Function	Description
7:4	Part I Revision	Part I Revision.	0x0	1.0	Part I Revision 1.0.
			0x1	1.1	Part I Revision 1.1.
			0x2	1.2	Part I Revision 1.2.
			0x3	1.3	Part I Revision 1.3.
3:0	Part II Revision	Part II Revision.	0x0	1.0	Part II Revision 1.0.
			0x1	1.1	Part II Revision 1.1.
			0x2	1.2	Part II Revision 1.2.
			0x3	1.3	Part II Revision 1.3.

MFR_ID (0x99)
Description: Sets the Manufacturers ID

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_MODEL (0x9A)
Description: Sets the MFR MODEL string.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

MFR_REVISION (0x9B)
Description: Sets the MFR revision string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_LOCATION (0x9C)
Description: Sets the MFR location string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_DATE (0x9D)
Description: This command returns the date the regulator was manufactured.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_SERIAL (0x9E)
Description: This command returns a string of 13 characters and numbers that provides a unique identification of the regulator.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

USER_DATA_00 (0xB0)
Description: This command is available as generic read/write storage for customers.

Bit	Description	Format
127:0	16 bytes of user data.	Byte Array

FW_CONFIG_REGULATION (0xC5)
Description: FW CONFIG REGULATION parameter

Bit	Description	Value	Function	Description
0	Enable diode emulation at startup	0	Disabled	
		1	Enabled	

FW_CONFIG_FAULTS (0xC8)
Description: FW CONFIG FAULTS parameter

Bit	Function	Description	Value	Function	Description

7:6	Vout Delay Unit	Vout_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vout Delay Unit Time unit for retry responses
			01	4ms/unit	Vout Delay Unit Time unit for retry responses
			10	16ms/unit	Vout Delay Unit Time unit for retry responses
			11	256ms/unit	Vout Delay Unit Time unit for retry responses
5:4	Vin Delay Unit	Vin_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vin Delay Unit Time unit for retry responses
			01	4ms/unit	Vin Delay Unit Time unit for retry responses
			10	16ms/unit	Vin Delay Unit Time unit for retry responses
			11	256ms/unit	Vin Delay Unit Time unit for retry responses
3:2	Iout Delay Unit	IOUT_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	IOUT Delay Unit Time unit for retry responses
			01	4ms/unit	IOUT Delay Unit Time unit for retry responses
			10	16ms/unit	IOUT Delay Unit Time unit for retry responses
			11	256ms/unit	IOUT Delay Unit Time unit for retry responses
1:0	Temperature Delay Unit	Temperature_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Temperature Delay Unit Time unit for retry responses
			01	4ms/unit	Temperature Delay Unit Time unit for retry responses
			10	16ms/unit	Temperature Delay Unit Time unit for retry responses
			11	256ms/unit	Temperature Delay Unit Time unit for retry responses

FW_CONFIG_PMBUS (0xC9)
Description: This command contains various configurable settings related to PMBus address and digital pins.

Bit	Function	Description	Format
31:24	PMBus Base Addr	Base Address for PMBus offset to start from	Integer Unsigned
23:17	PMBus Addr Offset	PMBUS Address offset when resistor offset Not enabled	Integer Unsigned

Bit	Function	Description	Value	Function	Description
33	Power good polarity	Power good polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
32	Control pin polarity	Control pin polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
16	PMBus Addr Offset Resistor Enable	PMBus_addr_offset_enable Enable PMBUS Address Offset via resistor	0	Disabled	
			1	Enabled	

MFR_IOUT_OC_FAST_FAULT_RESPONSE (0xCA)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			11	Shutdown and Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAST_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAST_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xC8.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

MFR_IOUT_AVG_OC_FAULT_LIMIT (0xD0)
Description: Average output over current limit.

Bit	Description	Format	Unit
15:0	Average output over current fault limit.	Linear	A

MFR_IOUT_OC_FAST_FAULT_LIMIT (0xD1)
Description: The MFR_IOUT_OC_FAST_FAULT_LIMIT command sets or retrieves Iout fast overcurrent fault threshold, in Amperes.

Bit	Description	Format	Unit
15:0	Sets Iout fast over-current fault threshold.	Integer Unsigned	A

MFR_IOUT_AVG_COEFF (0xD2)
Description: Coefficient for controlling the averaging strength for the averaged Iout current limit.

Bit	Description	Format
5:0	Coefficient for controlling the averaging strength for the averaged Iout current limit. Value 0-63. Setting the coefficient to 0 will disable the averaging and the average current limit behaviour.	Integer Unsigned

MFR_READ_VAUX (0xDA)
Description: Returns the measured auxiliary input voltage.

Bit	Description	Format	Unit
15:0	Returns the auxiliary input voltage reading.	Linear	V

MFR_SELECT_TEMPERATURE_SENSOR (0xDC)
Description: Select which temperature sensor, internal one or external remote temperature sensor, is used.

Bit	Function	Description	Value	Function	Description
4:3	Fault Source Select	Select which temperature sensor, internal one or external remote temperature sensor, is used.	00	Temp A	Temp A temperature sensor selected.
			01	Temp B	Temp B temperature sensor selected.
			10	Temp I	Temp I temperature sensor selected.
2:0	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select.	000	TempA TempB	TempA (External Temperature sensor A) TempB (External Temperature sensor B).
			001	TempA TempI	TempA (External Temperature sensor A) TempI (Internal Temperature sensor).
			010	TempB TempA	TempB (External Temperature sensor B) TempA (External Temperature sensor A).
			011	TempB TempI	TempB (External Temperature sensor B) TempI (Internal Temperature sensor).
			100	TempI TempA	TempI (Internal Temperature sensor) TempA (External Temperature sensor A).
			101	TempI TempB	TempI (Internal Temperature sensor) TempB (External Temperature sensor B).

MFR_FLEX_FIRMWARE_CMD (0xE0)
Description: Mfr. firmware command.

Bit	Description	Format
63:0	Mfr. firmware command.	Byte Array

MFR_TEMP_COEFF (0xE7)
Description: Coefficient for Iout temperature compensation.

Bit	Description	Format
15:0	Coefficient in Q16. Iout compensation factor calculated according to: 1 / (1 + t_coeff * (T - 20))	Fixed Point Unsigned

MFR_IOUT_APC (0xEA)
Description: The iout apc gain.

Bit	Description	Format	Unit
15:0	SSet the iout apc gain. the format is Linear 11, Exponent is -9 or -8 (User selection possible). The LSB varies with isen_gain_mode - ISEN_LSB/Secondary current sense resistor (Rsense).	Linear	A

MFR_MULTI_PIN_CONFIG (0xF9)
Description: The MFR_MULTI_PIN_CONFIG command can be re-configured to enable or disable different functions and set the pin configuration.

Bit	Function	Description	Value	Function	Description
7	SMBAlert pin function	Selects if the SMBAlert pin should be used for ALERT or SYNC.	00	Alert	SMBAlert pin used as SMBAlert.
			01	Sync	SMBAlert pin used as SYNC.
6:5	Sync Mode	These bits configures the direction of the sync pin as either SYNC OUT or SYNC IN. Use the INTERLEAVE command to enable/disable the sync function and to configure phase offset.	00	Disabled	Sync Pin disabled.
			01	Sync in	When the product is configured as SYNC IN it will synchronize its switching frequency to an external sync signal. The switching phases can be spread individually using the INTERLEAVE command 0x37.
			10	Sync out	When the product is configured as SYNC OUT it will send out a SYNC signal. Only 1 product in a group can be configured as SYNC OUT.
4	Sync Output	Selects the output type of the Sync pin.	0	Open Drain	Sync output configured as Open Drain.
2	Power Good Pull-down	This bit enables or disables Power Good pin pull-down.	0	Disabled	
			1	Enabled	
1	Power Good Output	Two output options are available for Power Good output, they are Push/Pull or Open Drain.	0	Push/Pull	Power Good configured Push/Pull.
			1	Open Drain	Power Good configured Open Drain.

Technical Reference PMBus BMR 316 XXX1/023

This appendix contains a detailed reference of the PMBus commands supported by the product.

Data Formats

The products make use of a few standardized numerical formats, along with custom data formats. A detailed walkthrough of the above formats is provided in AN304, as well as in sections 7 and 8 of the PMBus Specification Part II. The custom data formats vary depending on the command, and are detailed in the command description.

Standard Commands

The functionality of commands with code 0x00 to 0xCF is usually based on the corresponding command specification provided in the PMBus Standard Specification Part II (see Power System Management Bus Protocol Documents below). However there might be different interpretations of the PMBus Standard Specification or only parts of the Standard Specification applied, thus the detailed command description below should always be consulted.

Forum Websites

The System Management Interface Forum (SMIF)
http://www.powersig.org/
The System Management Interface Forum (SMIF) supports the rapid advancement of an efficient and compatible technology base that promotes power management and systems technology implementations. The SMIF provides a membership path for any company or individual to be active participants in any or all of the various working groups established by the implementer forums.

Power Management Bus Implementers Forum
(PMBUS-IF)
http://pmbus.org/
The PMBus-IF supports the advancement and early adoption of the PMBus protocol for power management. This website offers recent PMBus specification documents, PMBus articles, as well as upcoming PMBus presentations and seminars, PMBus Document Review Board (DRB) meeting notes, and other PMBus related news.

PMBus – Power System Management Bus Protocol Documents

These specification documents may be obtained from the PMBus-IF website described above. These are required reading for complete understanding of the PMBus implementation. This appendix will not re-address all of the details contained within the two PMBus Specification documents.

Specification Part I – General Requirements Transport And Electrical Interface
Includes the general requirements, defines the transport and electrical interface and timing requirements of hard wired signals.

Specification Part II – Command Language
Describes the operation of commands, data formats, fault management and defines the command language used with the PMBus.

SMBus – System Management Bus Documents

System Management Bus Specification, Version 2.0, August 3, 2000
This specification specifies the version of the SMBus on which Revision 1.2 of the PMBus Specification is based. This specification is freely available from the System Management Interface Forum Web site at:
http://www.smbus.org/specs/

PMBus Command Summary and Factory Default Values of Standard Configuration

The factory default values provided in the table below are valid for the Standard configuration. Factory default values for other configurations can be found using the Flex Power Designer tool.

Code	Name	Data Format	Factory Default Value Standard Configuration BMR316XXX1/023 R1		Min Set Value	Max Set Value	Unit
0x01	OPERATION	R/W Byte	0x80				
0x02	ON_OFF_CONFIG	R/W Byte	0x16				
0x03	CLEAR_FAULTS	Send Byte					
0x10	WRITE_PROTECT	R/W Byte					
0x12	RESTORE_DEFAULT_ALL	Send Byte					
0x15	STORE_USER_ALL	Send Byte					
0x16	RESTORE_USER_ALL	Send Byte					
0x19	CAPABILITY	Read Byte	0xB0				
0x1B	SMBALERT_MASK_VOUT (STATUS_VOUT)	SMBAlert Mask	0x6E				
0x1B	SMBALERT_MASK_IOUT (STATUS_IOUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_INPUT (STATUS_INPUT)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_TEMPERATURE (STATUS_TEMPERATURE)	SMBAlert Mask	0x60				
0x1B	SMBALERT_MASK_CML (STATUS_CML)	SMBAlert Mask	0xFB				
0x1B	SMBALERT_MASK_OTHER (STATUS_OTHER)	SMBAlert Mask	0x01				
0x1B	SMBALERT_MASK_MFR_SPECIFIC (STATUS_MFR_SPECIFIC)	SMBAlert Mask	0x09				
0x20	VOUT_MODE	Read Byte	0x15				
0x35	VIN_ON	R/W Word	0xF094	37.00			V
0x36	VIN_OFF	R/W Word	0xF080	32.00			V
0x37	INTERLEAVE	R/W Word	0x0120				
0x39	IOUT_CAL_OFFSET	Read Word	Unit Specific				
0x40	VOUT_OV_FAULT_LIMIT	R/W Word	0x8200	16.25	0	17	V
0x41	VOUT_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x42	VOUT_OV_WARN_LIMIT	R/W Word	0x7C00	15.50	0	17	V
0x43	VOUT_UV_WARN_LIMIT	R/W Word	0x4400	8.50	0	16	V
0x44	VOUT_UV_FAULT_LIMIT	R/W Word	0x3C00	7.50	0	16	V
0x45	VOUT_UV_FAULT_RESPONSE	R/W Byte	0x80				
0x46	IOUT_OC_FAULT_LIMIT	R/W Word	0x00D2	210.00	0	255	A
0x47	IOUT_OC_FAULT_RESPONSE	R/W Byte	0xC0				
0x48	IOUT_OC_LV_FAULT_LIMIT	R/W Word	0x0000	0.00			V
0x4A	IOUT_OC_WARN_LIMIT	R/W Word	0x005F	95.00	0	255	A
0x4B	IOUT_UC_FAULT_LIMIT	R/W Word	0xE440	-60.00			A
0x4C	IOUT_UC_FAULT_RESPONSE	R/W Byte	0x00				
0x4F	OT_FAULT_LIMIT	R/W Word	0x007D	125.00	-50	150	°C
0x50	OT_FAULT_RESPONSE	R/W Byte	0x80				
0x51	OT_WARN_LIMIT	R/W Word	0x006E	110.00	-50	150	°C
0x52	UT_WARN_LIMIT	R/W Word	0x0000	0.00	-50	150	°C
0x53	UT_FAULT_LIMIT	R/W Word	0xE4E0	-50.00	-50	150	°C
0x54	UT_FAULT_RESPONSE	R/W Byte	0x00				
0x55	VIN_OV_FAULT_LIMIT	R/W Word	0xF104	65.00	0	128	V

0x56	VIN_OV_FAULT_RESPONSE	R/W Byte	0x80				
0x57	VIN_OV_WARN_LIMIT	R/W Word	0xF0F8	62.00	0	128	V
0x58	VIN_UV_WARN_LIMIT	R/W Word	0xF002	0.50	0	128	V
0x59	VIN_UV_FAULT_LIMIT	R/W Word	0xF001	0.25	0	128	V
0x5A	VIN_UV_FAULT_RESPONSE	R/W Byte	0x00				
0x5E	POWER_GOOD_ON	R/W Word	0x4A66	9.30	0	16	V
0x5F	POWER_GOOD_OFF	R/W Word	0x48CD	9.10	0	16	V
0x60	TON_DELAY	R/W Word	0x0000	0.00	0	1023	ms
0x63	TON_MAX_FAULT_RESPONSE	R/W Byte	0x00				
0x64	TOFF_DELAY	R/W Word	0xF804	2.00	0	1023	ms
0x78	STATUS_BYTE	Read Byte					
0x79	STATUS_WORD	Read Word					
0x7A	STATUS_VOUT	Read Byte					
0x7B	STATUS_IOUT	Read Byte					
0x7C	STATUS_INPUT	Read Byte					
0x7D	STATUS_TEMPERATURE	Read Byte					
0x7E	STATUS_CML	Read Byte					
0x7F	STATUS_OTHER	Read Byte					
0x80	STATUS_MFR_SPECIFIC	Read Byte					
0x88	READ_VIN	Read Word					
0x8B	READ_VOUT	Read Word					
0x8C	READ_IOUT	Read Word					
0x8D	READ_TEMPERATURE_1	Read Word					
0x8E	READ_TEMPERATURE_2	Read Word					
0x94	READ_DUTY_CYCLE	Read Word					
0x95	READ_FREQUENCY	Read Word					
0x98	PMBUS_REVISION	Read Byte					
0x99	MFR_ID	Read Block12	Unit Specific				
0x9A	MFR_MODEL	Read Block20	Unit Specific				
0x9B	MFR_REVISION	Read Block12	Unit Specific				
0x9C	MFR_LOCATION	Read Block12	Unit Specific				
0x9D	MFR_DATE	Read Block12	Unit Specific				
0x9E	MFR_SERIAL	Read Block20	Unit Specific				
0xB0	USER_DATA_00	R/W Block16	Unit Specific				
0xC5	FW_CONFIG_REGULATION	Read Block14	0xF800F800D000F800F8000000A000				
0xC8	FW_CONFIG_FAULTS	Read Block25	0x000000000000000000000000000000000000000000000000				
0xC9	FW_CONFIG_PMBUS	R/W Block11	0x000000008800021001 2001				
0xCA	MFR_IOUT_OC_FAST_FAULT_RESPONSE	R/W Byte	0xC0				
0xD0	MFR_IOUT_AVG_OC_FAULT_LIMIT	Read Word	0x0064	100.00			A
0xD1	MFR_IOUT_OC_FAST_FAULT_LIMIT	Read Word	0x00DC	220			A
0xD2	MFR_IOUT_AVG_COEFF	Read Byte	0x1A				
0xDA	MFR_READ_VAUX	Read Word					
0xDC	MFR_SELECT_TEMPERATURE_SENSOR	Read Byte	0x00				
0xE0	MFR_FLEX_FIRMWARE_CMD	R/W Block8					
0xE7	MFR_TEMP_COEFF	Read Word	0x0189				
0xEA	MFR_IOUT_APC	Read Word	Unit Specific				
0xF9	MFR_MULTI_PIN_CONFIG	R/W Word	0x0002				

PMBus Command Details

OPERATION (0x01)
Description: Sets the desired PMBus enable and margin operations.

Bit	Function	Description	Value	Function	Description
7:6	Enable	Make the device enable or disable.	00	Immediate Off	Disable Immediately without sequencing.
			01	Soft Off	Disable "Softly" with sequencing.
			10	Enable	Enable device to the desired margin state.
5:4	Margin	Select between margin high/low states or nominal output.	00	Nominal	Operate at nominal output voltage.
			01	Margin Low	Operate at margin low voltage set in VOUT_MARGIN_LOW.
			10	Margin High	Operate at margin high voltage set in VOUT_MARGIN_HIGH.
3:2	Act on Fault	Set 10b to act on fault or set to 01b to ignore fault.	01	Ignore Faults	Ignore Faults when in a margined state. The device will ignore appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.
			10	Act on Faults	Act on Faults when in a margined state. The device will handle appropriate overvoltage/undervoltage warnings and faults and respond as programmed by the warning limit or fault response command.

ON_OFF_CONFIG (0x02)
Description: Configures how the device is controlled by the CONTROL pin and the PMBus.

Bit	Function	Description	Value	Function	Description
4	Powerup Operation	Sets the default to either operate any time power is present or for the on/off to be controlled by CONTROL pin and serial bus commands.	0	Enable Always	Unit powers up any time power is present regardless of state of the CONTROL pin, taking the RC configuration into account, see command 0xE3.
			1	Enable pin or PMBus	Unit does not power up until commanded by the CONTROL pin and OPERATION command.
3	PMBus Enable Mode	Controls how the unit responds to commands received via the serial bus.	0	Ignore PMBus	Unit ignores the on/off portion of the OPERATION command from serial bus.

			1	Use PMBus	To start, the unit requires that the on/off portion of the OPERATION command is instructing the unit to run.
2	Enable Pin Mode	Controls how the unit responds to the CONTROL pin.	0	Ignore pin	Unit ignores the CONTROL/Enable pin.
			1	Use pin	Unit requires the CONTROL pin to be asserted to start the unit.
1	Enable Pin Polarity	Polarity of the CONTROL pin.	0	Active Low	Enable pin will cause device to enable when driven low.
			1	Active High	Enable pin will cause device to enable when driven high.
0	Disable Action	CONTROL pin action when commanding the unit to turn off.	0	Soft Off	Use the programmed turn off delay and fall time.
			1	Imm. Off	Turn off the output and stop transferring energy to the output as fast as possible. The device's product literature shall specify whether or not the device sinks current to decrease the output voltage fall time.

CLEAR_FAULTS (0x03)
Description: Clears all fault status bits

WRITE_PROTECT (0x10)
Description: The WRITE_PROTECT command is used to control writing to the PMBus device. The intent of this command is to provide protection against accidental changes. This command is not intended to provide protection against deliberate or malicious changes to a device's configuration or operation.

Bit	Description	Value	Function	Description
7:0	All supported commands may have their parameters read, regardless of the WRITE_PROTECT settings.	0x80	Disable all writes	Disable all writes except to the WRITE_PROTECT command.
		0x40	Enable operation	Disable all writes except to the WRITE_PROTECT, OPERATION and PAGE commands.
		0x20	Enable control and Vout commands	Disable all writes except to the WRITE_PROTECT, OPERATION, PAGE, ON_OFF_CONFIG and VOUT_COMMAND commands.
		0x00	Enable all commands	Enable writes to all commands.

RESTORE_DEFAULT_ALL (0x12)
Description: Commands the device to restore its configuration from the Default Store.

STORE_USER_ALL (0x15)
Description: Stores, at the USER level, all PMBus values that were changed since the last restore command.

RESTORE_USER_ALL (0x16)
Description: Restores PMBus settings that were stored using STORE_USER_ALL. This command is automatically performed at power up.

CAPABILITY (0x19)
Description: This command provides a way for a host system to determine some key capabilities of a PMBus device.

Bit	Function	Description	Value	Function	Description
7	Packet Error Checking	Packet error checking.	0	Not Supported	Packet Error Checking not supported.
			1	Supported	Packet Error Checking is supported.
6:5	Maximum Bus Speed	Maximum bus speed.	00	100kHz	Maximum supported bus speed is 100 kHz.
			01	400kHz	Maximum supported bus speed is 400 kHz.
			10	1MHz	Maximum supported bus speed is 1 MHz.
4	Smbalert	SMBALERT	00	No Smbalert	The device does not have a SMBALERT# pin and does not support the SMBus Alert Response protocol.
			01	Have Smbalert	The device does have a SMBALERT# pin and does support the SMBus Alert Response protocol.
3	Numeric Format	Numeric format.	0	LINEAR or DIRECT Format	Numeric data is in LINEAR or DIRECT format.
			1	IEEE Half Precision Floating Point Format	Numeric data is in IEEE half precision floating point format.
2	AVSBus Support	AVSBus support.	0	AVSBus Not Supported	AVSBus not supported.
			1	AVSBus Supported	AVSBus supported.

SMBALERT_MASK_VOUT (0x1B)
Status Registers: STATUS_VOUT (0x7A)
Description: SMBALERT_MASK bits for the STATUS_VOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vout Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vout Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vout Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vout Undervoltage Fault		0	Pull SALERT	
			1	Ignore	

3	Vout Max Warning		0	Pull SALERT	
			1	Ignore	
2	Ton Max Fault		0	Pull SALERT	
			1	Ignore	
1	Toff Max Warning		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_IOUT (0x1B)
Status Registers: STATUS_IOUT (0x7B)
Description: SMBALERT_MASK bits for the STATUS_IOUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Iout Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Overcurrent And Low Voltage Fault		0	Pull SALERT	
			1	Ignore	
5	Iout Over Current Warning		0	Pull SALERT	
			1	Ignore	
4	Iout Undercurrent Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_INPUT (0x1B)
Status Registers: STATUS_INPUT (0x7C)
Description: SMBALERT_MASK bits for the STATUS_INPUT command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Vin Overvoltage Fault		0	Pull SALERT	
			1	Ignore	
6	Vin Overvoltage Warning		0	Pull SALERT	
			1	Ignore	
5	Vin Undervoltage Warning		0	Pull SALERT	
			1	Ignore	
4	Vin Undervoltage Fault		0	Pull SALERT	
			1	Ignore	
3	Insufficient Vin		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_TEMPERATURE (0x1B)
Status Registers: STATUS_TEMPERATURE (0x7D)
Description: SMBALERT_MASK bits for the STATUS_TEMPERATURE command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Overtemperature Fault		0	Pull SALERT	
			1	Ignore	

6	Overtemperature Warning		0	Pull SALERT	
			1	Ignore	
5	Undertemperature Warning		0	Pull SALERT	
			1	Ignore	
4	Undertemperature Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_CML (0x1B)
Status Registers: STATUS_CML (0x7E)
Description: SMBALERT_MASK bits for the STATUS_CML command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Invalid Or Unsupported Command Received		0	Pull SALERT	
			1	Ignore	
6	Invalid Or Unsupported Data Received		0	Pull SALERT	
			1	Ignore	
5	Packet Error Check Failed		0	Pull SALERT	
			1	Ignore	
4	Memory Fault Detected		0	Pull SALERT	
			1	Ignore	
3	Processor Fault Detected		0	Pull SALERT	
			1	Ignore	
1	Other Communication Fault		0	Pull SALERT	
			1	Ignore	
0	Memory Or Logic Fault		0	Pull SALERT	
			1	Ignore	

SMBALERT_MASK_OTHER (0x1B)
Status Registers: STATUS_OTHER (0x7F)
Description: SMBALERT_MASK bits for the STATUS_OTHER command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Description	Value	Function	Description
0		0	Pull SALERT	
		1	Ignore	

SMBALERT_MASK_MFR_SPECIFIC (0x1B)
Status Registers: STATUS_MFR_SPECIFIC (0x80)
Description: SMBALERT_MASK bits for the STATUS_MFR_SPECIFIC command. The SMBALERT_MASK command may be used to prevent a warning or fault condition from asserting the SALERT output signal.

Bit	Function	Description	Value	Function	Description
7	Sync Fault		0	Pull SALERT	
			1	Ignore	
6	Iout Average Overcurrent Fault		0	Pull SALERT	
			1	Ignore	

5	Iout Fast Overcurrent Fault		0	Pull SALERT	
			1	Ignore	
4	Short Circuit Protection Fault		0	Pull SALERT	
			1	Ignore	
3	Overtempera ture2 Fault		0	Pull SALERT	
			1	Ignore	
2	Auxiliary Voltage Fault		0	Pull SALERT	
			1	Ignore	
1	Startup Over Current Fault		0	Pull SALERT	
			1	Ignore	
0	Overtempera ture2 Warn		0	Pull SALERT	
			1	Ignore	

VOUT_MODE (0x20)
Description: Controls how future VOUT-related commands parameters will be interpreted.

Bit	Function	Description	Format
4:0		Five bit two's complement EXPONENT for the MANTISSA delivered as the data bytes for VOUT_COMMAND in VOUT_LINEAR Mode, five bit VID code identifier per in VID Mode or always set to 00000b in Direct Mode.	Integer Signed

Bit	Function	Description	Value	Function	Description
7:5		Set to 000b to select VOUT_LINEAR Mode (Five bit two's complement exponent for the MANTISSA delivered as the data bytes for an output voltage related command), set to 001b to select VID Mode (Five bit VID code identifier per) or set to 010b to select Direct Mode (Always set to 00000b).	000	Linear	Linear Mode Format.
			001	VID	VID Mode.
			010	Direct	Direct Mode.

VIN_ON (0x35)
Description: The VIN_ON command sets the value of the input voltage, in volts, at which the unit should start power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN ON threshold.	Linear	V

VIN_OFF (0x36)
Description: The VIN_OFF command sets the value of the input voltage, in volts, at which the unit, once operation has started, should stop power conversion.

Bit	Description	Format	Unit
15:0	Sets the VIN OFF threshold.	Linear	V

INTERLEAVE (0x37)
Description: Configures the phase offset with respect to a common SYNC clock. When multiple products share a common DC input supply, spreading of the switching phases between the products can be utilized. This reduces the input capacitance requirements and efficiency losses, since the peak current drawn from the input supply is effectively spread out over the whole switch period. If two or more units have their outputs connected in parallel, interleaving will reduce ripple currents. This requires that the products are synchronized using the SYNC pin.

Bit	Function	Description	Format
11:8	Group ID Number	Value 0-15. Sets an ID number to a group of interleaved rails.	Integer Unsigned
7:4	Number of Rails	Value 0-15. Sets the number of units in the group, including the SYNC OUT product.	Integer Unsigned
3:0	Rail Position	Value 0-15. Sets the interleave order for this unit. The product configured to SYNC OUT shall be assigned to number 0	Integer Unsigned

IOUT_CAL_OFFSET (0x39)
Description: Sets the current-sense offset.

Bit	Description	Format	Unit
15:0	Sets an offset to IOUT readings. Use to compensate for delayed measurements of current ramp.	Linear	A

VOUT_OV_FAULT_LIMIT (0x40)
Description: Output over voltage fault limit.

Bit	Description	Format	Unit
15:0	Output over voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_OV_FAULT_RESPONSE (0x41)
Description: Output over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VOUT_OV_WARN_LIMIT (0x42)
Description: Output over voltage warning limit.

Bit	Description	Format	Unit
15:0	Output over voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_WARN_LIMIT (0x43)
Description: Output under voltage warning limit.

Bit	Description	Format	Unit
15:0	Output under voltage warning limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_LIMIT (0x44)
Description: Output under voltage fault limit.

Bit	Description	Format	Unit
15:0	Output under voltage fault limit.	Vout Mode Unsigned (Exp = -11)	V

VOUT_UV_FAULT_RESPONSE (0x45)
Description: Output under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

		Describes the device interruption operation. 00b - The PMBus device continues operation without interruption. 01b - The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]). 10b - The device shuts down (disables the output) and responds according to the Retry Setting in bits [5:3]. 11b - The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.	01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

IOUT_OC_FAULT_LIMIT (0x46)
Description: Output over current limit.

Bit	Description	Format	Unit
15:0	Output over current fault limit.	Linear	A

IOUT_OC_FAULT_RESPONSE (0x47)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			01	Conditioned constant current	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT as long as the output voltage remains above the minimum value specified by IOUT_OC_LV_FAULT_LIMIT. If the output voltage is pulled down to less than that value, then the PMBus device shuts down and responds according to the Retry setting in bits [5:3].
			10	Delay w/ Const. Current & Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time		0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	

		Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	6	64	
			7	128	

IOUT_OC_LV_FAULT_LIMIT (0x48)
Description: Set the output over-current low-voltage fault threshold.

Bit	Description	Format	Unit
15:0	Set the output over-current low-voltage fault threshold.	Vout Mode Unsigned (Exp = -11)	V

IOUT_OC_WARN_LIMIT (0x4A)
Description: Output over current warning limit.

Bit	Description	Format	Unit
15:0	Output over current warning limit.	Linear	A

IOUT_UC_FAULT_LIMIT (0x4B)
Description: Sets the output under-current peak limit.

Bit	Description	Format	Unit
15:0	Sets the IOUT under-current peak fault threshold.	Linear	A

IOUT_UC_FAULT_RESPONSE (0x4C)
Description: Configures the output undercurrent fault response. The command format is the same as the PMBus standard responses for voltage and temperature faults except that it sets the undercurrent status bit.

Bit	Function	Description	Value	Function	Description
7:6	Response	Describes the device interruption operation. For all modes set by bits [7:6], the device pulls SALERT low and sets the related fault bit in the status registers.	00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and Retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retry Setting	The device attempts to restart the number of times set by these bits.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared.
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device (10 ms/unit) is to continue operating after a fault is detected or for the amount of time (8.2 ms/unit) between attempts to restart.	0	0	
			1	1	
			2	2	
			3	3	
			4	4	
			5	5	
			6	6	
			7	7	

OT_FAULT_LIMIT (0x4F)
Description: Over temperature fault limit.

Bit	Description	Format	Unit
15:0	Over temperature fault limit.	Linear	°C

OT_FAULT_RESPONSE (0x50)
Description: Over temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

OT_WARN_LIMIT (0x51)
Description: Over temperature warning limit.

Bit	Description	Format	Unit
15:0	Over temperature warning limit.	Linear	°C

UT_WARN_LIMIT (0x52)
Description: Under temperature warning limit.

Bit	Description	Format	Unit
15:0	Under temperature warning limit.	Linear	°C

UT_FAULT_LIMIT (0x53)
Description: Under temperature fault limit.

Bit	Description	Format	Unit
15:0	Under temperature fault limit.	Linear	°C

UT_FAULT_RESPONSE (0x54)
Description: Under temperature fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_FAULT_LIMIT (0x55)
Description: Input over voltage fault limit.

Bit	Description	Format	Unit
15:0	Input over voltage fault limit.	Linear	V

VIN_OV_FAULT_RESPONSE (0x56)
Description: Input over voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.

5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

VIN_OV_WARN_LIMIT (0x57)
Description: Input over voltage warning limit.

Bit	Description	Format	Unit
15:0	Input over voltage warning limit.	Linear	V

VIN_UV_WARN_LIMIT (0x58)
Description: Input under voltage warning limit. This command set also the input voltage threshold for the HRR function (Hybrid Ratio Regulation). The HRR function is enabled with command MFR_SPECIAL_OPTIONS (0xE0).

Bit	Description	Format	Unit
15:0	Input under voltage warning limit and/or HRR threshold.	Linear	V

VIN_UV_FAULT_LIMIT (0x59)
Description: Input under voltage fault limit.

Bit	Description	Format	Unit
15:0	Input under voltage fault limit.	Linear	V

VIN_UV_FAULT_RESPONSE (0x5A)
Description: Input under voltage fault response.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.
			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].

			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

POWER_GOOD_ON (0x5E)
Description: Sets the output voltage threshold for asserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_ON command sets the output voltage at which an optional POWER_GOOD signal should be asserted.	Vout Mode Unsigned (Exp = -11)	V

POWER_GOOD_OFF (0x5F)
Description: Sets the output voltage threshold for deasserting PG (Power Good).

Bit	Description	Format	Unit
15:0	The POWER_GOOD_OFF command sets the output voltage at which an optional POWER_GOOD signal should be deasserted.	Vout Mode Unsigned (Exp = -11)	V

TON_DELAY (0x60)
Description: Sets the turn-on delay time

Bit	Description	Format	Unit
15:0	Sets the delay time from ENABLE to start of VOUT rise.	Linear	ms

TON_MAX_FAULT_RESPONSE (0x63)
Description: Only some of the response types are supported.

Bit	Function	Description	Value	Function	Description
7:6	Response		00	Ignore Fault	The PMBus device continues operation without interruption.

			01	Perform Retries while Operating	The PMBus device continues operation for the delay time specified by bits [2:0] and the delay time unit specified for that particular fault. If the fault condition is still present at the end of the delay time, the unit responds as programmed in the Retry Setting (bits [5:3]).
			10	Disable and retry	The device shuts down (disables the output) and responds according to the retry setting in bits [5:3].
			11	Disable, Resume When OK	The device's output is disabled while the fault is present. Operation resumes and the output is enabled when the fault condition no longer exists.
5:3	Retries		000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xD2. TON_MAX_FAULT_RESPONSE time unit is referenced to VOUT FAULT time unit.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

TOFF_DELAY (0x64)
Description: Sets the turn-off delay.

Bit	Description	Format	Unit
15:0	Sets the delay time from DISABLE to start of VOUT fall.	Linear	ms

STATUS_BYTE (0x78)
Description: Returns a brief fault/warning status byte.

Bit	Function	Description	Value	Description
7	Busy	This bit is asserted if the unit is busy	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No fault
			1	Fault
4		An output overcurrent fault has occurred.	0	No fault

	Iout Overcurrent Fault		1	Fault
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No fault
			1	Fault
2	Temperature	A temperature fault or warning has occurred.	0	No fault
			1	Fault
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault
			1	Fault
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault
			1	Fault

STATUS_WORD (0x79)
Description: Returns an extended fault/warning status byte.

Bit	Function	Description	Value	Description
15	Vout	An output voltage fault or warning has occurred.	0	No fault
			1	Fault
14	Iout/Pout	An output current or output power fault or warning has occurred.	0	No Fault.
			1	Fault.
13	Input	An input voltage, input current, or input power fault or warning has occurred.	0	No Fault.
			1	Fault.
12	Mfr Specific	A manufacturer specific fault or warning has occurred.	0	No fault.
			1	Fault.
11	Power-Good	The Power-Good signal, if present, is negated.	0	No Fault.
			1	Fault.
9	Other	A bit in Status-Other is set.	0	No fault
			1	Fault
6	Off	This bit is asserted if the unit is not providing power to the output, regardless of the reason, including simply not being enabled.	0	No fault
			1	Fault
5	Vout Overvoltage Fault	An output overvoltage fault has occurred.	0	No Fault.
			1	Fault.
4	Iout Overcurrent Fault	An output overcurrent fault has occurred.	0	No Fault.
			1	Fault.
3	Vin Undervoltage Fault	An input undervoltage fault has occurred.	0	No Fault.
			1	Fault.
2	Temperature	A temperature fault or warning has occurred.	0	No Fault.
			1	Fault.
1	Communication/Logic	A communications, memory or logic fault has occurred.	0	No fault.
			1	Fault.
0	None of the Above	A fault or warning not listed in bits [7:1] has occurred.	0	No fault.
			1	Fault.

STATUS_VOUT (0x7A)
Description: Returns Vout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vout Overvoltage Fault	Vout Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vout Overvoltage Warning	Vout Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vout Undervoltage Warning	Vout Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vout Undervoltage Fault	Vout Undervoltage Fault.	0	No Fault.
			1	Fault.

3	Vout Max Warning	Vout Max Warning (An attempt has been made to set the output voltage to value higher than allowed by the Vout Max command (Section 13.5).	0	No Warning.
			1	Warning.
2	Ton Max Fault	Ton-Max Fault.	0	No Fault
			1	Fault.
1	Toff Max Warning	Toff Max Warning.	0	No Warning.
			1	Warning.

STATUS_IOUT (0x7B)
Description: Returns Iout-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Iout Overcurrent Fault	Iout Overcurrent Fault.	0	No Fault.
			1	Fault.
6	Iout Overcurrent And Low Voltage Fault	Iout Overcurrent and low voltage fault.	0	No Fault.
			1	Fault.
5	Iout Over Current Warning	Iout Overcurrent Warning.	0	No Warning.
			1	Warning.
4	Iout Undercurrent Fault	Iout Undercurrent Fault.	0	No Fault.
			1	Fault.

STATUS_INPUT (0x7C)
Description: Returns VIN/IIN-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Vin Overvoltage Fault	Vin Overvoltage Fault.	0	No Fault.
			1	Fault.
6	Vin Overvoltage Warning	VIN Overvoltage Warning.	0	No Warning.
			1	Warning.
5	Vin Undervoltage Warning	Vin Undervoltage Warning.	0	No Warning.
			1	Warning.
4	Vin Undervoltage Fault	Vin Undervoltage Fault.	0	No Fault.
			1	Fault.
3	Insufficient Vin	Asserted when either the input voltage has never exceeded the input turn-on threshold Vin-On, or if the unit did start, the input voltage decreased below the turn-off threshold.	0	No Insufficient VIN encountered yet.
			1	Insufficient Unit is off.

STATUS_TEMPERATURE (0x7D)
Description: Returns the temperature-related fault/warning status bits

Bit	Function	Description	Value	Description
7	Overtemperature Fault	Overtemperature Fault.	0	No Fault.
			1	Fault.
6	Overtemperature Warning	Overtemperature Warning.	0	No Warning.
			1	Warning.
5	Undertemperature Warning	Undertemperature Warning.	0	No Warning.
			1	Warning.
4	Undertemperature Fault	Undertemperature Fault.	0	No Fault.
			1	Fault.

STATUS_CML (0x7E)
Description: Returns Communication/Logic/Memory-related fault/warning status bits.

Bit	Function	Description	Value	Description
7	Invalid Or Unsupported Command Received	Invalid Or Unsupported Command Received.	0	No Invalid Command Received.
			1	Invalid Command Received.
6	Invalid Or Unsupported Data Received	Invalid Or Unsupported Data Received.	0	No Invalid Data Received.
			1	Invalid Data Received.
5	Packet Error Check Failed	Packet Error Check Failed.	0	No Failure.
			1	Failure.
4	Memory Fault Detected	Memory Fault Detected.	0	No Fault.
			1	Fault.
3	Processor Fault Detected	Processor fault detected.	0	No Fault.
			1	Fault.
1	Other Communication Fault	A communication fault other than the ones listed in this table has occurred.	0	No Fault.
			1	Fault.
0	Memory Or Logic Fault	Other Memory Or Logic Fault has occurred.	0	No Fault.
			1	Fault.

STATUS_OTHER (0x7F)
Description: Returns a brief other fault/warning status bits.

Bit	Description	Value	Description
0	The device was the first to assert SMBALERT.		

STATUS_MFR_SPECIFIC (0x80)
Description: Returns manufacturer specific status information.

Bit	Function	Description	Value	Description
7	Sync Fault	Sync fault.	0	No fault.
			1	Fault.
6	Iout Average Overcurrent Fault	Iout average overcurrent fault.	0	No fault.
			1	Fault.
5	Iout Fast Overcurrent Fault	Iout fast overcurrent fault.	0	No fault.
			1	Fault.
4	Short Circuit Protection Fault	Short circuit protection fault.	0	No fault.
			1	Fault.
3	Overtemperature2 Fault	Overtemperature2 fault.	0	No fault.
			1	Fault.
2	Auxiliary Voltage Fault	Auxiliary voltage fault.	0	No fault.
			1	Fault.
1	Startup Over Current Fault	Startup over current fault.	0	No fault.
			1	Fault.
0	Overtemperature2 Warn	Overtemperature2 warn.	0	No fault.
			1	Fault.

READ_VIN (0x88)
Description: Returns the measured input voltage.

Bit	Description	Format	Unit
15:0	Returns the input voltage reading.	Linear	V

READ_VOUT (0x8B)
Description: Returns the measured output voltage.

Bit	Description	Format	Unit
15:0	Returns the measured output voltage.	Vout Mode Unsigned (Exp = -11)	V

READ_IOUT (0x8C)
Description: Returns the measured output current.

Bit	Description	Format	Unit
15:0	Returns the measured output current.	Linear	A

READ_TEMPERATURE_1 (0x8D)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_TEMPERATURE_2 (0x8E)
Description: Reads temperature from the temperature sensor chosen in MFR_SELECT_TEMPERATURE_SENSOR (0xDC) command.

Bit	Description	Format	Unit
15:0		Linear	°C

READ_DUTY_CYCLE (0x94)
Description: Returns the actual duty cycle in percent.

Bit	Description	Format	Unit
15:0	Returns the actual duty cycle in percent.	Linear	%

READ_FREQUENCY (0x95)
Description: Returns the actual switching frequency.

Bit	Description	Format	Unit
15:0	Returns the actual switching frequency.	Linear	kHz

PMBUS_REVISION (0x98)
Description: Returns the PMBus revision number for this device.

Bit	Function	Description	Value	Function	Description
7:4	Part I Revision	Part I Revision.	0x0	1.0	Part I Revision 1.0.
			0x1	1.1	Part I Revision 1.1.
			0x2	1.2	Part I Revision 1.2.
			0x3	1.3	Part I Revision 1.3.
3:0	Part II Revision	Part II Revision.	0x0	1.0	Part II Revision 1.0.
			0x1	1.1	Part II Revision 1.1.
			0x2	1.2	Part II Revision 1.2.
			0x3	1.3	Part II Revision 1.3.

MFR_ID (0x99)
Description: Sets the Manufacturers ID

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_MODEL (0x9A)
Description: Sets the MFR MODEL string.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

MFR_REVISION (0x9B)
Description: Sets the MFR revision string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_LOCATION (0x9C)
Description: Sets the MFR location string.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_DATE (0x9D)
Description: This command returns the date the regulator was manufactured.

Bit	Description	Format
95:0	Maximum of 12 characters.	ASCII

MFR_SERIAL (0x9E)
Description: This command returns a string of 13 characters and numbers that provides a unique identification of the regulator.

Bit	Description	Format
159:0	Maximum of 20 characters.	ASCII

USER_DATA_00 (0xB0)
Description: This command is available as generic read/write storage for customers.

Bit	Description	Format
127:0	16 bytes of user data.	Byte Array

FW_CONFIG_REGULATION (0xC5)
Description: FW CONFIG REGULATION parameter

Bit	Description	Value	Function	Description
0	Enable diode emulation at startup	0	Disabled	
		1	Enabled	

FW_CONFIG_FAULTS (0xC8)
Description: FW CONFIG FAULTS parameter

Bit	Function	Description	Value	Function	Description

7:6	Vout Delay Unit	Vout_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vout Delay Unit Time unit for retry responses
			01	4ms/unit	Vout Delay Unit Time unit for retry responses
			10	16ms/unit	Vout Delay Unit Time unit for retry responses
			11	256ms/unit	Vout Delay Unit Time unit for retry responses
5:4	Vin Delay Unit	Vin_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Vin Delay Unit Time unit for retry responses
			01	4ms/unit	Vin Delay Unit Time unit for retry responses
			10	16ms/unit	Vin Delay Unit Time unit for retry responses
			11	256ms/unit	Vin Delay Unit Time unit for retry responses
3:2	Iout Delay Unit	IOUT_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	IOUT Delay Unit Time unit for retry responses
			01	4ms/unit	IOUT Delay Unit Time unit for retry responses
			10	16ms/unit	IOUT Delay Unit Time unit for retry responses
			11	256ms/unit	IOUT Delay Unit Time unit for retry responses
1:0	Temperature Delay Unit	Temperature_Delay_Unit Time unit for retry responses. 0: 1ms, 1: 4ms, 2: 16ms, 3: 256ms	00	1ms/unit	Temperature Delay Unit Time unit for retry responses
			01	4ms/unit	Temperature Delay Unit Time unit for retry responses
			10	16ms/unit	Temperature Delay Unit Time unit for retry responses
			11	256ms/unit	Temperature Delay Unit Time unit for retry responses

FW_CONFIG_PMBUS (0xC9)
Description: This command contains various configurable settings related to PMBus address and digital pins.

Bit	Function	Description	Format
31:24	PMBus Base Addr	Base Address for PMBus offset to start from	Integer Unsigned
23:17	PMBus Addr Offset	PMBUS Address offset when resistor offset Not enabled	Integer Unsigned

Bit	Function	Description	Value	Function	Description
33	Power good polarity	Power good polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
32	Control pin polarity	Control pin polarity (1:active high; 0: active low).	0	Active low	
			1	Active high	
16	PMBus Addr Offset Resistor Enable	PMBus_addr_offset_enable Enable PMBUS Address Offset via resistor	0	Disabled	
			1	Enabled	

MFR_IOUT_OC_FAST_FAULT_RESPONSE (0xCA)
Description: Output over current fault response.

Bit	Function	Description	Value	Function	Description

7:6	Response	For all values of bits [7:6],the device: Sets the corresponding fault bit in the status registers and If the device supports notifying the host, it does so.	00	Ignore Fault	The PMBus device continues to operate indefinitely while maintaining the output current at the value set by IOUT_OC_FAULT_LIMIT without regard to the output voltage (known as constant-current or brickwall limiting).
			11	Shutdown and Retry	The PMBus device continues to operate, maintaining the output current at the value set by IOUT_OC_FAST_FAULT_LIMIT without regard to the output voltage, for the delay time set by bits [2:0] and the delay time units for specified in the IOUT_OC_FAST_FAULT_RESPONSE. If the device is still operating in current limiting at the end of the delay time, the device responds as programmed by the Retry Setting in bits [5:3].
5:3	Retries	The device attempts to restart the number of times set by these bits. 000b means the device does not attempt a restart. 111b means the device attempts restarting continuously.	000	Do Not Retry	A zero value for the Retry Setting means that the unit does not attempt to restart. The output remains disabled until the fault is cleared (Section 10.7).
2:0	Retry Time and Delay Time	Number of delay time units. Used for either the amount of time the device is to continue operating after a fault is detected or for the amount of time between attempts to restart. The time unit is set in register 0xC8.	0	1	
			1	2	
			2	4	
			3	8	
			4	16	
			5	32	
			6	64	
			7	128	

MFR_IOUT_AVG_OC_FAULT_LIMIT (0xD0)
Description: Average output over current limit.

Bit	Description	Format	Unit
15:0	Average output over current fault limit.	Linear	A

MFR_IOUT_OC_FAST_FAULT_LIMIT (0xD1)
Description: The MFR_IOUT_OC_FAST_FAULT_LIMIT command sets or retrieves Iout fast overcurrent fault threshold, in Amperes.

Bit	Description	Format	Unit
15:0	Sets Iout fast over-current fault threshold.	Integer Unsigned	A

MFR_IOUT_AVG_COEFF (0xD2)
Description: Coefficient for controlling the averaging strength for the averaged Iout current limit.

Bit	Description	Format
5:0	Coefficient for controlling the averaging strength for the averaged Iout current limit. Value 0-63. Setting the coefficient to 0 will disable the averaging and the average current limit behaviour.	Integer Unsigned

MFR_READ_VAUX (0xDA)
Description: Returns the measured auxiliary input voltage.

Bit	Description	Format	Unit
15:0	Returns the auxiliary input voltage reading.	Linear	V

MFR_SELECT_TEMPERATURE_SENSOR (0xDC)
Description: Select which temperature sensor, internal one or external remote temperature sensor, is used.

Bit	Function	Description	Value	Function	Description
4:3	Fault Source Select	Select which temperature sensor, internal one or external remote temperature sensor, is used.	00	Temp A	Temp A temperature sensor selected.
			01	Temp B	Temp B temperature sensor selected.
			10	Temp I	Temp I temperature sensor selected.
2:0	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select	READ_TEMPERATURE_1 READ_TEMPERATURE_2 Source Select.	000	TempA TempB	TempA (External Temperature sensor A) TempB (External Temperature sensor B).
			001	TempA TempI	TempA (External Temperature sensor A) TempI (Internal Temperature sensor).
			010	TempB TempA	TempB (External Temperature sensor B) TempA (External Temperature sensor A).
			011	TempB TempI	TempB (External Temperature sensor B) TempI (Internal Temperature sensor).
			100	TempI TempA	TempI (Internal Temperature sensor) TempA (External Temperature sensor A).
			101	TempI TempB	TempI (Internal Temperature sensor) TempB (External Temperature sensor B).

MFR_FLEX_FIRMWARE_CMD (0xE0)
Description: Mfr. firmware command.

Bit	Description	Format
63:0	Mfr. firmware command.	Byte Array

MFR_TEMP_COEFF (0xE7)
Description: Coefficient for Iout temperature compensation.

Bit	Description	Format
15:0	Coefficient in Q16. Iout compensation factor calculated according to: 1 / (1 + t_coeff * (T - 20))	Fixed Point Unsigned

MFR_IOUT_APC (0xEA)
Description: The iout apc gain.

Bit	Description	Format	Unit
15:0	SSet the iout apc gain. the format is Linear 11, Exponent is -9 or -8 (User selection possible). The LSB varies with isen_gain_mode - ISEN_LSB/Secondary current sense resistor (Rsense).	Linear	A

MFR_MULTI_PIN_CONFIG (0xF9)
Description: The MFR_MULTI_PIN_CONFIG command can be re-configured to enable or disable different functions and set the pin configuration.

Bit	Function	Description	Value	Function	Description
7	SMBAlert pin function	Selects if the SMBAlert pin should be used for ALERT or SYNC.	00	Alert	SMBAlert pin used as SMBAlert.
			01	Sync	SMBAlert pin used as SYNC.
6:5	Sync Mode	These bits configures the direction of the sync pin as either SYNC OUT or SYNC IN. Use the INTERLEAVE command to enable/disable the sync function and to configure phase offset.	00	Disabled	Sync Pin disabled.
			01	Sync in	When the product is configured as SYNC IN it will synchronize its switching frequency to an external sync signal. The switching phases can be spread individually using the INTERLEAVE command 0x37.
			10	Sync out	When the product is configured as SYNC OUT it will send out a SYNC signal. Only 1 product in a group can be configured as SYNC OUT.
4	Sync Output	Selects the output type of the Sync pin.	0	Open Drain	Sync output configured as Open Drain.
2	Power Good Pull-down	This bit enables or disables Power Good pin pull-down.	0	Disabled	
			1	Enabled	
1	Power Good Output	Two output options are available for Power Good output, they are Push/Pull or Open Drain.	0	Push/Pull	Power Good configured Push/Pull.
			1	Open Drain	Power Good configured Open Drain.