



BMR 350 2253/803

1300-1700 W digital quarter brick DC/DC IBC

The BMR350 2253/803 is the new generation of high-power digital DC/DC converter. The impressive performance of this converter includes an efficiency of 97.7% at 51V_{in} and half load.

The BMR350 2253/803 is a non-isolated quarter brick, and has a low building height of only 12 mm. The converter delivers a fully regulated 12.20 V. BMR350 2253/803 delivers a continuous power level of 1300 W and has a peak power capability of up to 1700 W for limited time.

This converter is designed for through-hole mounting using wave solder or pin-in-paste production, and incorporates a novel design of baseplate, which optimizes thermal performance while minimizing height.







Key features

- High efficiency with 97.7%
- Non-isolated
- 12.20V fully regulated
- Event data recorder (black-box)
- Parallel operation with DLS Current Sharing
- Screwed baseplate
- Monotonic start-up
- Output over voltage protection
- Over temperature protection
- Output short-circuit protection
- Remote control
- PMBus configuration

Soldering methods

- Reflow soldering Pin In Paste
- Wave soldering
- Manual soldering

Key electrical information

Parameter	Values
Input range	40-60 V
Output voltage	12.20V
Output current	108A
Peak current	140A
Output power	1300 W
Peak power	1700 W

Mechanical

58.4 x 36.8 x 12 mm / 2.30 x 1.45 x 0.47 in

Application areas

• Datacom applications



Product options

The table below describes the different product options.

Example:	BMR350	2	2	53	/803	Н	Definitions
Product family	BMR350						
Pin length options		2					0 = 5.33 mm / 0.21 in 2 = 3.69 mm / 0.15 in 3 = 4.57 mm / 0.18 in 4 = 2.79 mm / 0.11 in
Baseplate / HS option			2				2 = Baseplate top and bottom
Other hardware options				53			53 = 1300W with Sense function, 7-pin digital header
Configuration code					/803		/803=12.20 V _{out} , DLS current sharing
Packaging options						Н	E = soft tray, dry pack (PIP reflow soldering) H = hard tray, dry pack (PIP reflow soldering) blank = foam tray (no dry pack, wave soldering)

For more information, please refer to Part 3 Mechanical information.

If you do not find the variant you are looking for, please contact us at <u>Flex Power Modules</u>.

Order number examples

Part number	Vin	Outputs	Configuration
BMR350 2253/803H	40-60 V	12.20V / 108A/ 1300W	3.69 mm pins / 7-pin digital header / base plate / sense /DLS /dry pack, hard tray



Absolute maximum ratings

Stress in excess of our defined absolute maximum ratings may cause permanent damage to the converter. Absolute maximum ratings, also referred to as non-destructive limits, are normally tested with one parameter at a time exceeding the limits in the electrical specification.

Characteristics	min	typ	max	Unit
Operating temperature (T _{P1})	-40		+125	°C
Storage temperature	-55		+125	°C
Input voltage (Vin)	-0.5		+65	V
Isolation voltage (input to output)			0	V
Isolation voltage (baseplate to output)			0	V
Remote control pin voltage	-0.3		5	V

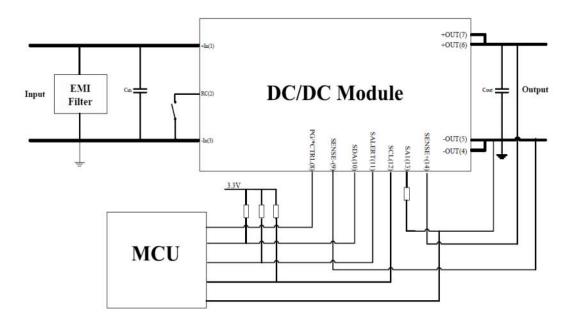
Reliability

Failure rate (λ) and mean time between failures (MTBF= 1/ λ) are calculated based on *Telcordia SR-332 Issue 4: Method 1, Case 3, (80% of Iout_TDP, TP1=40°C, Airflow=200 LFM)*.

Typical application diagram

	Mean	90% confidence level	Unit
Steady-state failure rate (λ)	123	151	nfailures/h
Standard deviation (σ)	22.2		nfailures/h
MTBF	8.1	6.6	MHr

Capacitor values are defined in the Electrical Specification tables. The EMI filter is defined in the EMC Part 2.





Electrical specifications for BMR350 2253/803

12.20V, 108A (140A) $\leq 1300W$ (1700W)

Min and Max values are valid for: $T_{P1} = -30$ to $+90^{\circ}$ C, $V_{in} = 40$ V to 60V, unless otherwise specified under conditions. Typical values given at: $T_{P1} = +25^{\circ}$ C, $V_{in} = 51$ V, max P_{out_TDP} , unless otherwise specified under conditions, see Note 1.

Additional external $C_{in} = 220 \mu F$, $C_{out} = 1.5 mF$

Characteristic	conditions	minimum	typical	maximum	unit		
Key features	Key features						
	50% of Pout_TDP		97.7		%		
Efficiency (ŋ)	100% of Pout_TDP		96.9		%		
P _{out_TDP} thermal design power (TDP)	See Note 1			1300	W		
P _{out_MAX} peak power (t ≤200ms)	See Note 1			1700	W		
Power dissipation	100% of Pout_TDP		42.4	57.6	W		
Switching frequency (fs)	0-100 % of P _{out_TDP}	130	150	160	kHz		
Recommend capacitive load		1500		20000	μF		
Input characteristics							
Input voltage range (Vin)		40		60	V		
Input idling power	P _{out} = 0 W		5.4	12.1	W		
Input standby power	(turned off with RC)		552	768	mW		
Input OVP			70		V		
Internal input capacitance			90		μF		
Recommended external input capacitance		220			μF		

Note 1: Max. output current is rated at 140 A. Max power is \leq 1700 W and continuous power (thermal design power (TDP) is \leq 1300 W depending on thermal conditions.)



Electrical specifications for BMR350 2253/803

12.2V, 108A (140A) $\leq 1300W$ (1700W)

Min and Max values are valid for: T_{P1} = -30 to +90°C, V_{in} = 40V to 60V, unless otherwise specified under conditions. Typical values given at: T_{P1} = +25°C, V_{in} = 51V, max P_{out_TDP} , unless otherwise specified under conditions, see Note 1.

Additional external $C_{in} = 220 \mu F$, $C_{out} = 1.5 mF$

Characteristic	conditions	minimum	typical	maximum	unit
Output characteristics					
Output voltage initial set- ting and accuracy	P _{out} = 0 W	12.16	12.20	12.24	V
Output voltage tolerance band	0 – 100% of max P _{out_TDP} V _{in} = 40-60 V	11.50		12.30	V
Output adjust range	0-100% of max Pout_TDP	8		13.2	V
Idling voltage	P _{out} = 0 W, 51 V	12.05		12.35	V
Line regulation	V _{in} = 40 - 60 V 0 - 100% of max P _{out_TDP}		15		mV
Load regulation	0 - 100% of max Pout_TDP		550		mV
Output current (Iout)	V _{in} = 40 - 60 V	0		108	А
Load transient voltage deviation	Load step 25-75-25% of		±400		mV
Load transient recovery time	max P _{out_TDP} di/dt = 1 A/µs. See Note 2		200		μs
Output ripple & noise	max P _{out_TDP} See Note 3		50	160	mV _{p-p}

Note 1: Max. output current is rated at 140 A. Max power is \leq 1700 W and continuous power (thermal design power (TDP) is \leq 1300 W depending on thermal conditions.

Note 2: Cout is 2x2.2mF

Note 3: See Technical Reference doc: Design considerations



Electrical specifications for BMR3502253/803

12.20V, 108A (140A) ≤ 1300W (1700W)

Characteristic	conditions	minimum	typical	maximum	unit	
On/off control						
Turn-off input voltage	Decreasing input voltage	34	35	37	V	
Turn-on input voltage	Increasing input voltage	35	37	39	V	
Ramp-up time (from 0–100% of V _{out})		7	10	13	ms	
Start-up time (from V _{in} connection to 90% of V _{out})			40		ms	
RC start-up time			25		ms	
Logic high: trigger level			1.4		V	
Logic low: trigger level			1.3		V	
Logic low: response time		0.1	0.2	0.3	ms	
Sink current		0.4			mA	
Protection features						
Current limit threshold (OCP)	T _{P1} < max T _{P1}	176	190	204	Α	
Output current limit (OCP) response time and type	latch		25		μs	
Current limit threshold (Slow OCP)			130		Α	
Current limit Off Delay (Slow OCP)	Load step 108-140A.			200	ms	
Output overvoltage protection (OVP)			14.2		٧	
Output overvoltage protection (OVP) response time and type	Disabled until fault cleared		70		μs	
Over temperature protection (OTP)	See note 1		125		°C	
Over temperature protection (OTP) re-start temperature and type	Disabled until fault cleared		90		°C	

Note 1: Please attach thermocouple on NTC resistor to test OTP function, the hot spot (P1) temperature is just for reference.



Electrical specifications for BMR350 2253/803

12.20V, 108A (140A) $\leq 1300W$ (1700W)

In the table below all PMBus are written in capital letters.

 T_{P1} = -30 to +90 °C, V_{in} = 40V to 60V, unless otherwise specified under conditions.

Typical values given at: $T_{P1} = +25$ °C, $V_{in} = 51$ V, max P_{out_TDP} , unless otherwise specified under conditions

Command	Conditions	minimum	typical	maximum	Unit		
Monitoring accuracy	Monitoring accuracy						
Input voltage READ_VIN			±125		mV		
Output voltage READ_VOUT			±40		mV		
Output current	$T_{P1} = 25 ^{\circ}\text{C}, V_{O} = 12 \text{V}$		±0.5		А		
READ_IOUT	T _{P1} = -20 - 120 °C, V _O = 12 V		±2.5		А		
Duty cycle READ_DUTY_CYCLE No tolerance, Read value is the actual value applied by PWM controller							
Temperature READ_TEMPERATURE_1	Temperature sensor		±5		°C		

For more detailed information please refer to Technical Reference Document: PMBus commands.

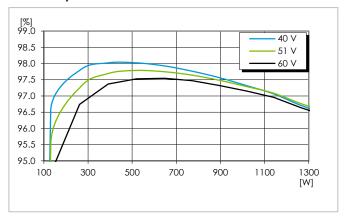
This product is supported by the Flex Power Designer tool.



Electrical graphs for BMR350 2253/803

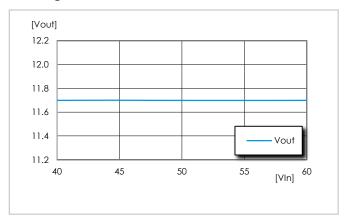
12.20V, 108A (140A) $\leq 1300W$ (1700W)

Efficiency



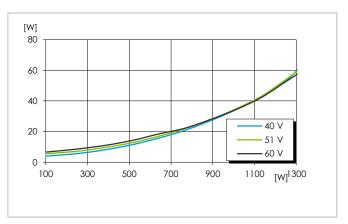
Efficiency vs. output power and input voltage at $T_{P1} = +25^{\circ}$.

Line regulation



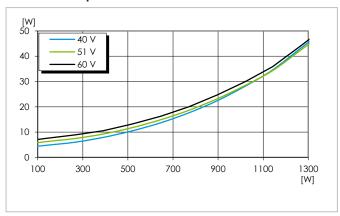
Output voltage vs. input voltage at T_{P1} = +90°C, 100% of max Pout_TDP, 1300W.

Power loss at max temperature



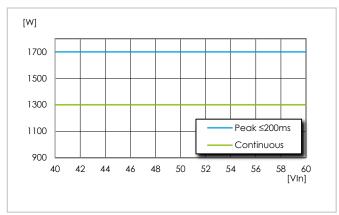
Dissipated power vs. output power and input voltage at T_{P1} = +90°C, Above Pout_TDP, 1300W only peak t \leq 200ms

Power dissipation



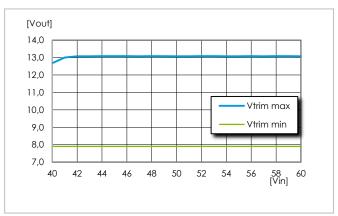
Dissipated power vs. load power at T_{P1} = +25°C. Above Pout_TDP, 1300W only peak t \leq 200ms

Available power



Available output power vs. input voltage, TP1 = +90°C

Output voltage adjust range



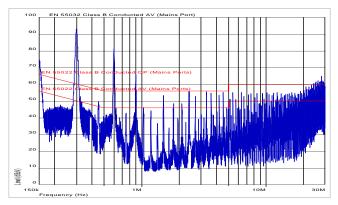
Max and min V_{out} trim vs V_{in.} At Pout_TDP, 1300W.



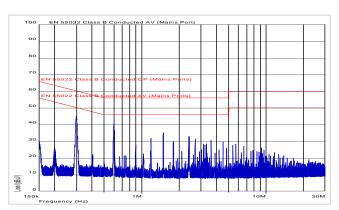
Part 2: EMC

EMC specifications

Conducted EMI measured according to EN55022 / EN55032, CISPR 22 / CISPR 32 and FCC part 15J (see test set-up below). The fundamental switching frequency is 150 kHz for BMR350. The EMI characteristics below is measured at V_{in} = 51 V and max I_{out} .



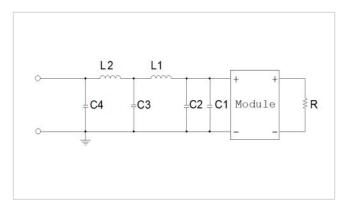
EMI without filter. EN55032 test method and limits are the same as EN55022. 1000 μF 100V input capacitor and 1mF + 470 μF 16 V OS-CON output capacitors used



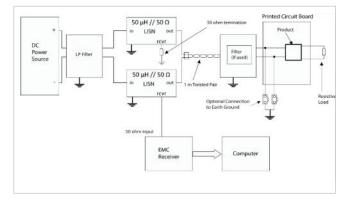
EMI with filter, EN55032 test methods and limits are the same as EN55022

Optional external filter for Class B

Suggested external input filter in order to meet Class B in EN 55022 / EN 55032, CISPR 22 / CISPR 32 and FCC part 15J.



Filter components: C1 = 1000μF (e-lyt) C2 = 2 x 2.2 μF C3, C4 = 10 μF L1, L2 = 4.7 μH



Test set-up

Layout recommendations

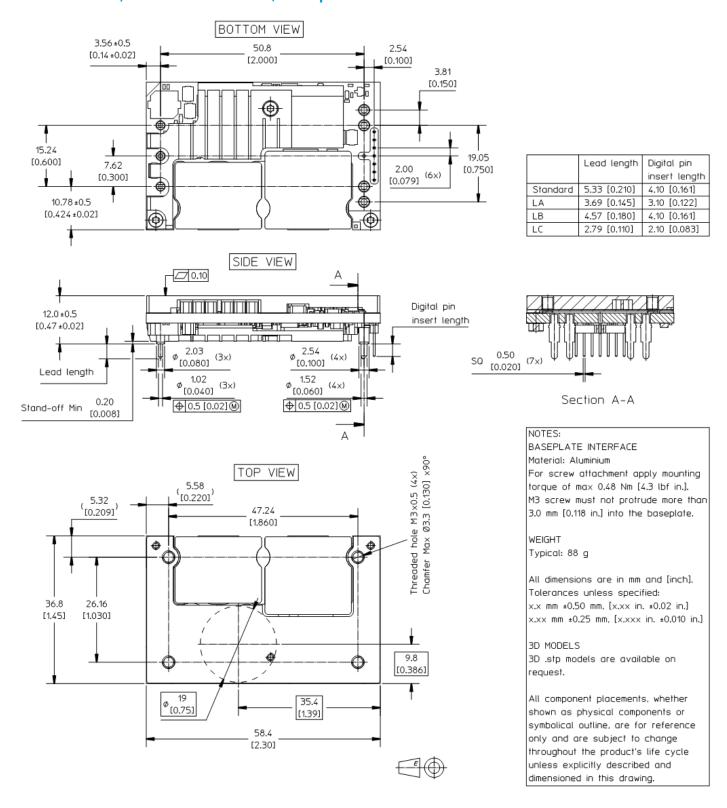
The radiated EMI performance of the product will depend on the PWB layout and ground layer design. It is also important to consider the stand-off of the product. If a ground layer is used, it should be connected to the output of the product and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PWB and improve the high frequency EMC performance.



Part 3: Mechanical information

BMR350 2253/803: hole mounted, baseplate version



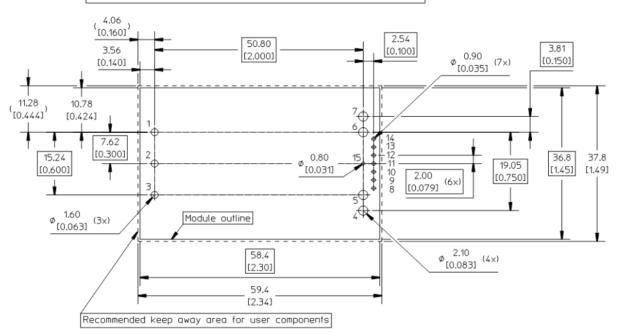
The mechanical information is based on a module which is hole mounted and has a baseplate.



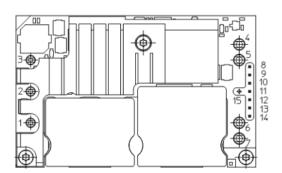
Part 3: Mechanical information

Footprint and pin positions

OUTLINE AND RECOMMENDED FOOTPRINT - TOP VIEW



CONNECTIONS - BOTTOM VIEW



PIN SPECIFICATIONS:
Pin 1-3, 4-7 Material: Copper alloy Plating: Min Au 0.1 µm over Ni 1-3 µm
Pin 8-14 Material: Brass Plating: Min Au 0.1 µm over Ni 1-3 µm
Pin 15 Not mounted (Option)

Pin	Desig- nation	Function 7 pin
1	+ln	Positive Input
2	RC	Remote Control
3	-In	Negative Input
4	-Out	Negative Output
5	-Out	Negative Output
6	+Out	Positive Output
7	+Out	Positive Output

Pin	Desig- nation	Function 7 pin
8	PG	Power Good
9	SENSE-	Remote Sense-
10	SDA	PMBus Data
11	SALERT	PMBus alert signal
12	SCL	PMBus Clock
13	SA1	PMBus Address 1
14	SENSE+	Remote Sense+



Part 4: Thermal considerations

Thermal considerations

The products are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

General

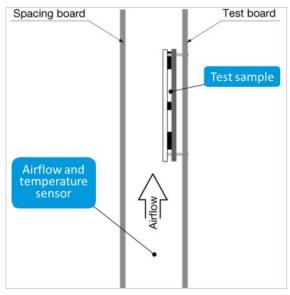
For products mounted on a PWB without a heatsink attached, cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependent on the airflow across the product. Increased airflow enhances the cooling of the product. The wind speed and temperature are measured in a point upstream the device. The output current derating graphs found later in this section for each model provide the available output current vs. ambient air temperature and air velocity at $V_{in} = 51 \text{ V}$.

For products using any form of heatsink structure a top spacing board and side airflow guides are used to ensure airflow hitting the module and not diverted away.

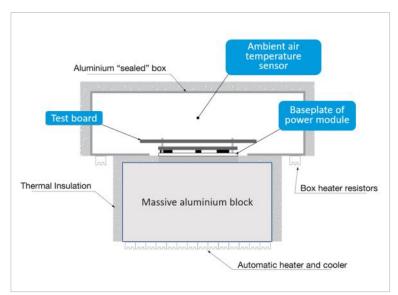
Distance between the tested device and the top space board and the side airflow guides are $6.35 \text{ mm} \pm 1 \text{ mm}$.

The product is tested on a 185×185 mm, $105 \, \mu m$ (3 oz), 6-layer test board mounted vertically in a wind tunnel.

For products with baseplate used in a sealed box/cold wall application, cooling is achieved mainly by conduction through the cold wall. The product is tested in a sealed box test set up with ambient temperatures 85°C. See Design Note 028 for further details.



Picture: general test set-up



Picture: cold wall test set-up



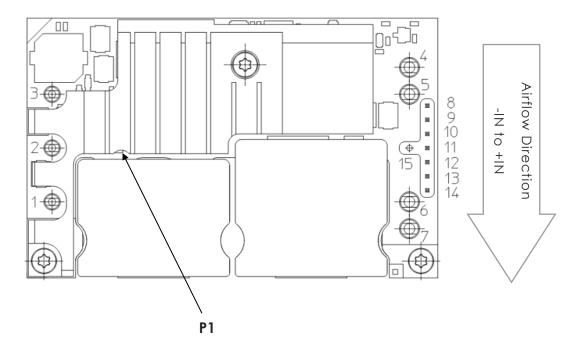
Part 4: Thermal considerations

Definition of product operating temperature

Proper thermal conditions can be verified by measuring the temperature at position P1 as shown below. The temperature at this position (T_{P1}) should not exceed the maximum temperatures in the table below. The number of measurement points may vary with different thermal design and topology. Temperatures above maximum T_{P1}, measured at the reference point P1 are not allowed and may cause permanent damage.

Position	Description	Max. Temp.
P1	PWB reference point	T _{P1} = 125 °C

Bottom view

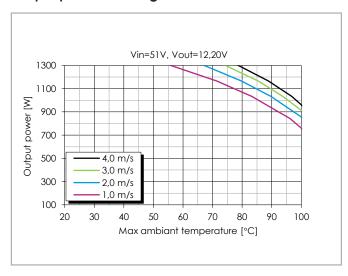




Part 4: Thermal considerations

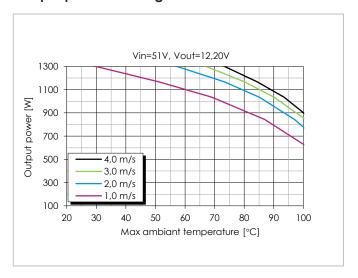
Thermal graphs

Output power derating - 1.0 inch heatsink



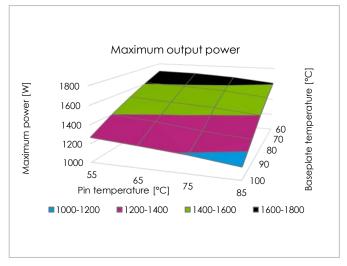
Available output power vs. ambient air temperature and airflow. Above Pout_TDP, 1300W only for a limited time. Airflow Direction -IN to +IN

Output power derating - 0.5 inch heatsink



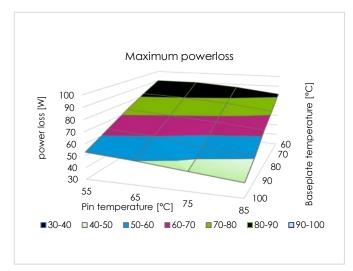
Available output power vs. ambient air temperature and airflow. Above Pout_TDP, 1300W only for a limited time. Airflow Direction -IN to +IN.

Maximum Output Power



Maximum allowed thermal power (restriction by POMAX might occur). Above Pout_TDP, 1300W only for a limited time.

Maximum Allowed Power Loss



Available output power vs. pin and baseplate temperature See Thermal Consideration section. Above Pout_TDP, 1300W only for a limited time.

For more information, please refer to our thermal models on the website.



Part 5: Packaging **Packaging information**

H option: Select for PIP reflow solder and pick & place - dry packed

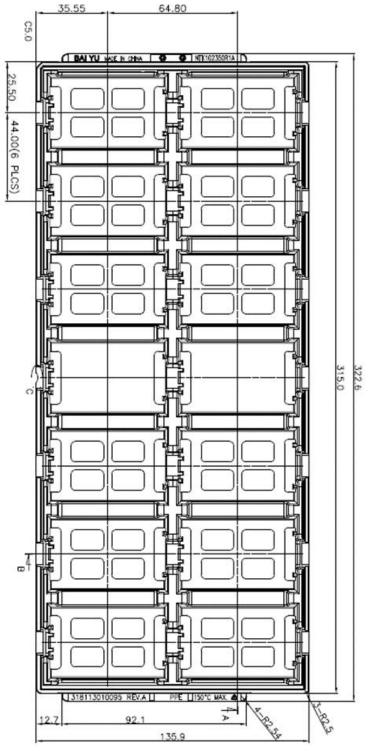
Material	Antistatic Polyphenylene Ester (PPE)	
Surface resistance	≥ 1 x 10 ⁴ to < 1 x 10 ¹¹ ohms	
Bakabilty	Tray can be baked at max. 125 °C for 24 h.	
Tray thickness	25.0 mm 0.984 [inch]	
Tray capacity	14 converters/tray	
Box capacity	42 products (3 full trays/box)	
Tray weight	156g empty tray, 1385g full tray - open deck baseplate.	

JEDEC standard tray. All dimensions in mm

Tolerances: X.x ±0.26 [0.01], X.xx ±0.13 [0.005]

Note: Pick up positions refer to center of pocket.

See <u>mechanical drawing</u> for exact location on product.



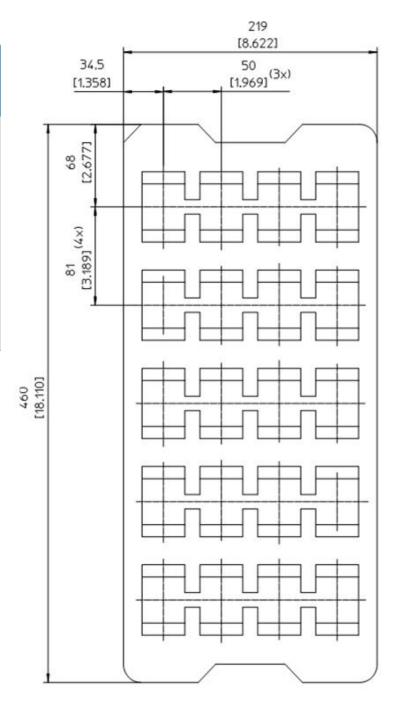
Example hard tray (14 pcs)



Part 5: Packaging

Packaging information

Blank option: Select for wave or hand soldering, NOT dry packed				
Material	Antistatic Polyethylene (PE) foam			
Surface resistance	≥ 1 x 10 ⁴ to < 1 x 10 ¹¹ ohms			
Bakabilty	Tray cannot be baked			
Tray capacity	20 converters / tray			
Box capacity	60 products (3 full trays/box)			
Weight	48 g empty tray, 1800g full tray.			



Example PE foam tray



Part 6: Revision history Revision table

Revision number	revision change	date	revisor
A	New	2023/2/10	jidmawan
В	Update hardtray and packing weight & modified value of slow ocp	2023/3/9	jidjliaa

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