

BMR511

2-phase integrated power stage up to 140 A peak

The BMR511 is a two-phase integrated power stage with a continuous output current of 80A, and a peak current of 140 A.

It comes in a compact footprint of just 0.9 cm² / 0.14 in² and is available with either LGA or solder bump termination.

The device is designed with the power stages located on bottom for the most effective down stream conducted cooling, and delivers excellent thermal results.

The BMR511 features protection mechanisms such as over-current protection and over-temperature protection. Two enable inputs are provided for each phase, and the module accepts tri-state PWM inputs.



Key features

- High efficiency:
 - 1.8V - up to 94.5% at light load, 92.5% at full load.
 - 0.75V - up to 92% at light load, 88% at full load.
- Can be paralleled with other units
- Remote control
- Reporting temperature and current for each phase
- Excellent thermal performance
- Halogen-free

Soldering methods

- Reflow soldering, LGA and solder bump options

Key electrical information

Parameter	Values
Input voltage range	5 - 15 V
Output voltage range	0.5 - 1.8 V
Max output current	80 A TDC/ 140 A peak

Mechanical

10 x 9 x 8 mm / 0.39 x 0.35 x 0.31 in

Application areas

- Designed for AI applications
- Used by CPU, GPUs, IPUs, high-performance ASICs

Product options

The table below describes the different product options.

	BMR511	1	04	4	/002	C	Definitions
Product family	BMR511						
Mounting options		1					0 = solder bump 1 = LGA
Product variants			04				04 = standard electrical variant
Mechanical configuration				4			4 = standard mechanical configuration
Configuration code					/002		/002 = 2 phases
Packaging options						C	C = Antistatic tape and reel packaging

For more information, please refer to [Part 2 Mechanical information](#).

If you do not find the variant you are looking for, please contact us at [Flex Power Modules](#).

Order number examples

Part number	V _{in}	outputs	configuration
BMR5111034/001C	5-15 V	0.5-1.8 V	Standard 2 phase module, tape and reel packaging

Part 1: Electrical specifications

Absolute maximum ratings

Stress in excess of our defined *absolute maximum ratings* may cause permanent damage to the converter. Absolute maximum ratings, also referred to as *non-destructive limits*, are normally tested with one parameter at a time exceeding the limits in the electrical specification.

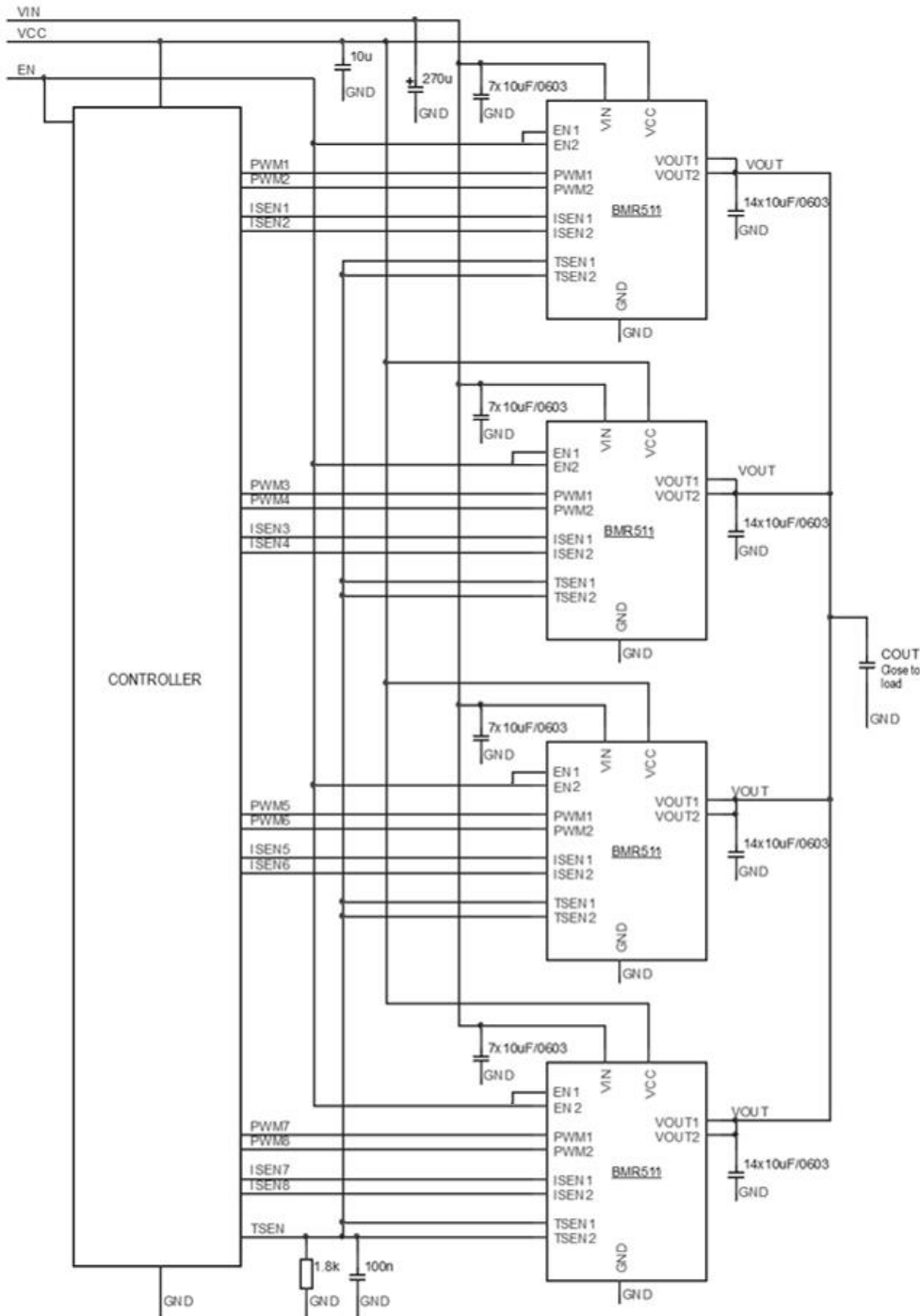
Characteristics		min	typ	max	Unit
Operating temperature (T _{P1})		-40		125	°C
Storage temperature		-40		125	°C
Input voltage (V _{in})		-0.3		20	V
Signal I/O voltage	EN, PWM,ISEN, TSEN	-0.3		VCC+0.3	V
Driver and logic supply	VCC	-0.3	3.3	4	V

Reliability

Failure rate (λ) and mean time (50%) between failures (MTBF= 1/ λ) are calculated based on *Telcordia SR-332 Issue 4: Method 1, Case 3, (80% of I_{out}, T_{P1}=40°C)*.

	Mean	90% confidence level	Unit
Steady-state failure rate (λ)	14	21	nfailures/h
Standard deviation ()	5.5		nfailures/h
MTBF	70.13	46.97	MHr

Typical application diagram



Typical application circuit for 8 phase rail

Notes:

1. Value of output capacitance will depend on the application and load transient requirements.
2. TSEN filter values might be adjusted if connecting together TSEN of more than 16 phases.

Electrical specifications for BMR511 — Control and Monitoring

$T_{P1} = -40\text{ °C}$ to 125 °C , $V_I = 12\text{ V}$, $V_{CC} = V_{EN} = 3.3\text{ V}$, unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25\text{ °C}$, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
UVLO _{VIN}	VIN Under Voltage Lock-Out	Rising threshold		2.5	3.0	V
		Hysteresis		200		mV
UVLO _{VCC}	VCC Under Voltage Lock-Out	Rising threshold		2.75	2.95	V
		Hysteresis		200		mV
V _{IL,EN}	EN input low threshold				0.9	V
V _{IH,EN}	EN input high threshold		1.4			V
V _{IL,PWM}	PWM input low threshold				0.6	V
V _{IH,PWM}	PWM input high threshold		2.6			V
V _{TRL,PWM}	PWM tri-state region		1.1		2.1	V
V _{HIZ,PWM}	PWM high impedance voltage			1.6		V
I _{PWM}	PWM sink/source current	PWM = 0 V		500		μA
		PWM = 3.3 V		-500		μA
t _{PWM}	PWM minimum pulse width			15		ns
V _{O,ISEN}	TSEN voltage when fault		3.0	3.3		V
G _{TSEN}	TSEN gain			8		mV/°C
O _{TSEN}	TSEN offset	T _J = +25 °C		800		mV
T _{TSEN}	TSEN overtemperature shutdown and fault flag			160		°C
G _{ISEN}	ISEN gain			5		μA/A
		Accuracy	-2	0	2	%
O _{ISEN}	ISEN offset	I _O = 0 A, V _{ISEN} = 1.2 V T _J = +25 °C	-4	0	4	μA
V _{ISEN}	ISEN voltage range		0.7		2.1	V
I _{LIM,H}	High-side current limit	Threshold, cycle-by-cycle		120		A
		Shutdown counter		10		Times
I _{LIM,L}	Low-side current limit	Threshold, cycle-by-cycle		-50		A
		No fault report Off time		200		ns

Electrical specifications for BMR511 – Power Conversion (1.8 V out)

$T_{P1} = -10\text{ °C}$ to 95 °C , $V_I = 5$ to 15 V , $V_{CC} = 3.3\text{ V}$, unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25\text{ °C}$, $V_I = 12\text{ V}$, $V_O = 1.8\text{ V}$, $I_O = 80\text{ A}$, $f_{sw} = 800\text{ kHz}$, 2-phase single rail operation, otherwise specified under Conditions.

Measurements made on Reference board ROA 170 347.

External $C_{IN} = 1 \times 270\text{ }\mu\text{F}/22\text{ m OSCON} + 28 \times 10\text{ }\mu\text{F}$ ceramic.

External $C_{OUT} = 10 \times 470\text{ }\mu\text{F}/3\text{ m POSCAP} + 356 \times 10\text{ }\mu\text{F}$ ceramic.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input supply	Continuous operation	5		15	V
		Peak			16	V
V_{CC}	Driver and logic supply		3.0	3.3	3.6	V
V_O	Output voltage range		0.5		1.8	V
V_{Oac}	Output ripple & noise	20 MHz BW		3		mVp-p
f_{sw}	Switching frequency	$T_{P1} = +25\text{ °C}$	500	800	1000	kHz
C_I	Internal input capacitance	$V_I = 0\text{ V}$		8.8		μF
C_O	Internal output capacitance	$V_O = 0\text{ V}$		0		μF
L_O	Output inductance	$I_O = 0\text{ A}$		120		nH
I_O	Output current, peak	2-phase operation			140	A
		1-phase operation			70	A
	Output current, continuous, Note 1	2-phase operation	0	80		A
		1-phase operation	0	40		A
I_{IN}	VIN input current	Standby, EN = low		10		μA
I_{VCC}	VCC input current	1-phase operation		38		mA
		$I_O = 40\text{ A}$				
		2-phase operation		71		mA
		$I_O = 80\text{ A}$				
		Standby, PWM1 = PWM2 = low		3		mA

Note 1: The maximum continuous output current will also be limited by the thermal conditions.

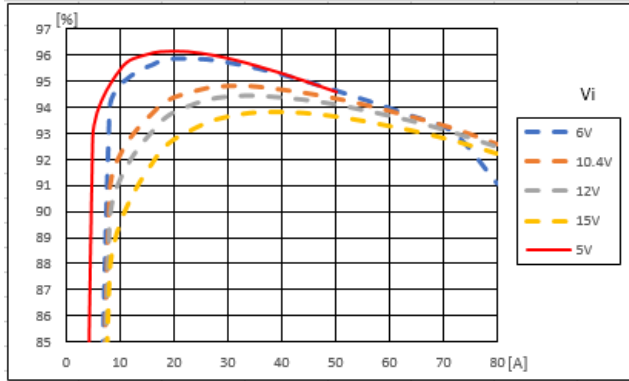
See derating graphs and section Thermal Considerations.

$V_{in} = 5\text{--}6\text{ V}$, $V_O = 1.8\text{ V}$, I_O typ = 50A

Electrical graphs for BMR511

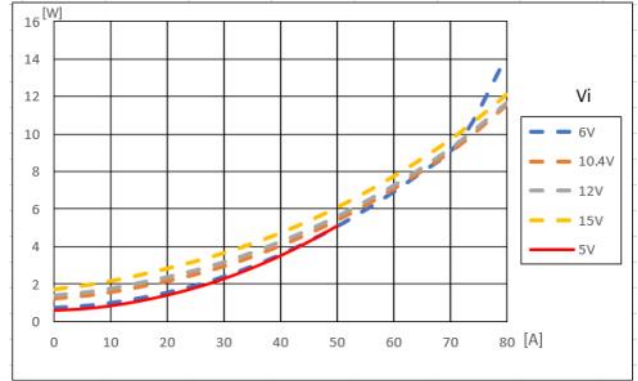
$V_{out} = 1.8\text{ V}$

Efficiency



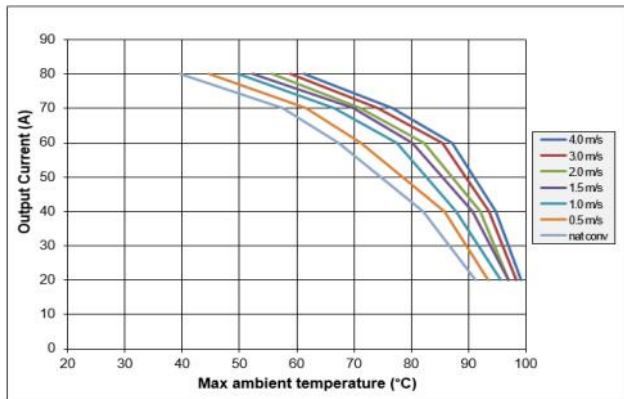
Driver losses excluded, 2 phases, fsw=800 kHz

Power dissipation



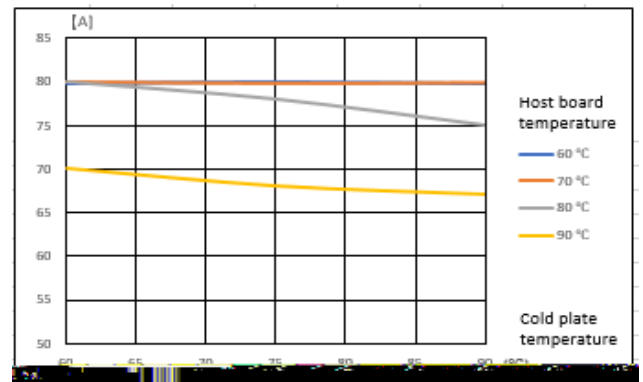
Driver losses excluded, 2 phases, fsw= 800 kHz

Output Current Derating-wind tunnel



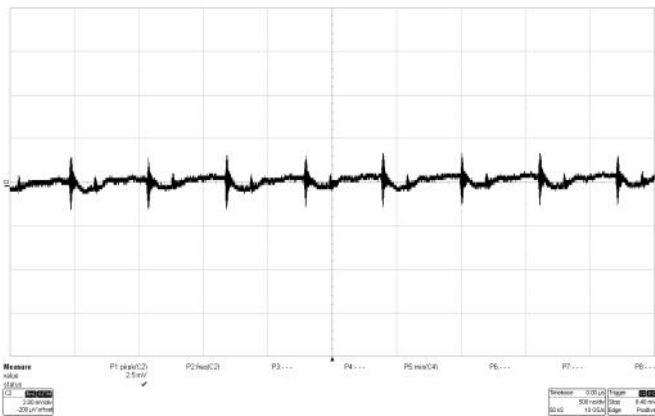
Available load current vs. ambient air temperature and air flow at $V_i = 12\text{ V}$.

Output Current Derating-cold wall



Thermal interface gap pad 1 mm, 8 W/mK. $V_i = 12\text{ V}$.

Output ripple



$V_i = 12\text{ V}$, $I_o = 80\text{ A}$, Scale: 2 mV/div, 0.5 μs /div, 20 MHz BW. $C_{OUT} = 10 \times 470\ \mu\text{F}/3\text{ m POSCAP} + 365 \times 10\ \mu\text{F}$ ceramic.

Part 1: Electrical specifications

Electrical specifications for BMR511 – Power Conversion (0.75 V out)

$T_{P1} = -10\text{ °C to }95\text{ °C}$, $V_I = 5\text{ to }15\text{ V}$, $V_{CC} = 3.3\text{ V}$, unless otherwise specified under Conditions.

Typical values given at: $T_{P1} = +25\text{ °C}$, $V_I = 12\text{ V}$, $V_O = 0.75\text{ V}$, $I_O = 80\text{ A}$, $f_{sw} = 450\text{ kHz}$, 2-phase single rail operation, otherwise specified under Conditions.

Measurements made on Reference board ROA 170 347.

External $C_{IN} = 1 \times 270\text{ }\mu\text{F}/22\text{ m OSCON} + 28 \times 10\text{ }\mu\text{F}$ ceramic.

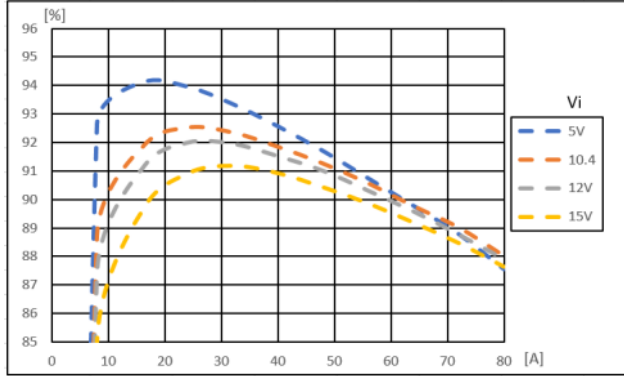
External $C_{OUT} = 10 \times 470\text{ }\mu\text{F}/3\text{ m POSCAP} + 356 \times 10\text{ }\mu\text{F}$ ceramic.

Characteristics	Conditions	min	typ	max	Unit
V_I Input supply					

Electrical graphs for BMR511

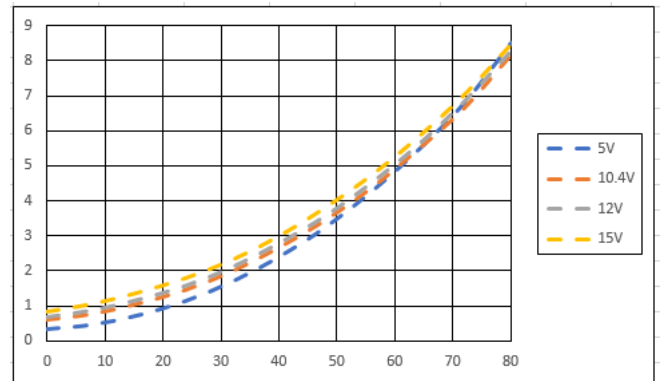
$V_{out} = 0.75 V$

Efficiency



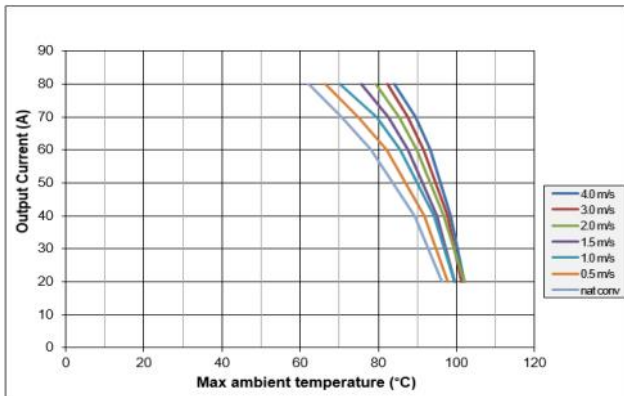
Driver losses excluded, 2 phases, fsw=450 kHz

Power dissipation



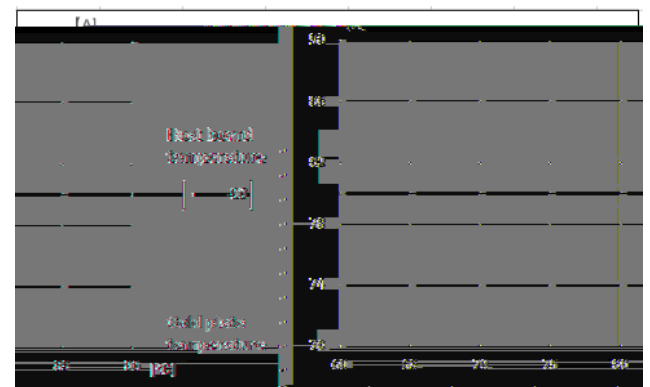
Driver losses excluded, 2 phases, fsw= 450 kHz

Output Current Derating-wind tunnel



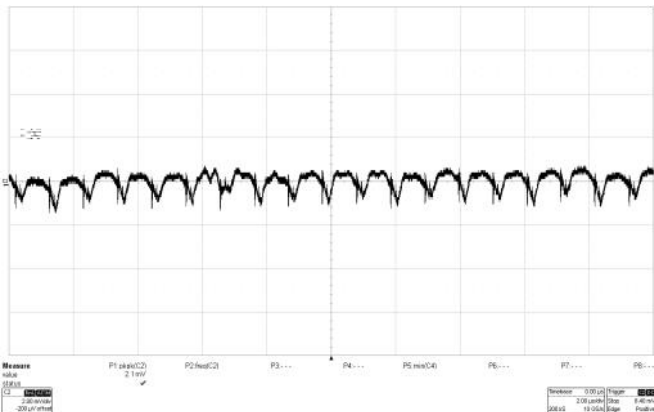
Available load current vs. ambient air temperature and air flow at $V_i = 12V$.

Output Current Derating-cold wall



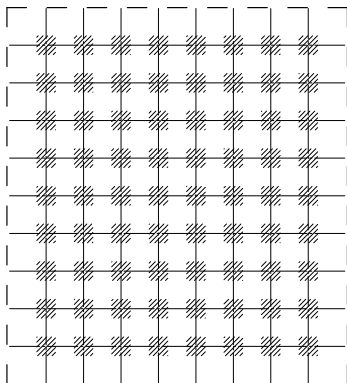
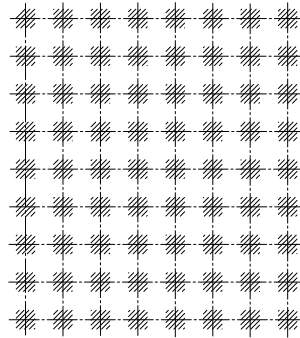
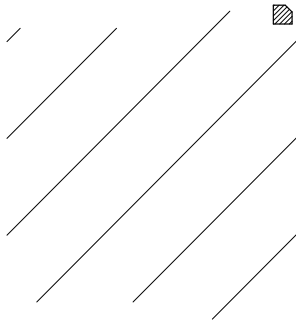
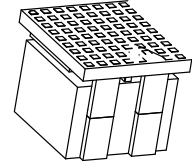
Thermal interface gap pad 1 mm, 8 W/mK. $V_i = 12V$.

Output ripple

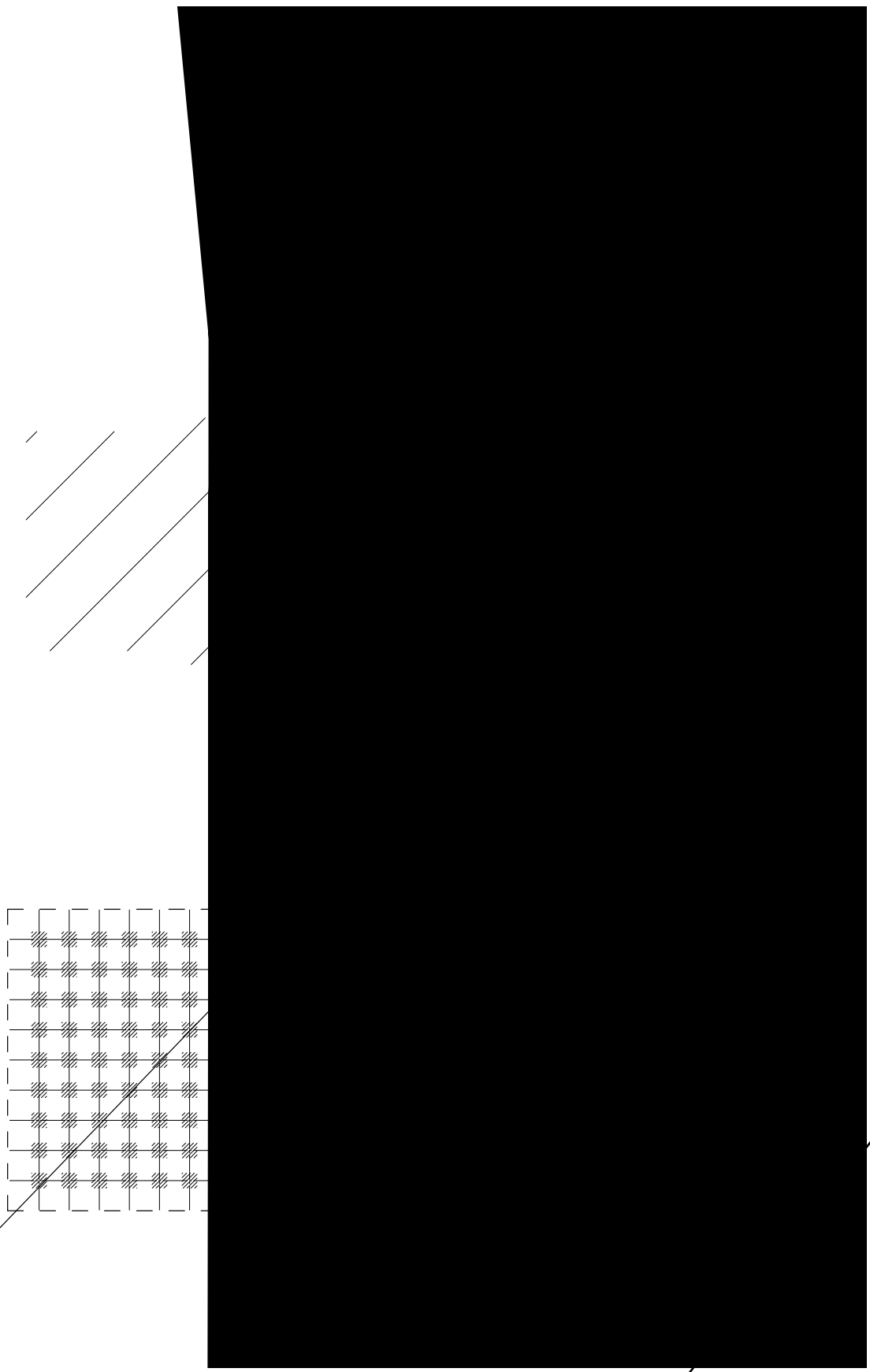


$V_i = 12 V$, $I_o = 80 A$, Scale: 2 mV/div, 2 μ s/div, 20 MHz BW. $C_{OUT} = 10 \times 470 \mu F/3m$ POSCAP + 365 x 10 μ F ceramic.

Part 2: Mechanical information
BMR511: Surface Mount Version



BMR511: Surface Mount Version with solder bumps



Part 3: Thermal considerations

Thermal Consideration

General

The product is designed with inductor on top. Cooling is mainly achieved by conduction to the host board and convection which is dependent on the airflow across the product. Increased airflow enhances the cooling of the product.

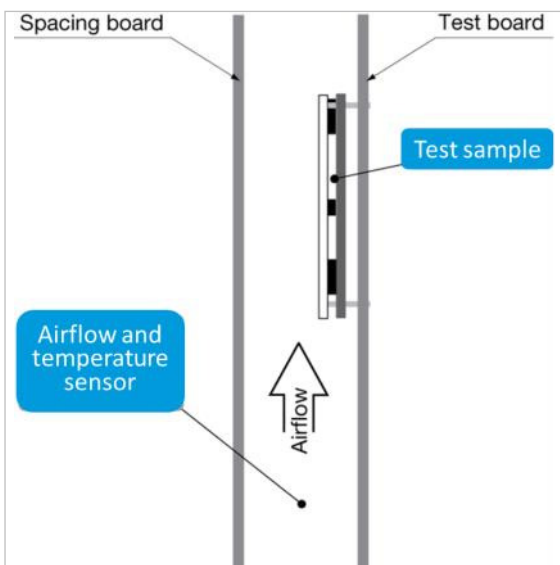
The Output Current Derating graph found in the Electrical Specification section provides the available output current versus Max. ambient temperature, and output current versus cold plate temperature and host board temperature.

Test Setup - Wind tunnel

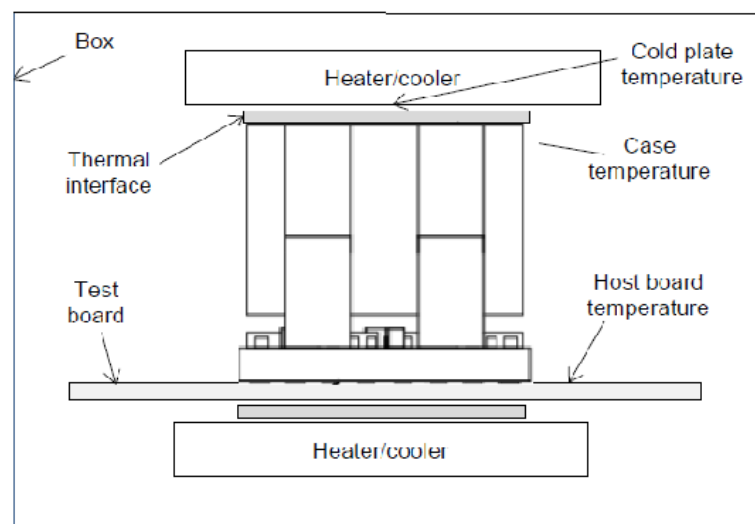
The product is tested on a 254 x 254 mm, 35 μm (1 oz), 8 layer test board mounted vertically in a wind tunnel with a cross section of 608 x 203 mm. Distances between the tested device and the top space board are 12 mm.

Test Setup – Cold Plate

The product is tested in a box with two heater/coolers; one as a cold plate to control the temperature at the top of the module, another on the bottom side of the test board to control the host board temperature. The test board used is 254 x 254 mm in size with 1.6 mm thickness and 8 layers of 1 oz.



Picture: test set-up - wind tunnel



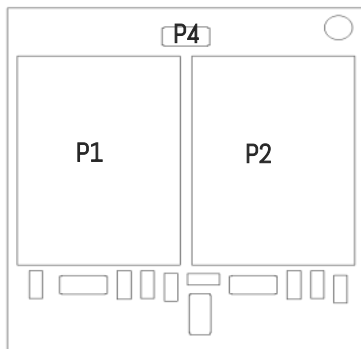
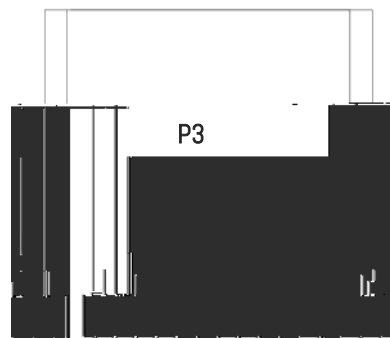
Picture: test setup — Cold plate

Definition of Product Operating Temperature

The temperature at positions P1-P4 should not exceed the maximum temperatures in the table below. The number of measurement points may vary with different thermal design and topology. Temperature above specified maximum measured at the specified position is not allowed and may cause permanent damage.

Part 3: Thermal considerations

Position	Description	Max temperature
P1	Power switch case phase 1 reference point	$T_{P1}=125^{\circ}\text{C}$
P2	Power switch case phase 2	$T_{P2}=125^{\circ}\text{C}$
P3	M1, Inductor core	$T_{P3}=125^{\circ}\text{C}$
P4	capacitors	$T_{P4}=105^{\circ}\text{C}$

Top view (inductor re-
moved)

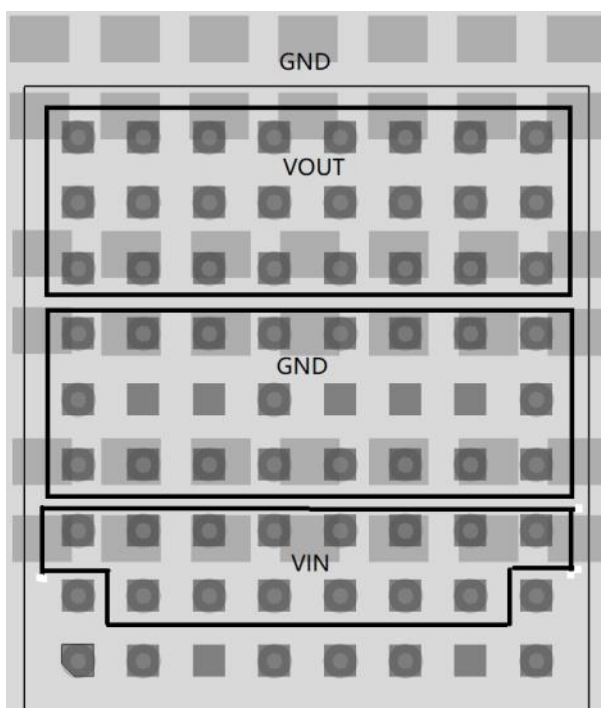
Side view

Part 4: PCB layout considerations

PCB Layout Consideration

1. The radiated EMI performance of the product will depend on the PCB layout and ground layer design. A ground plane shall be used, to increase the stray capacitance in the PCB and improve the high frequency EMC performance. The ground plane shall connect to the GND pins of the devices and the equipment ground or chassis.
2. For a multiphase rail including several power modules, layout should be as symmetrical as possible in order to help balance the current between devices.
3. If possible, use planes on several layers to carry VI, VO and ground. There should be a large number of vias close to the VIN, VOUT and GND pins to lower input and output impedances and improve heat spreading between the product and the host board.
4. Care should be taken in the routing of the ISEN and TSEN connections. The routing should be along a GND plane and should avoid areas of switching signals or high electric or magnetic fields, e.g. keep away from PWM signals.
5. The external input capacitors, CI_EXT, shall be placed as close to the input pins as possible and with low **impedance connections e.g. using via stitching around capacitors' terminals See AN323 for more details**
6. The external output capacitors, CO_EXT, should be placed close to the output pins to handle the output current ripple, and close to the load to handle the load transients. See AN321 for more details. **Low impedance connections must be used, e.g. via stitching around capacitors' terminals**

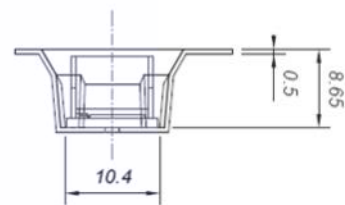
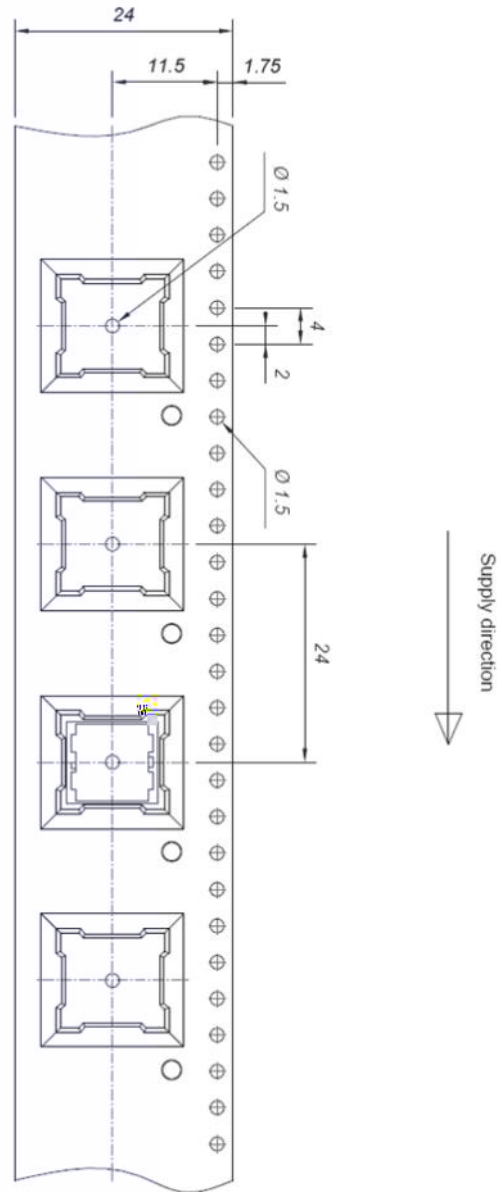
Below picture shows a layout example where the module is mounted on the top side of the PCB and 7 pcs 0603 input capacitors and 14 pcs 0603 output capacitors are placed on the bottom side of the PCB, providing a short connection to the VIN, VOUT and GND pins through vias in module pads.



Layout example: Input and output capacitance placement close to the module (top view).

Part 5: Packaging
Packaging information

C option:	
Material	Antistatic polyphenylene ester (PPE)
Surface resistance	$10^5 < \text{ohm/square} < 10^{11}$
Bakeability	The tape is not bakeable
Tape width, W	24 mm [0.95 inch]
Pocket pitch, P₁	24 mm [0.95 inch]
Pocket depth, K₀	8.65 mm [0.34 inch]
Reel diameter	330 mm [13.0 inch]
Reel capacity	300 products /reel
Reel weight	1400 g/full reel



Part 6: Revision history

Revision table

Revision number	revision change	date	revisor
Rev. A	New document	2023/6/15	JIDDIYAN
Rev. B	Spell check	2023/6/15	KARTWAER

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TECHNICAL REFERENCE DOCUMENT: GENERAL INFORMATION

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the *RoHS directive 2011/65/EU* and *2015/863* have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB, PBDE, DEHP, BBP, DBP, DIBP and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Flex Power Modules products are found in the [Statement of Compliance document](#).

Flex Power Modules fulfills and will continuously fulfill all its obligations under regulation (EC) No 1907/2006 concerning the registration, evaluation, authorization and restriction of chemicals ([REACH](#)) as they enter into force and is through product materials declarations preparing for the obligations to communicate information on substances in the products.

Quality statement

The products are designed and manufactured in an industrial environment where quality systems and methods like [ISO 9001](#), [ISO 14001](#), [ISO 45001](#), *Six Sigma*, and *SPC* are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged workforce, contribute to the high quality of the products.

Warranty

Warranty period and conditions are defined in *Flex Power Modules' General Terms and Conditions of Sales*.

Limitation of Liability

Flex Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

Product qualification specifications

Characteristics

External visual inspection	IPC-A-610		
Temperature shock test (Temperature cycling)	IEC 60068-2-14 Na	Temperature range	-40 to 125°C
		Number of cycles	700
		Dwell/transfer time	15 min/0-1 min
Cold (in operation)	IEC 60068-2-1 Ad	Temperature T _A	-45°C
		Duration	72 hr
Damp heat	IEC 60068-2-67 Cy	Temperature	85°C
		Humidity	85% RH
		Duration	1000 hr
Dry heat	IEC 60068-2-2 Bd	Temperature	125°C
		Duration	1000 hr
Electrostatic discharge sus- ceptibility	IEC 61340-3-1, JESD 22-A114	Human body model (HBM)	Class 2, 2000 V
	IEC 61340-3-2, JESD 22-A115	Machine Model (MM)	Class 3, 200 V
Immersion in cleaning sol- vents	IEC 60068-2-45 XA, method 2	Water	55°C
		Fluxcleaner	23° C
		Isopropyl alcohol	35°C
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration	100 g
		Duration	6 ms
Moisture classification and reflow sensitivity ¹	J-STD-020E	Level 3 (Pb Free)	245°C
Operational Life test Rapid Temp.	MIL-STD-202G, method 108A	Duration	1000 hr
Robustness of terminations	IEC 60068-2-21 Test Ue1	Surface mount products	All leads
Solderability (surface mount)	IEC 60068-2-58 test T _d	Preconditioning	Steam ageing
		Temperature, Pb-free	245°C
Vibration, broad band ran- dom	IEC 60068-2-64 Fh, method 1	Frequency	

TECHNICAL REFERENCE DOCUMENT: DESIGN & APPLICATION GUIDELINES

Operating Information

BMR 511 is a two-phase non-isolated high-efficiency small footprint step-down power block. It includes drivers, MOSFETs, output inductors and sensing circuitry for output currents and junction temperatures. Further, it includes over-current and over-temperature protection functions with fault reporting. The product has two independent outputs, which can be used separately (dual rail) or in parallel (single rail dual phase). Several products can be paralleled for multiphase operation.

Input and Output Impedance

The impedance of both the input source and the load will interact with the impedance of the product. It is important that the input source has low characteristic impedance. If the input voltage source contains significant inductance, the addition of a capacitor with low ESR at the input of the product will ensure stable operation.

External Input Capacitors

For most applications non-tantalum capacitors are preferred due to the robustness of such capacitors to accommodate high inrush currents of systems being powered from very low impedance sources. It is recommended to use a combination of ceramic capacitors and low-ESR electrolytic/polymer bulk capacitors. The low ESR of ceramic capacitors effectively limits the input ripple voltage level, while the bulk capacitance minimizes deviations in the input voltage during large load transients.

It is recommended to use at least 70 μF ceramic external input capacitors for each module, placed closed to input pins and with low impedance connections to the VIN and GND pins to be effective. See application note AN323 for further guidelines on how to choose and apply input capacitors.

External Output Capacitors

The output capacitor requirement depends on two considerations: output ripple voltage and load transient response. To achieve low ripple voltage, the output capacitor bank must have a low ESR value, which is achieved with ceramic output capacitors. A low ESR value is critical also for a small output voltage deviation during load transients. Designs with smaller load transients can use fewer capacitors and designs with more dynamic load content will require more load capacitors to minimize output voltage deviation.

It is recommended to place at least 140 μF ceramic capacitors close to each product, since it has no internal output capacitance. In addition, low ESR ceramic and low ESR electrolytic/polymer capacitors shall be placed as close to the load as possible, using several capacitors in parallel to lower the effective ESR. It is important to use low resistance and low inductance PCB layouts for capacitance to be effective.

See application note AN321 for further guidelines on how to choose and apply output capacitors.

TECHNICAL REFERENCE DOCUMENT: DESIGN & APPLICATION GUIDELINES

Enable

When the voltages at VIN and VCC pins are sufficiently high and the EN pin is high, operation begins. There are separated EN pins for each phase.

Pulse-Width Modulation (PWM)

The pulse-width modulation input pins are capable of tri-state input. When the PWM input is high, the high-side MOSFET is turned on and the low-side MOSFET is turned off. When the PWM input is low, the high-side MOSFET is turned off and the low-side MOSFET is turned on. If the PWM input is floated, it will force the PWM into tri-state.

When the PWM input signal is within the tri-state threshold window for a typical 40 ns, the high-side MOSFET turns off immediately, and the low-side MOSFET enters diode emulation mode, which is on until zero-current detection.

The tri-state PWM input can come from a forced mid-voltage PWM signal or made by floating the PWM input. The internal current source charges the signal to a middle voltage.

When operating both phases of the product, PWM1 and PWM2 pulses shall be 180° shifted.

Diode Emulation Mode

In diode emulation mode, when PWM is in a tri-state input, the low side MOSFET is turned on whenever the inductor current is positive. The low side MOSFET turns off if the inductor current is negative or after the inductor current crosses the zero current.

Current Sense

The ISEN pins are bidirectional current source outputs proportional to the inductor current of each phase. The gain is 5 $\mu\text{A}/\text{A}$. An external resistor must be used to program the voltage gain proportional to the inductor current, if needed.

The ISEN outputs have two states. In disable mode (EN = low), the current sense circuit is disabled and the ISEN outputs are in the high impedance state.

EN	ISEN
High	Active
Low	Hi-Z

The voltage of the ISEN pins must be within the specified voltage range to achieve an accurate current reporting. In general, there is a resistor, RIOUT, connected from ISEN to a reference voltage, VCM, that can sink small currents to provide enough voltage level to meet the required operating voltage range. Proper VCM and RIOUT values can be determined by the equation:

$$0.7 \text{ V} < (I_{\text{ISEN}} \times R_{\text{IOUT}}) + V_{\text{CM}} < 2.1 \text{ V}$$

TECHNICAL REFERENCE DOCUMENT: DESIGN & APPLICATION GUIDELINES

The current sense outputs can be used by the controller to monitor the output current accurately. The cycle-by-cycle current information from the ISEN pins can be used for phase-current balancing, over-current protection, and voltage positioning (output voltage droop).

Current Limit

When high side over-current is detected, the high side MOSFET turns off for that PWM cycle. If there are ten consecutive cycles of a high side current limit event, the high side MOSFET latches off, TSEN pulls high to VCC, and the low side MOSFET turns on until zero current detection. Toggle EN or recycle VIN or VCC to release the latch and restart operation.

When the low side under-current is detected (negative current) the low side MOSFET is turned off and the high side MOSFET is turned on for 200 ns to limit the negative current cycle by cycle. The low side negative current limit will not trigger a fault report.

Temperature Sense with Fault Indicator

The TSEN pins have two functions:

1. Junction temperature sense.
2. Fault detection.

The voltage at the TSEN pin is proportional to the junction temperature whenever VIN and VCC is higher than their UVLO thresholds. The gain is 8 mV/°C and has an 800 mV offset at 25 °C. Examples:

$$V_{TSEN} = 0.8 \text{ V @ } T_J = 25 \text{ °C}$$

$$V_{TSEN} = 1.6 \text{ V @ } T_J = 125 \text{ °C}$$

When any fault occurs, TSEN is pulled to the VCC voltage to report the fault event, regardless of the current junction temperature. 200 ns after the fault has occurred, the PWM impedance changes accordingly to indicate the fault type:

Fault Type	PWM Impedance
Over-current limit protection	10 kΩ to GND
Over-temperature limit protection	20 kΩ to GND
Internal short protection	1 kΩ to VCC

For multiphase operation, connect TSEN of each phase together to report the highest junction temperature. It is recommended to add a filter to the TSEN signal. See Application Example for typical component values to use.

Temperature Limit

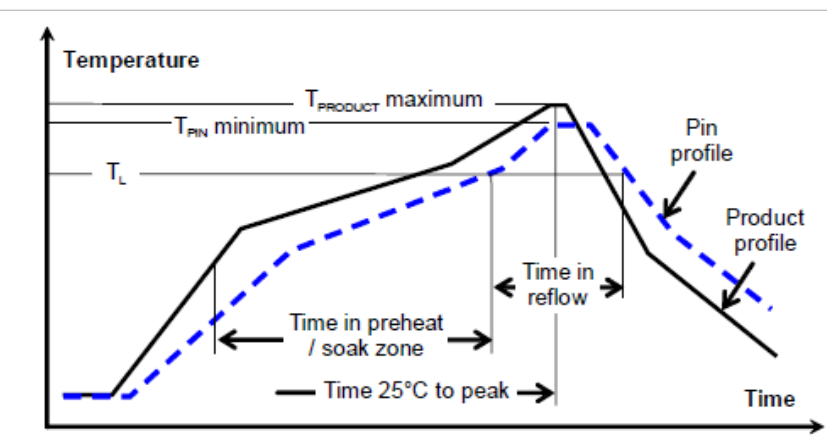
When the over-temperature threshold is exceeded, the high side MOSFET latches off, TSEN pulls high to VCC, and the low side MOSFET turns on until zero current detection. Toggle EN or recycle VIN or VCC to release the latch and restart operation.

TECHNICAL REFERENCE DOCUMENT: SOLDERING

Soldering Information — Surface mounting

is measured on the power module's hotspot

General reflow process specification		Pb-free, SAC305
		-



Typical soldering profile

Pb-free solder classification

Products reflow processes - Lead-free (Pb-free) solder processes

Dry Pack Information

Surface Mount Assembly and Repair

Assembly

not

Repair

TECHNICAL REFERENCE DOCUMENT: SAFETY

Safety specifications

Flex Power Modules' DC/DC converters and DC/DC regulators are designed in accordance with the safety standards *IEC 62368-1*, *EN 62368-1* and *UL 62368-1 Audio/video, information and communication technology equipment - Part 1: Safety requirements*

IEC/EN/UL 62368-1 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Electrically-caused fire
- Injury caused by hazardous substances
- Mechanically-caused injury
- Skin burn
- Radiation-caused injury

On-board DC/DC converters, Power Interface Modules and DC/DC regulators are defined as component power supplies. As components they cannot fully comply with the provisions of any safety requirements without "conditions of acceptability". Clearance between conductors and between conductive parts of the component power supply and conductors on the board in the final product must meet the applicable safety requirements. Certain conditions of acceptability apply for component power supplies with limited stand-off (please refer to *Technical Specification under Mechanical Information* for further information). It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable safety standards and regulations for the final product.

Component power supplies for general use shall comply with the requirements in *IEC/EN/UL 62368-1*. Product related standards, e.g. *IEEE 802.3af Power over Ethernet*, and *ETS-300132-2 Power interface at the input to telecom equipment, operated by direct current (dc)* are based on *IEC/EN/UL 62368-1* with regards to safety.

All Flex Power Modules' DC/DC converters, Power Interface Modules and DC/DC regulators are recognized and certified in accordance with *IEC/EN/UL 62368-1*. The flammability rating for all construction parts of the products meet requirements for V-0 class material according to *IEC 62368-1 10 Fire hazard testing, test flames – 50 W horizontal and vertical flame test methods*.

Non-isolated DC/DC regulators

The DC/DC regulator output is ES1 energy source if the input source meets the requirements for ES1 according to *IEC/EN/UL 62368-1*.