



# DESCRIPTION THERMAL MODEL FOR BMR 463 SERIES



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## General

The model is an estimation for the thermal behavior of BMR 463 2002, which is a SIP design.

The model is intended for steady-state thermal simulations.

## Model Description

The model is a readymade Flotherm 11.1 model. The model consists of four major components:

### 3D CAD Geometry

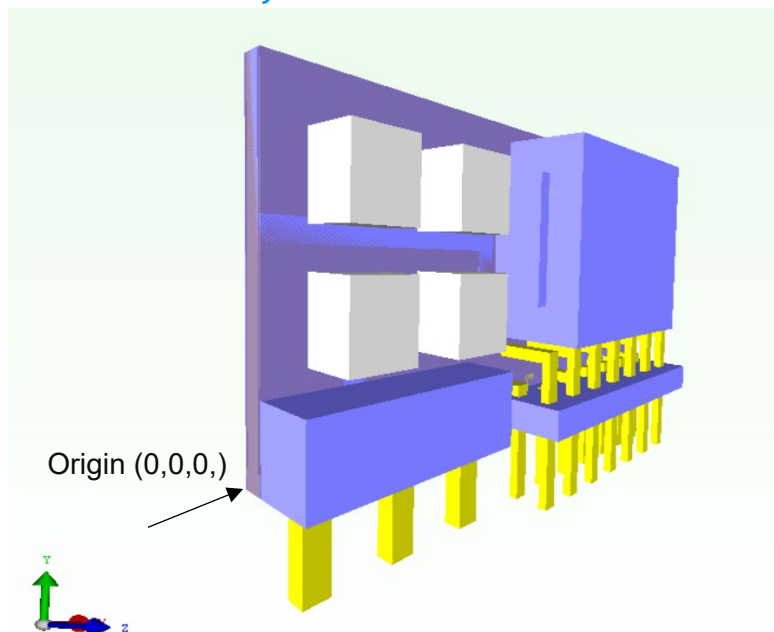


Figure 1 Model origin in lower left corner of PCB and axis orientation.

Origin has been placed so that [0,0,0] is in the lower left corner of the PCB.

Unit in file: [mm]

In the geometry most components are maintained per original design but have been simplified in FloMCAD to cuboids. The PCB has been simplified into 5 layers: Outer layer copper (2), dielectric layer between outer and inner layers (2), and finally a middle bulk layer. The vias in the PCB has been imported through FloEDA and are used in the dielectric layers. In order to capture the plated through holes, cuboids of high conducting have been placed in the middle bulk section.

The model comes with a rudimentary grid constraint, which was used during the model development. It generates approx. 590 000 cells. It is of course voluntarily to use this grid.

## Domains of power loss distribution

There are several sources for power loss. The power loss for each of them, at certain module total power, are given in *Appendix 1 - Power Loss Distribution*.

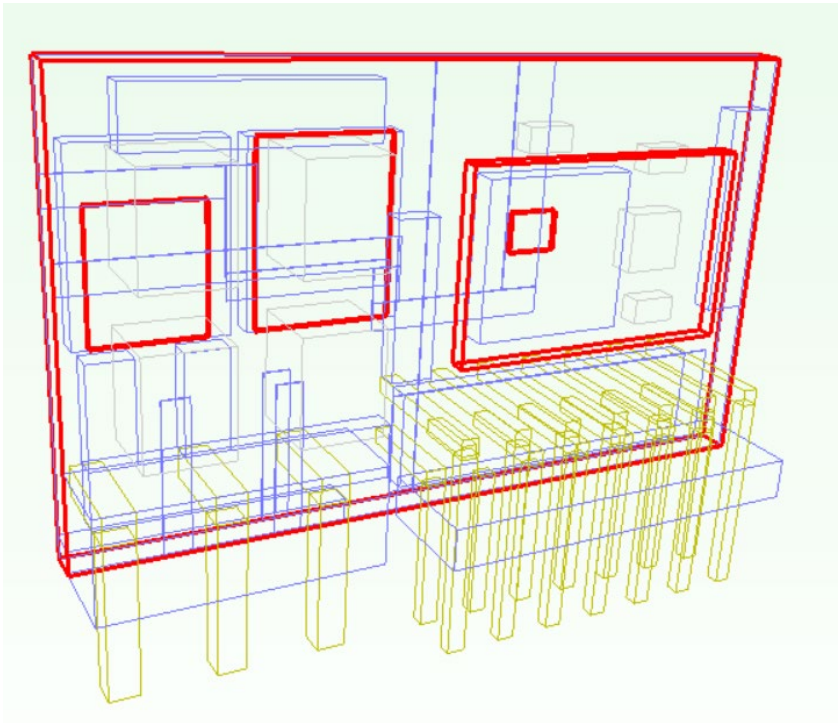
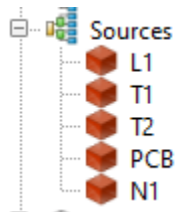


Figure 2: Heat Sources

## Domains of material data

There are several material domains. The heat conductivity for each of them is given either as isotropic, or anisotropic values in x-, y-, and z-direction (x,y,z) per the following list.

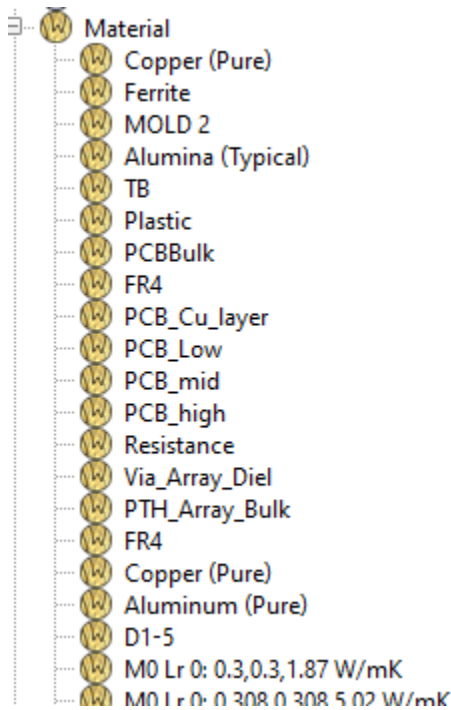


Figure 3. Domains of material data

**Note.** The given heat conductivities are only intended to model the temperature distribution of the module in this application. The values should not be treated as physically true or transferable to other applications.

## Monitor points

The model comes with predefined monitor points:

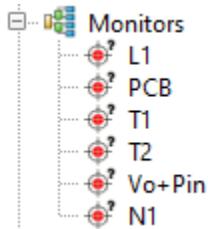


Figure 4. Probe points.

## Model Calibration

The model has been calibrated to give temperatures as similar as possible compared to thermal verification document 102 65-BMR 463 2002 Rev C for  $V_{in}=12[V]$ ,  $V_{out}=1[V]$ ,  $I_{in}=2[A]$ ,  $I_{out}=20[A]$ ,  $3 [m/s]$ . The calibration was done using power loss settings per Appendix 1 - Power Loss Distribution.

Calibration data: (air direction=neg x),  $T_{amb}=26.3[C]$ , spacing 25[mm], board 254x254x3 [mm<sup>3</sup>], board thermal conductivity  $k=(56,2,56)[W/(mK)]$ .

Simulation temperatures are within  $\pm 2.3$  [C] compared to measured values.

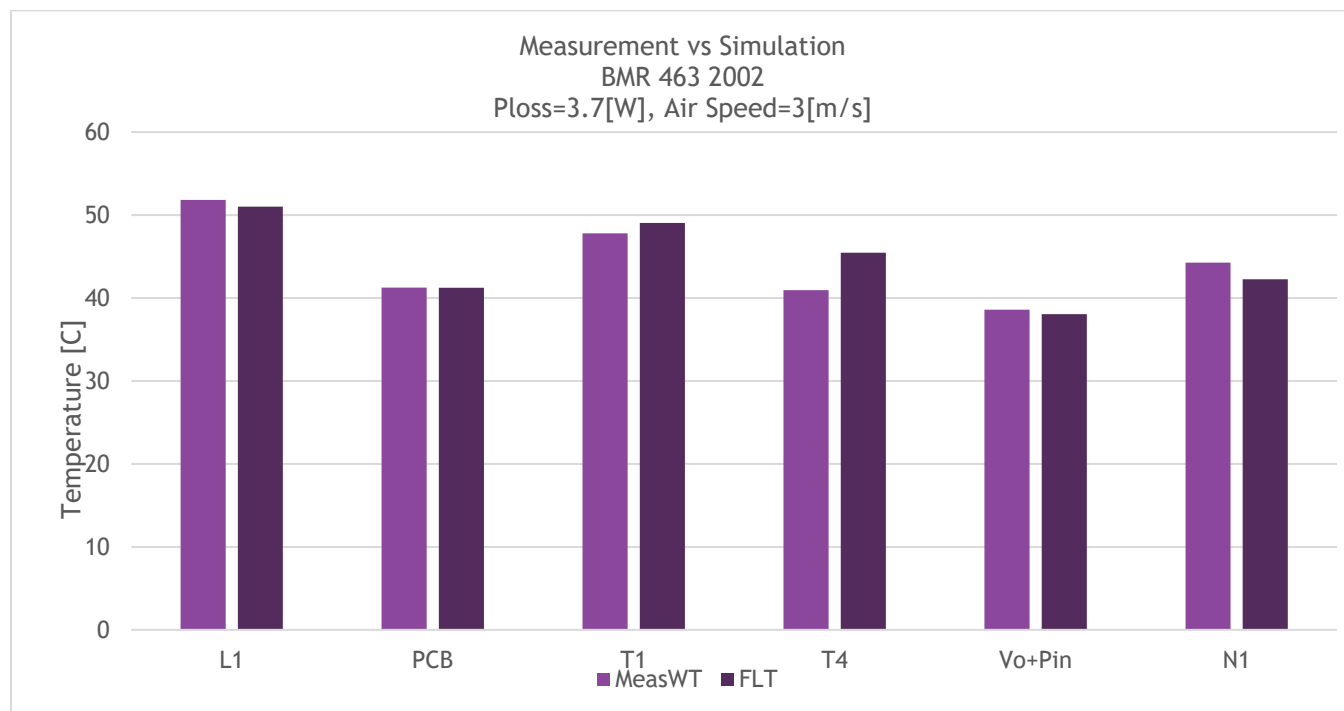


Figure 5: Model calibration result.



## Model Usage

Import the \*.pdml file into the desired project.

Adjust the dissipated power by altering the thermal sources per Figure 2, according to Appendix 1 - Power Loss Distribution. Default settings are for  $V_{in}=12[V]$ ,  $V_{out}=1[V]$ ,  $I_{in}=2[A]$ ,  $I_{out}=20[A]$

If the model is rotated, make sure that the orientation of the orthotropic materials properties are preserved (also rotated).

Do not change the order of power sources and geometry objects, as this can change the power and material settings.

The module temperatures can be monitored in predefined monitor points.

## Additional Information

Model has been constructed with SI units.

### Reference

BMR4632002A.pdml

### Disclaimer

The model and model documentation described herein are provided for the sole purpose of facilitating thermal modeling of a structure where the referenced product is included. It should not and cannot be interpreted neither as a detailed description of the product itself, nor as a statement of the product's performance.

The model has been constructed on a best effort basis, but we cannot accept liability for any discrepancy between model predictions and actual values.

### Revision history

A	2020-01-15	New document
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## Appendix 1 - Power Loss Distribution

Power loss distribution examples for BMR 463 2002.

Condition:  $V_{in}=12[V]$ ,  $V_{out}=1[V]$ ,  $I_{in}=2[A]$ ,  $I_{out}=20[A]$ ,

Domain	Number of domains	Power loss per domain [W]	Power loss per volume [mW/mm <sup>3</sup> ]	Subtotal [W]
T1	1	1.407	-	1.407
T2	1	0.842	-	0.842
L1	1	0.902	-	0.902
N1	1	0.05	-	0.05
PCB	1	0.568	-	0.568
Total [W]				3.77